

PRODUCT SPECIFICATION

9.0" TN TFT LCD MODULE

MODEL: T090102105-A0TMN-001 Ver:1.4

ROHS

< ◇ > Preliminary Specification

< ◆ > Finally Specification

CUSTOMER'S APPROVAL	
CUSTOMER :	
SIGNATURE:	DATE:

APPROVED BY	PM REVIEWED	PD REVIEWED	PREPARED BY

Revision History

Revision	Date	Originator	Detail	Remarks
1.0	2014.08.08		Initial Release	
1.1	2015.07.08	TQ	Add Weight Add Current Consumption All Black Modify Chromaticity Transmissive Modify Outline Drawing	P4 P5 P6 P23
1.2	2017.03.23	ZFY	Modify luminance	P6
1.3	2017.03.24	ZFY	Modify luminance Modify outline drawing (C)	P6 P23
1.4	2018.04.23	ZFY	Add LED working life Modify many details	P5 P20/P21

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1. General Description

The specification is a transmissive type color active matrix liquid crystal display (LCD) which uses amorphous thin film transistor (TFT) as switching devices. This product is composed of a TFT-LCD panel, driver Ics and a backlight unit.

2. Module Parameter

Features	Details	Unit
Display Size(Diagonal)	9.0"	
LCD type	TN TFT	
Display Mode	Transmissive /normally white	
Resolution	1024 x(RGB) x 105	Pixels
View Direction	12 O'CLOCK	Best Image
Gray scale inversion direction	6 O'CLOCK	
Module Outline	240.08 (H) x 37.8 (V) x 4.3 (T) (Note1)	mm
Active Area	230.4 (H) x23.62 (V)	mm
Pixel Size	225 (H)x 225 (V)	um
Pixel Arrangement	R.G.B. Vertical Stripe	
Polarizer Surface Treatment	Anti-Glare	
Display Colors	16.7M	
Interface	24 bit RGB Interface	
Driver IC	ILI6150 & ILI5120	
With or Without Touch Panel	Without	
Operating Temperature	-20~70	°C
Storage Temperature	-30~80	°C
Weight	80	g

Note 1: Exclusive hooks, posts, FFC/FPC tail, PCB etc.

3. Absolute Maximum Ratings

V_{SS}=0V, Ta=25°C

Item	Symbol	Min.	Max.	Unit
Supply Voltage	VDD	-0.5	5.0	V
Storage temperature	T _{stg}	-30	+80	°C
Operating temperature	T _{op}	-20	+70	°C

Note 1: If Ta below 50°C, the maximal humidity is 90%RH, if Ta over 50°C, absolute humidity should be less than 60%RH.

Note 2: The response time will be extremely slow when the operating temperature is around -10°C, and the back ground will become darker at high temperature operating.

4. DC Characteristics

Item	Symbol	Min.	Typ.	Max.	Unit
Supply Voltage	VDD	2.3	3.3	3.6	V
	AVDD	-	(12)	-	V
	VGH	-	(18)	-	V
	VGL	-	(-7)	-	V
	VCOM	-	(6.0)	-	V
Logic Low input voltage	V_{IL}	0	-	0.3*VDD	V
Logic High input voltage	V_{IH}	0.7*VDD	-	VDD	V
Logic Low output voltage	V_{OL}	-	-	0GND+0.4	V
Logic High output voltage	V_{OH}	VDD-0.4	-	-	V
Current Consumption All Black	$I_{CC+ IIN}$	-	280	-	mA

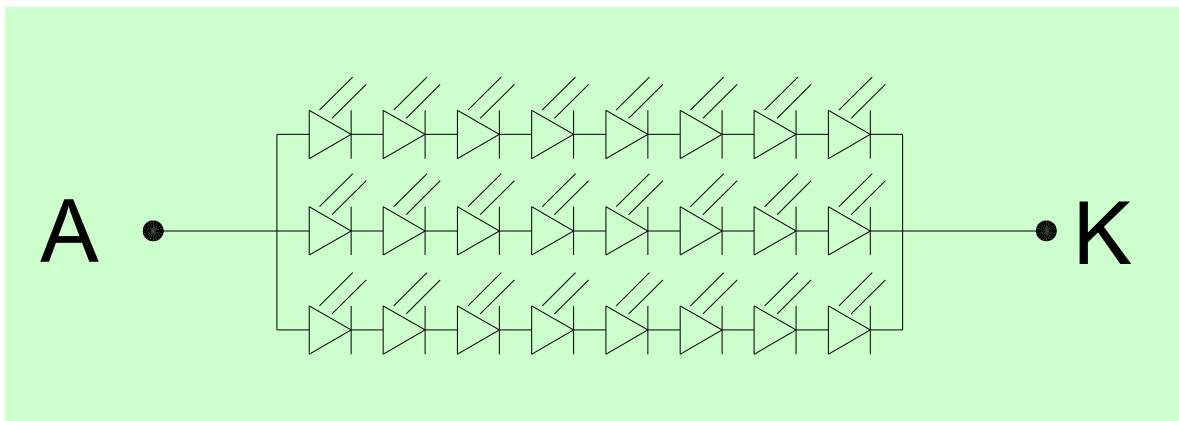
5. Backlight Characteristic

5.1. Backlight Characteristic

Item	Symbol	Condition	Min.	Typ.	Max.	Unit
Forward Voltage	V_F	Ta=25 °C, $I_F=20\text{mA}/\text{LED}$	23.2	25.6	26.4	V
Forward Current	I_F	Ta=25 °C, $V_F=3.2\text{V}/\text{LED}$	-	60	-	mA
Power dissipation	P_D		-	1536	-	mW
Uniformity	Avg		80	-	-	%
LED working life(25°C)	-		-	20,000	-	Hrs
Drive method	Constant current					
LED Configuration	24 White LEDs(8 LEDs in one string and 3 groups in parallel)					

Note1: LED life time defined as follows: The final brightness is at 50% of original brightness.
The environmental conducted under ambient air flow, at Ta=25±2 °C,60%RH±5%, $I_F=20\text{mA}$

5.2. Backlight Characteristic



6. Optical Characteristics

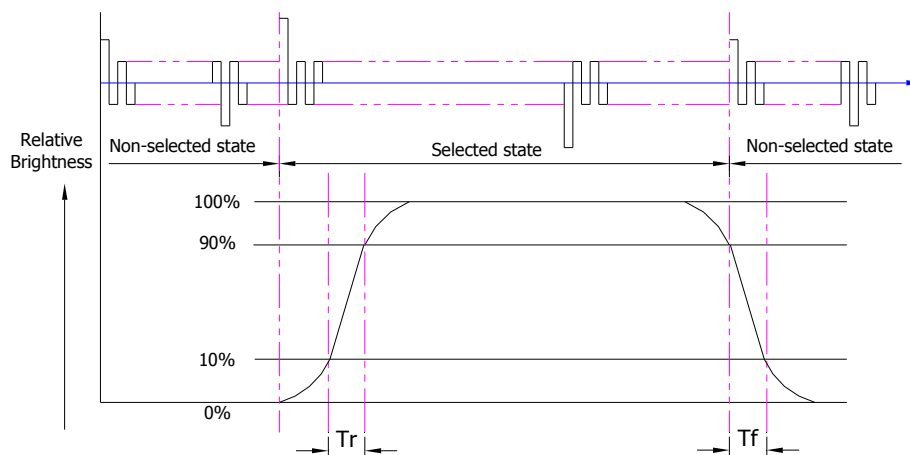
6.1. Optical Characteristics

Ta=25°C, VCC=3.3V

	Item	Symbol	Condition	Specification			Unit	
				Min.	Typ.	Max.		
Backlight On (Transmissive Mode)	Luminance on TFT	Lv	Normally viewing angle $\theta_x = \phi_y = 0^\circ$	760	950	-	cd/m ²	
	Contrast ratio(See 6.3)	CR		300	400	-		
	Response time (See 6.2)	TR+TF		-	25	50	ms	
	Chromaticity Transmissive (See 6.5)	Red	X _R	Center CR≥10	0.553	0.603	0.653	
			Y _R		0.297	0.347	0.397	
		Green	X _G		0.258	0.308	0.358	
			Y _G		0.599	0.649	0.699	
		Blue	X _B		0.095	0.145	0.195	
			Y _B		0.036	0.086	0.136	
	White	X _W	0.237	0.287	0.337			
Y _W		0.301	0.351	0.401				
Viewing Angle (See 6.4)	Horizontal	θ_{x+}	Center CR≥10	35	45	-	Deg.	
		θ_{x-}		35	45	-		
	Vertical	ϕ_{y+}		15	25	-		
		ϕ_{y-}		35	45	-		
NTSC Ratio(Gamut)				-	58	-	%	

6.2. Definition of Response Time

6.2.1. Normally Black Type (Negative)

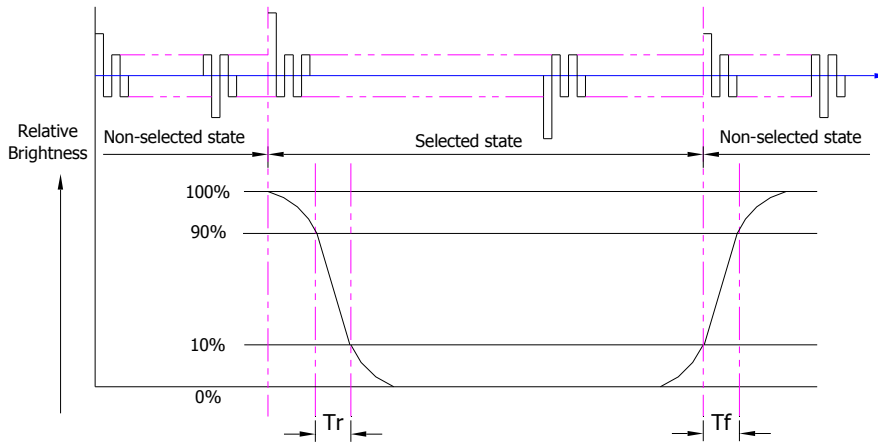


Tr is the time it takes to change from non-selected stage with relative luminance 10% to selected state with relative luminance 90%;

Tf is the time it takes to change from selected state with relative luminance 90% to non-selected state with relative luminance 10%.

Note: Measuring machine: LCD-5100

6.2.2. Normally White Type (Positive)



Tr is the time it takes to change from non-selected stage with relative luminance 90% to selected state with relative luminance 10%;

Tf is the time it takes to change from selected state with relative luminance 10% to non-selected state with relative luminance 90%;

Note: Measuring machine: LCD-5100 or EQUI

6.3. Definition of Contrast Ratio

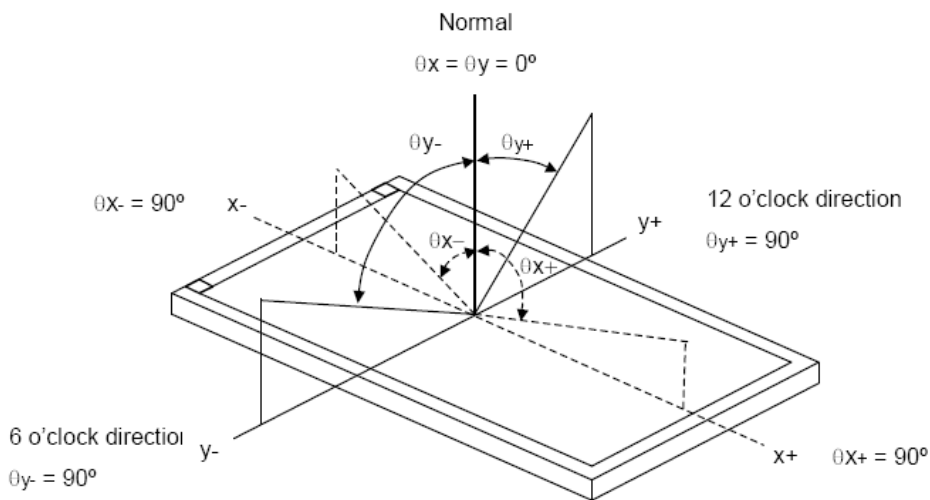
Contrast is measured perpendicular to display surface in reflective and transmissive mode.

The measurement condition is:

Measuring Equipment	Eldim or Equivalent
Measuring Point Diameter	3mm//1mm
Measuring Point Location	Active Area centre point
Test pattern	A: All Pixels white
	B: All Pixel black
Contrast setting	Maximum

Definitions: CR (Contrast) = Luminance of White Pixel / Luminance of Black Pixel

6.4. Definition of Viewing Angles



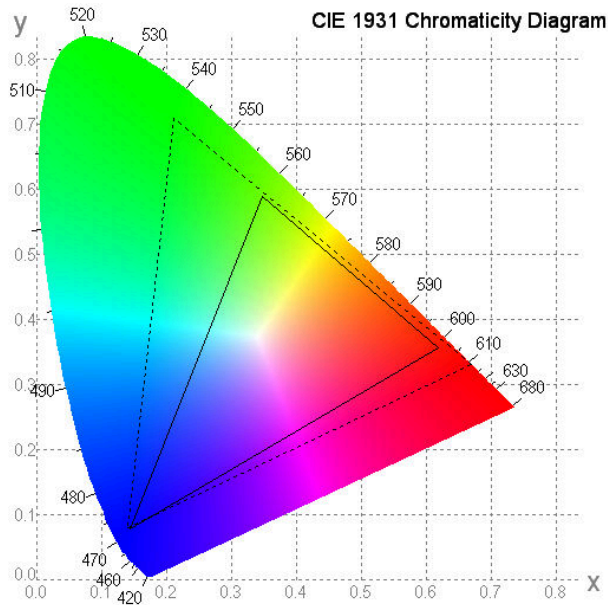
Measuring machine: LCD-5100 or EQUI

6.5. Definition of Color Appearance

R, G, B and W are defined by (x, y) on the IE chromaticity diagram

NTSC=area of RGB triangle/area of NTSC triangleX100%

Measuring picture: Red, Green, Blue and White (Measuring machine: BM-7)



6.6. Definition of Surface Luminance, Uniformity and Transmittance

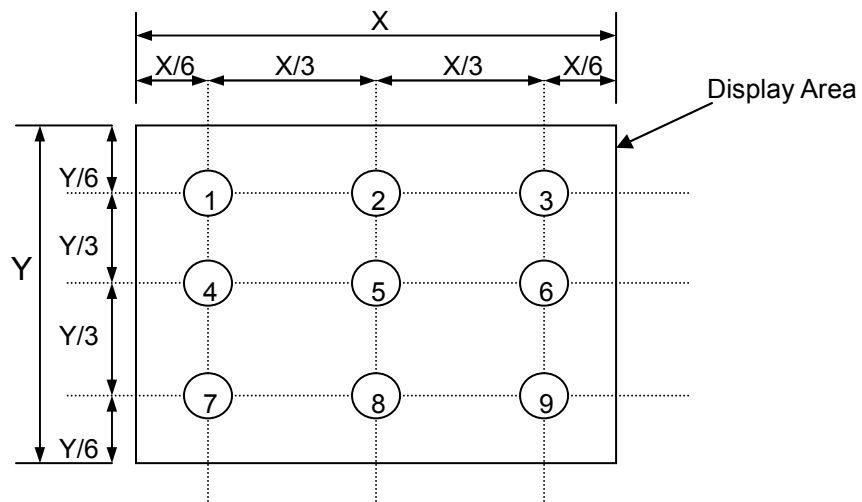
Using the transmissive mode measurement approach, measure the white screen luminance of the display panel and backlight.

6.6.1. Surface Luminance: $L_V = \text{average} (L_{P1}:L_{P9})$

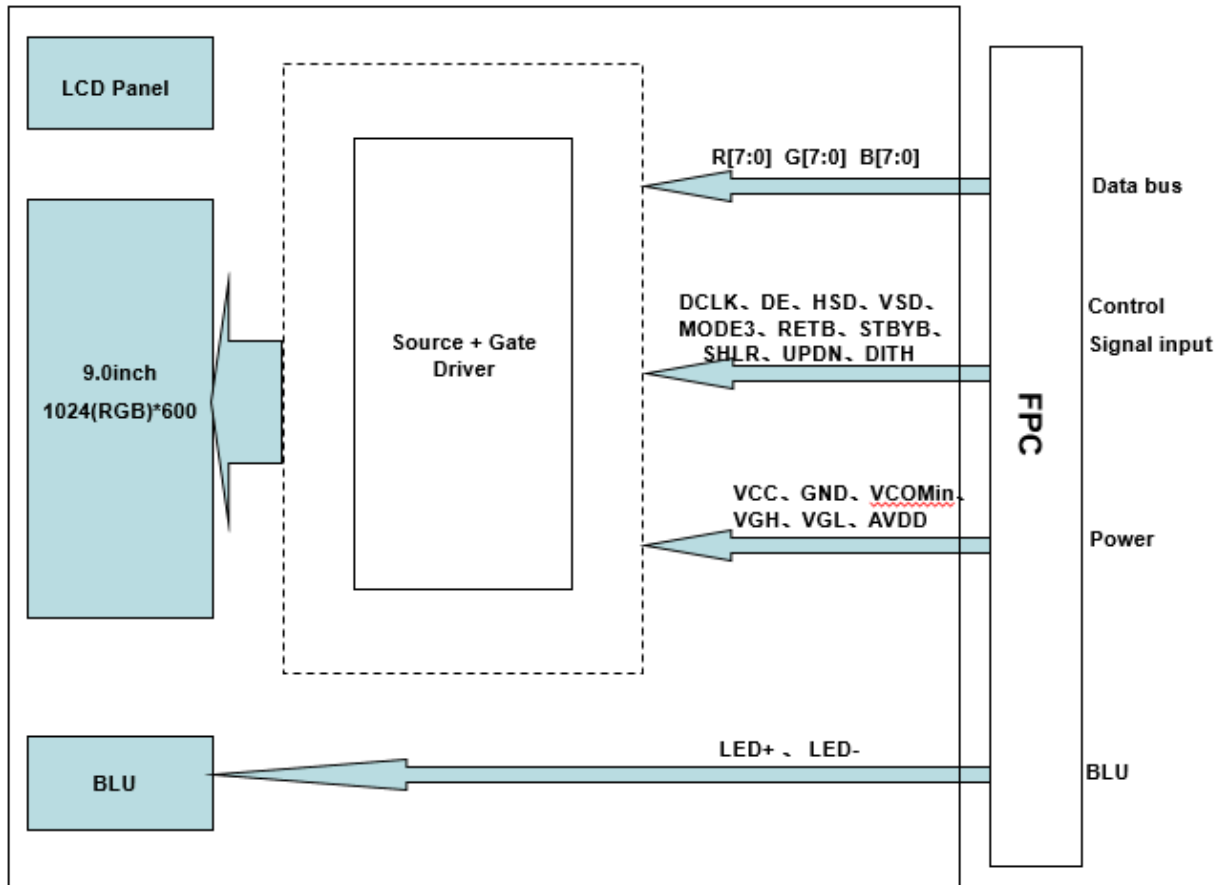
6.6.2. Uniformity = Minimal ($L_{P1}:L_{P9}$) / Maximal ($L_{P1}:L_{P9}$) * 100%

6.6.3. Transmittance = L_V on LCD / L_V on Backlight * 100%

Note: Measuring machine: BM-7



7. Block Diagram and Power Supply



8. Interface Pins Definition

No.	Symbol	Function	Remark
1	GND	Ground.	
2	AVDD	Analog Power.	
3	VCC	Digital Power.	
4	R0	Data bus.	
5	R1	Data bus.	
6	R2	Data bus.	
7	R3	Data bus.	
8	R4	Data bus.	
9	R5	Data bus.	
10	R6	Data bus.	
11	R7	Data bus.	
12	G0	Data bus.	
13	G1	Data bus.	
14	G2	Data bus.	
15	G3	Data bus.	
16	G4	Data bus.	
17	G5	Data bus.	
18	G6	Data bus.	
19	G7	Data bus.	
20	B0	Data bus.	
21	B1	Data bus.	
22	B2	Data bus.	
23	B3	Data bus.	
24	B4	Data bus.	
25	B5	Data bus.	
26	B6	Data bus.	
27	B7	Data bus.	
28	DCLK	Clock input.	
29	DE	Data enable signal.	
30	HSD	Horizontal sync input.	
31	VSD	Vertical sync input.	
32	MODE3	DE/SYNC mode select. Normally pull high. H:DE mode, L:HSD/VSD mode.	
33	RSTB	Reset signal.	
34	STBYB	Standby mode, normally pull high STBYB="1", normal operation STBYB="0", timing control, source driver will turn off, all output are high-Z.	
35	SHLR	Source right or left sequence control.	
36	VCC	Digital Power.	
37	UPDN	Gate up or down scan control.	
38	GND	Ground.	

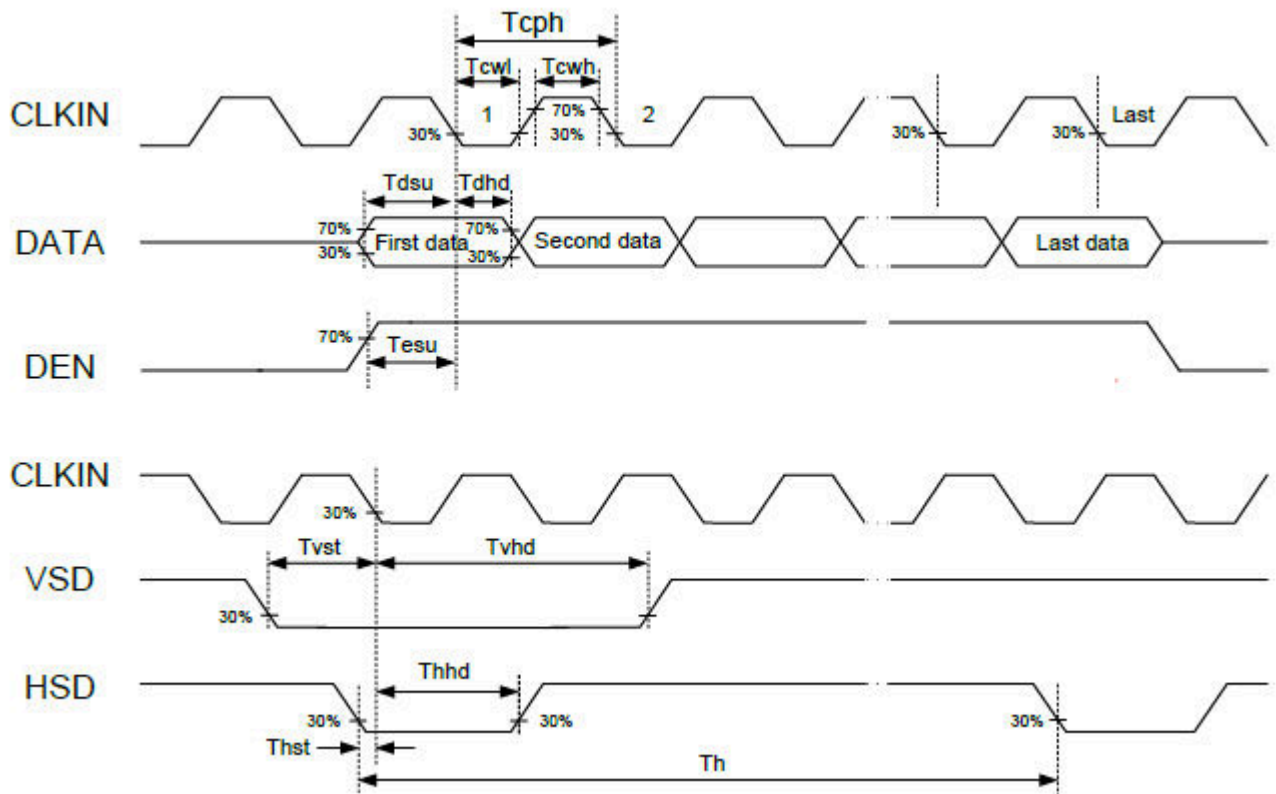
39	GND	Ground.	
40	AVDD	Analog Power.	
41	VCOMin	For external VCOM DC input(optical)	
42	DITH	Dithering setting: DITH="H" 6bit resolution (last 2 bits of input data truncated) (default setting). DITH="L" 8bit resolution.	
43	NC	No connection.	
44	NC	No connection.	
45	NC	No connection.	
46	NC	No connection.	
47	NC	No connection.	
48	NC	No connection.	
49	NC	No connection.	
50	NC	No connection.	
51	NC	No connection.	
52	NC	No connection.	
53	NC	No connection.	
54	NC	No connection.	
55	NC	No connection.	
56	VGH	Positive power for TFT.	
57	VCC	Digital Power.	
58	VGL	Negative power for TFT.	
59	GND	Ground.	
60	NC	No connection.	

9. Timing Diagram

9.1 AC Electrical Characteristics

Parameter	Symbol	Min	Typ.	Max	Unit	Conditions
VDD power on Slew rate	TPOR	-	-	20	ms	From 0V to 0.9VDD
RSTB pulse width	TRst	50	-	-	us	DCLK=65MHz
DCLK cycle time	Tcph	14			ns	
DCLK pulse duty	Tcwh	40	50	60	%	
VSD setup time	Tvst	5	-	-	ns	
VSD hold time	Tvhd	5	-	-	ns	
HSD setup time	Thst	5	-	-	ns	
HSD hold time	Thhd	5	-	-	ns	
Data setup time	Tdsu	5	-	-	ns	D0[7:0],D1[7:0],D2[7:0] to DCLK
Data hold time	Tdhu	5	-	-	ns	D0[7:0],D1[7:0],D2[7:0] to DCLK
OE setup time	Tesu	5	-	-	ns	
OE hold time	Tehd	5	-	-	ns	
Output stable time	Tsst	-	-	6	us	10% to 90% target Voltage. CL=90pF,R=10K ohm (Cascade) Dual gate
				3		

9.2 Input Clock and Data Timing Diagram



10. Quality Assurance

10.1 Purpose

This standard for Quality Assurance assures the quality of LCD module products supplied to customer.

10.2 Standard for Quality Test

10.2.1 Sampling Plan:

GB2828.1-2012

Single sampling, general inspection level II

10.2.2 Sampling Criteria:

Visual inspection: AQL 1.5%

Electrical functional: AQL 0.65%.

10.2.3 Reliability Test:

Detailed requirement refer to Reliability Test Specification.

10.3 Nonconforming Analysis & Disposition

10.3.1 Nonconforming analysis:

10.3.1.1 Customer should provide overall information of non-conforming sample for their complaints.

10.3.1.2 After receipt of detailed information from customer, the analysis of nonconforming parts usually should be finished in one week.

10.3.1.3 If cannot finish the analysis on time, customer will be notified with the progress status.

10.3.2 Disposition of nonconforming:

10.3.2.1 Non-conforming product over PPM level will be replaced.

10.3.2.2 The cause of non-conformance will be analyzed. Corrective action will be discussed and implemented.

10.4 Agreement Items

Shall negotiate with customer if the following situation occurs:

10.4.1 There is any discrepancy in standard of quality assurance.

10.4.2 Additional requirement to be added in product specification.

10.4.3 Any other special problem.

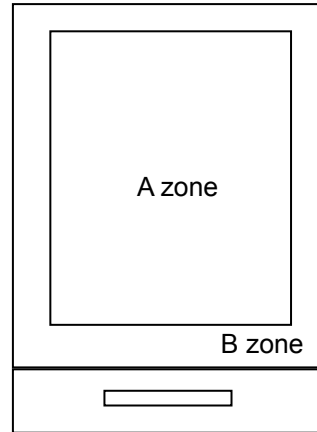
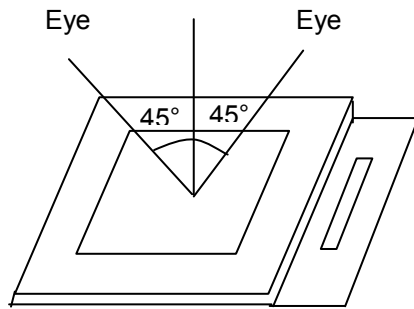
10.5 Standard of the Product Visual Inspection

10.5.1 Appearance inspection:

10.5.1.1 The inspection must be under illumination about 1000 – 1500 lx, and the distance of view must be at 30cm ± 2cm.

10.5.1.2 The viewing angle should be 45° from the vertical line without reflection light or follows customer's viewing angle specifications.

10.5.1.3 Definition of area: A Zone: Active Area, B Zone: Viewing Area,

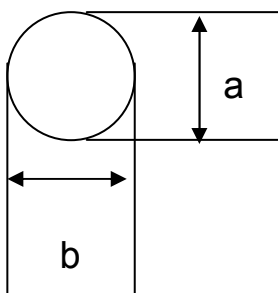


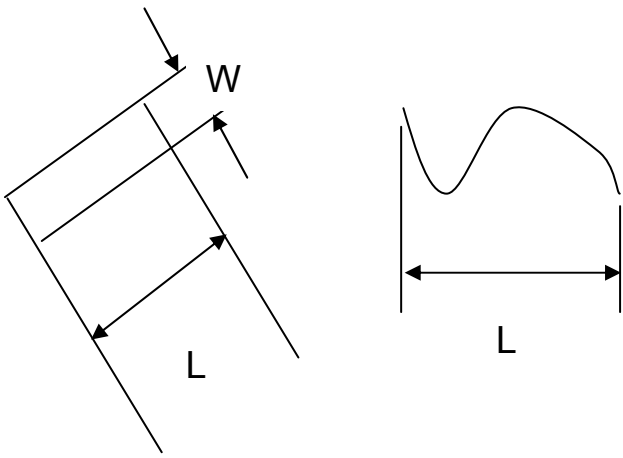
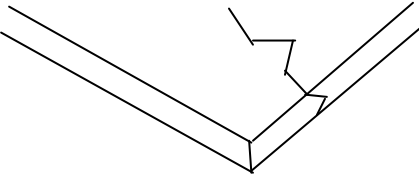
10.5.2 Basic principle:

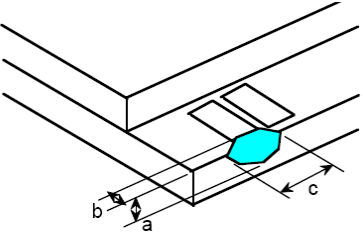
10.5.2.1 A set of sample to indicate the limit of acceptable quality level must be discussed by both us and customer when there is any dispute happened.

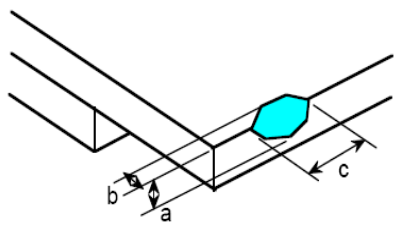
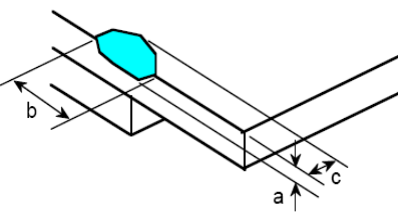
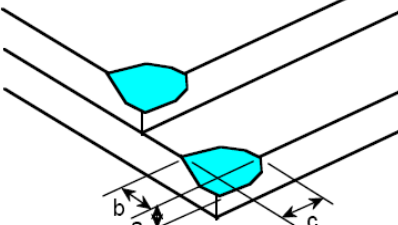
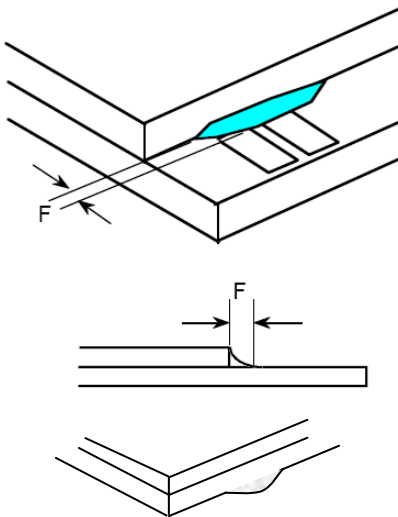
10.5.2.2 New item must be added on time when it is necessary.

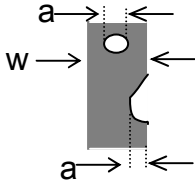
10.6 Inspection Specification

No.	Item	Criteria (Unit: mm)																		
01	Black / White spot Foreign material (Round type) Pinholes Stain Particles inside cell. (Minor defect)	 <table border="1" data-bbox="909 1030 1412 1254"> <thead> <tr> <th>Size</th> <th>Area</th> <th>Acc. Qty</th> </tr> </thead> <tbody> <tr> <td>$\phi \leq 0.20$</td> <td></td> <td>Ignore</td> </tr> <tr> <td>$0.20 < \phi \leq 0.50$</td> <td></td> <td>$N \leq 3$</td> </tr> <tr> <td>$0.50 < \phi$</td> <td></td> <td>0</td> </tr> </tbody> </table> <p>$\phi = (a + b) / 2$ Distance between 2 defects should more than 5mm apart.</p>	Size	Area	Acc. Qty	$\phi \leq 0.20$		Ignore	$0.20 < \phi \leq 0.50$		$N \leq 3$	$0.50 < \phi$		0						
Size	Area	Acc. Qty																		
$\phi \leq 0.20$		Ignore																		
$0.20 < \phi \leq 0.50$		$N \leq 3$																		
$0.50 < \phi$		0																		
02	Electrical Defect (Minor defect)	<table border="1" data-bbox="550 1433 1412 1691"> <thead> <tr> <th>Bright dot</th> <th>Display Area</th> <th>Total</th> <th rowspan="3">Note1</th> </tr> </thead> <tbody> <tr> <td></td> <td>$N \leq 2$</td> <td>$N \leq 2$</td> </tr> <tr> <td>Dark dot</td> <td>$N \leq 4$</td> <td>$N \leq 4$</td> </tr> <tr> <td>Total dot</td> <td>$N \leq 4$</td> <td>$N \leq 4$</td> <td></td> </tr> <tr> <td>Mura</td> <td colspan="2">Not visible through 5% ND filters.</td> <td>Note2</td> </tr> </tbody> </table> <p>Remark: 1. Bright dot caused by scratch and foreign object accords to item 1.</p>	Bright dot	Display Area	Total	Note1		$N \leq 2$	$N \leq 2$	Dark dot	$N \leq 4$	$N \leq 4$	Total dot	$N \leq 4$	$N \leq 4$		Mura	Not visible through 5% ND filters.		Note2
Bright dot	Display Area	Total	Note1																	
	$N \leq 2$	$N \leq 2$																		
Dark dot	$N \leq 4$	$N \leq 4$																		
Total dot	$N \leq 4$	$N \leq 4$																		
Mura	Not visible through 5% ND filters.		Note2																	

<p>03</p>	<p>Black and White line Scratch Foreign material (Line type) (Minor defect)</p>	 <table border="1" data-bbox="608 748 1235 1012"> <thead> <tr> <th>Length</th> <th>Width</th> <th>Acc. Qty</th> </tr> </thead> <tbody> <tr> <td>/</td> <td>$W \leq 0.1$</td> <td>Ignore</td> </tr> <tr> <td>$L \leq 2.5$</td> <td>$0.1 < W \leq 0.2$</td> <td>3</td> </tr> <tr> <td>$L > 2.5$</td> <td>$0.2 < W$</td> <td>0</td> </tr> <tr> <td colspan="2">Total</td> <td>3</td> </tr> </tbody> </table> <p>Distance between 2 defects should more than 3mm apart. Scratches not viewable through the back of the display are acceptable.</p>	Length	Width	Acc. Qty	/	$W \leq 0.1$	Ignore	$L \leq 2.5$	$0.1 < W \leq 0.2$	3	$L > 2.5$	$0.2 < W$	0	Total		3
Length	Width	Acc. Qty															
/	$W \leq 0.1$	Ignore															
$L \leq 2.5$	$0.1 < W \leq 0.2$	3															
$L > 2.5$	$0.2 < W$	0															
Total		3															
<p>04</p>	<p>Glass Crack (Minor defect)</p>	 <p>Crack is potential to enlarge, any type is not allowed.</p>															

<p>05</p>	<p>Glass Chipping Pad Area: (Minor defect)</p> 	<table border="1" data-bbox="863 1727 1334 1899"> <thead> <tr> <th>Length and Width</th> <th>Acc. Qty</th> </tr> </thead> <tbody> <tr> <td>$c > 3.0, b < 1.0$</td> <td>1</td> </tr> <tr> <td>$c < 3.0, b < 1.0$</td> <td>3</td> </tr> <tr> <td colspan="2">$a < \text{Glass Thickness}$</td> </tr> </tbody> </table>	Length and Width	Acc. Qty	$c > 3.0, b < 1.0$	1	$c < 3.0, b < 1.0$	3	$a < \text{Glass Thickness}$	
Length and Width	Acc. Qty									
$c > 3.0, b < 1.0$	1									
$c < 3.0, b < 1.0$	3									
$a < \text{Glass Thickness}$										

<p>06</p>	<p>Glass Chipping Rear of Pad Area: (Minor defect)</p> 	<table border="1"> <thead> <tr> <th>Length and Width</th> <th>Acc. Qty</th> </tr> </thead> <tbody> <tr> <td>$c > 3.0, b < 1.0$</td> <td>1</td> </tr> <tr> <td>$c < 3.0, b < 1.0$</td> <td>2</td> </tr> <tr> <td>$c < 3.0, b < 0.5$</td> <td>4</td> </tr> <tr> <td colspan="2" style="text-align: center;">$a < \text{Glass Thickness}$</td> </tr> </tbody> </table>	Length and Width	Acc. Qty	$c > 3.0, b < 1.0$	1	$c < 3.0, b < 1.0$	2	$c < 3.0, b < 0.5$	4	$a < \text{Glass Thickness}$	
Length and Width	Acc. Qty											
$c > 3.0, b < 1.0$	1											
$c < 3.0, b < 1.0$	2											
$c < 3.0, b < 0.5$	4											
$a < \text{Glass Thickness}$												
<p>07</p>	<p>Glass Chipping Except Pad Area: (Minor defect)</p> 	<table border="1"> <thead> <tr> <th>Length and Width</th> <th>Acc. Qty</th> </tr> </thead> <tbody> <tr> <td>$c > 3.0, b < 1.0$</td> <td>1</td> </tr> <tr> <td>$c < 3.0, b < 1.0$</td> <td>2</td> </tr> <tr> <td>$c < 3.0, b < 0.5$</td> <td>4</td> </tr> <tr> <td colspan="2" style="text-align: center;">$a < \text{Glass Thickness}$</td> </tr> </tbody> </table>	Length and Width	Acc. Qty	$c > 3.0, b < 1.0$	1	$c < 3.0, b < 1.0$	2	$c < 3.0, b < 0.5$	4	$a < \text{Glass Thickness}$	
Length and Width	Acc. Qty											
$c > 3.0, b < 1.0$	1											
$c < 3.0, b < 1.0$	2											
$c < 3.0, b < 0.5$	4											
$a < \text{Glass Thickness}$												
<p>08</p>	<p>Glass Corner Chipping: (Minor defect)</p> 	<table border="1"> <thead> <tr> <th>Length and Width</th> <th>Acc. Qty</th> </tr> </thead> <tbody> <tr> <td>$c < 3.0, b < 3.0$</td> <td>Ignore</td> </tr> <tr> <td colspan="2" style="text-align: center;">$a < \text{Glass Thickness}$</td> </tr> </tbody> </table>	Length and Width	Acc. Qty	$c < 3.0, b < 3.0$	Ignore	$a < \text{Glass Thickness}$					
Length and Width	Acc. Qty											
$c < 3.0, b < 3.0$	Ignore											
$a < \text{Glass Thickness}$												
<p>09</p>	<p>Glass Burr: (Minor defect)</p> 	<table border="1"> <thead> <tr> <th>Length</th> <th>Acc. Qty</th> </tr> </thead> <tbody> <tr> <td>$F < 1.0$</td> <td>Ignore</td> </tr> </tbody> </table> <p>Glass burr don't affect assemble and module dimension.</p>	Length	Acc. Qty	$F < 1.0$	Ignore						
Length	Acc. Qty											
$F < 1.0$	Ignore											

10	<p>FPC Defect: (Minor defect)</p> 	<p>10.1 Dent, pinhole width $a < w/3$. (w: circuitry width.) 10.2 Open circuit is unacceptable. 10.3 No oxidation, contamination and distortion.</p>								
11	Bubble on Polarizer (Minor defect)	<table border="1" data-bbox="737 595 1206 766"> <thead> <tr> <th>Diameter</th> <th>Acc. Qty</th> </tr> </thead> <tbody> <tr> <td>$\varphi \leq 0.30$</td> <td>Ignore</td> </tr> <tr> <td>$0.30 < \varphi \leq 0.50$</td> <td>$N \leq 2$</td> </tr> <tr> <td>$0.50 < \varphi$</td> <td>$N=0$</td> </tr> </tbody> </table>	Diameter	Acc. Qty	$\varphi \leq 0.30$	Ignore	$0.30 < \varphi \leq 0.50$	$N \leq 2$	$0.50 < \varphi$	$N=0$
Diameter	Acc. Qty									
$\varphi \leq 0.30$	Ignore									
$0.30 < \varphi \leq 0.50$	$N \leq 2$									
$0.50 < \varphi$	$N=0$									
12	Dent on Polarizer (Minor defect)	<table border="1" data-bbox="737 837 1206 1008"> <thead> <tr> <th>Diameter</th> <th>Acc. Qty</th> </tr> </thead> <tbody> <tr> <td>$\varphi \leq 0.25$</td> <td>Ignore</td> </tr> <tr> <td>$0.25 < \varphi \leq 0.50$</td> <td>$N \leq 4$</td> </tr> <tr> <td>$0.50 < \varphi$</td> <td>None</td> </tr> </tbody> </table>	Diameter	Acc. Qty	$\varphi \leq 0.25$	Ignore	$0.25 < \varphi \leq 0.50$	$N \leq 4$	$0.50 < \varphi$	None
Diameter	Acc. Qty									
$\varphi \leq 0.25$	Ignore									
$0.25 < \varphi \leq 0.50$	$N \leq 4$									
$0.50 < \varphi$	None									
13	Bezel	<p>13.1 No rust, distortion on the Bezel. 13.2 No visible fingerprints, stains or other contamination.</p>								
14	Touch Panel	<p>D: Diameter W: width L: length 14.1 Spot: $D < 0.25$ is acceptable $0.25 \leq D \leq 0.4$ 2dots are acceptable and the distance between defects should more than 10 mm. $D > 0.4$ is unacceptable 14.2 Dent: $D > 0.40$ is unacceptable 14.3 Scratch: $W \leq 0.03$, $L \leq 10$ is acceptable, $0.03 < W \leq 0.10$, $L \leq 10$ is acceptable Distance between 2 defects should more than 10 mm. $W > 0.10$ is unacceptable.</p>								
15	PCB	<p>15.1 No distortion or contamination on PCB terminals. 15.2 All components on PCB must same as documented on the BOM/component layout. 15.3 Follow IPC-A-600F.</p>								
16	Soldering	Follow IPC-A-610C standard								

17	Electrical Defect (Major defect)	<p>The below defects must be rejected.</p> <p>17.1 Missing vertical / horizontal segment, 17.2 Abnormal Display. 17.3 No function or no display. 17.4 Current exceeds product specifications. 17.5 LCD viewing angle defect. 17.6 No Backlight. 17.7 Dark Backlight. 17.8 Touch Panel no function.</p>
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Remark: LCD Panel Broken shall be rejected. Defect out of LCD viewing area is acceptable.

10.7 Classification of Defects

10.7.1 Visual defects (Except no / wrong label) are treated as minor defect and electrical defect is major.

10.7.2 Two minor defects are equal to one major in lot sampling inspection.

10.8 Identification/marketing criteria

Any unit with illegible / wrong /double or no marking/ label shall be rejected.

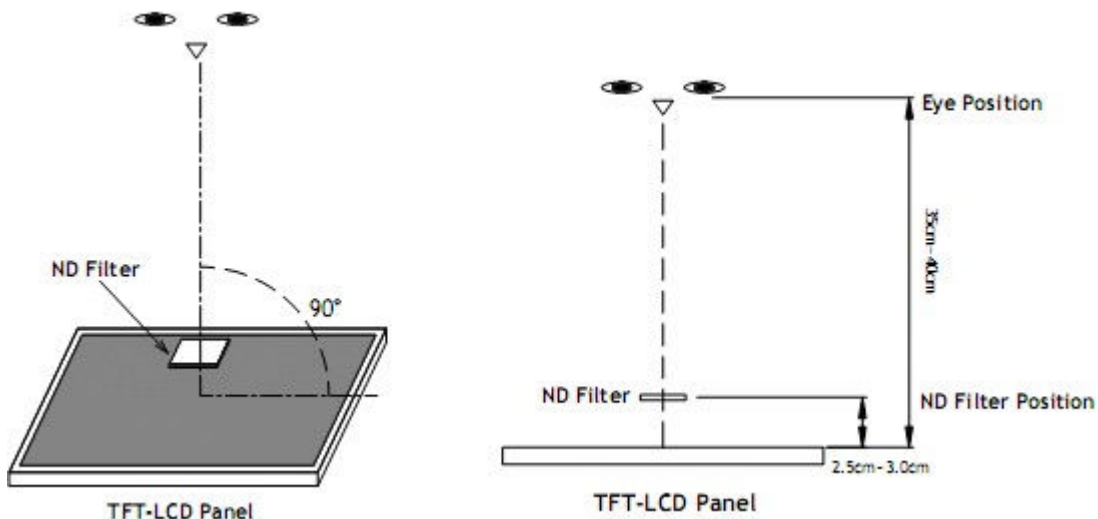
10.9 Packaging

10.9.1 There should be no damage of the outside carton box, each packaging box should have one identical label.

10.9.2 Modules inside package box should have compliant mark.

10.9.3 All direct package materials shall offer ESD protection

Note1: Bright dot is defined as the defective area of the dot is larger than 50% of one sub-pixel area.



Bright dot: The bright dot size defect at black display pattern. It can be recognized by 2% transparency of filter when the distance between eyes and panel is 350mm ± 50mm.

Dark dot: Cyan, Magenta or Yellow dot size defect at white display pattern. It can be recognized by 5% transparency of filter when the distance between eyes and panel is 350mm ± 50mm.

Note2: Mura on display which appears darker / brighter against background brightness on parts of display area.

11. Reliability Specification

No	Item	Condition	Quantity	Criteria
1	High Temperature Operating	70°C, 96Hrs	2	GB/T2423.2-2008
2	Low Temperature Operating	-20°C, 96Hrs	2	GB/T2423.1-2008
3	High Humidity	50°C, 90%RH, 96Hrs	2	GB/T2423.3-2006
4	High Temperature Storage	80°C, 96Hrs	2	GB/T2423.2-2008
5	Low Temperature Storage	-30°C, 96Hrs	2	GB/T2423.1-2008
6	Thermal Cycling Test	-20°C, 60min~70°C, 60min, 20 cycles.	2	GB/T2423.22-2012
7	Packing vibration	Frequency range:10Hz~50Hz Acceleration of gravity:5G X, Y, Z 30 min for each direction.	2	GB/T5170.14-2009
8	Electrical Static Discharge	Air: ±8KV 150pF/330Ω 5 times Contact: ±4KV 150pF/330Ω 5 times	2	GB/T17626.2-2006
9	Drop Test (Packaged)	Height:80 cm,1 corner, 3 edges, 6 surfaces.	2	GB/T2423.8-1995

Note1. No deflection cosmetic and operational function allowable.

Note2. Total current Consumption should be below double of initial value

12. Precautions and Warranty

12.1. Safety

- 12.1.1. The liquid crystal in the LCD is poisonous. Do not put it in your mouth. If the liquid crystal touches your skin or clothes, wash it off immediately using soap and water.
- 12.1.2. Since the liquid crystal cells are made of glass, do not apply strong impact on them. Handle with care.

12.2. Handling

- 12.2.1. Reverse and use within ratings in order to keep performance and prevent damage.
- 12.2.2. Do not wipe the polarizer with dry cloth, as it might cause scratch. If the surface of the LCD needs to be cleaned, wipe it swiftly with cotton or other soft cloth soaked with petroleum IPA, do not use other chemicals.

12.3. Storage

- 12.3.1. Do not store the LCD module beyond the specified temperature ranges.
- 12.3.2. Strong light exposure causes degradation of polarizer and color filter

12.4. Metal Pin (Apply to Products with Metal Pins)

12.4.1. Pins of LCD and Backlight

12.4.1.1 Solder tip can touch and press on the tip of Pin LEAD during the soldering

12.4.1.2 Recommended Soldering Conditions

Solder Type: Sn96.3~94-Ag3.3~4.3-Cu0.4~1.1

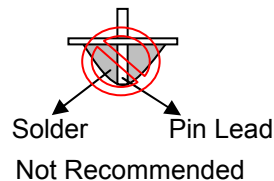
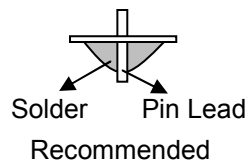
Maximum Solder Temperature: 370℃

Maximum Solder Time: 3s at the maximum temperature

Recommended Soldering Temp: 350±20℃

Typical Soldering Time: ≤3s

12.4.1.3 Solder Wetting



12.4.2. Pins of EL

12.4.2.1 Solder tip can touch and press on the tip of EL leads during soldering.

12.4.2.2 No Solder Paste on the soldering pad on the motherboard is recommended.

12.4.2.3 Recommended Soldering Conditions

Solder type: Nippon Alimit Leadfree SR-34, size 0.5mm

Recommended Solder Temperature: 270~290℃

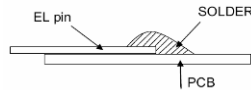
Typical Soldering Time: ≤2s

Minimum solder distance from EL lamp (body):2.0mm

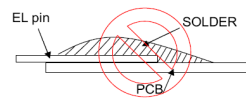
12.4.2.4 No horizontal press on the EL leads during soldering.

12.4.2.5 180° bend EL leads three times is not allowed.

12.4.2.6 Solder Wetting

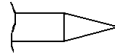


Recommended

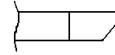


Not Recommended

12.4.2.7 The type of the solder iron:

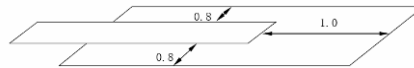


Recommended



Not Recommended

12.4.2.8 Solder Pad



12.5. Operation

- 12.5.1. Do not drive LCD with DC voltage
- 12.5.2. Response time will increase below lower temperature
- 12.5.3. Display may change color with different temperature
- 12.5.4. Mechanical disturbance during operation, such as pressing on the display area, may cause the segments to appear "fractured".
- 12.5.5. Do not connect or disconnect the LCM to or from the system when power is on.
- 12.5.6. Never use the LCM under abnormal condition of high temperature and high humidity.
- 12.5.7. Module has high frequency circuits. Sufficient suppression to the electromagnetic interface shall be done by system manufacturers. Grounding and shielding methods may be important to minimize the interference.
- 12.5.8. Do not display the fixed pattern for long time (we suggest the time not longer than one hour) because it may develop image sticking due to the TFT structure.

12.6. Static Electricity

- 12.6.1. CMOS LSIs are equipped in this unit, so care must be taken to avoid the electro-static charge, by ground human body, etc.
- 12.6.2. The normal static prevention measures should be observed for work clothes and benches.
- 12.6.3. The module should be kept into anti-static bags or other containers resistant to static for storage.

12.7. Limited Warranty

- 12.7.1. Our warranty liability is limited to repair and/or replacement. We will not be responsible for any consequential loss.
- 12.7.2. If possible, we suggest customer to use up all modules in six months. If the module storage time over twelve months, we suggest that recheck it before the module be used.
- 12.7.3. After the product shipped, any product quality issues must be feedback within three months, otherwise, we will not be responsible for the subsequent or consequential events.

13. Packaging

TBD

14. Outline Drawing

