

Spec No.	TQ3C-8EAF0-E1YAZ10-00
Date	January 16, 2015

TYPE: TCG104XGLPAPNN-AN40

< 10.4 inch XGA transmissive color TFT with LED backlight and constant current circuit for LED backlight>

CONTENTS

- 1. Application
- 2. Construction and outline
- 3. Mechanical specifications
- 4. Absolute maximum ratings
- 5. Electrical characteristics
- 6. Optical characteristics
- 7. Interface signals
- 8. Input timing characteristics
- 9. Lot number identification
- 10. Warranty
- 11. Precautions for use
- 12. Reliability test data
- 13. Outline drawing



KYOCERA DISPLAY CORPORATION

This specification is subject to change without notice. Consult Kyocera before ordering.

Original	Designed by: I	Engineering de _l	Confirmed by: QA dept.		
Issue Date	Prepared	Checked	Approved	Checked	Approved
January 16, 2015	M. Koyama	Y. Yamazaki	W. Yano	O. Sato	1. Hamais



Spec No.	Part No.	Page
TQ3C-8EAF0-E1YAZ10-00	TCG104XGLPAPNN-AN40	-

Warning

- 1. This Kyocera LCD module has been specifically designed for use only in electronic devices and industrial machines in the area of audio control, office automation, industrial control, home appliances, etc. The module should not be used in applications where the highest level of safety and reliability are required and module failure or malfunction of such module results in physical harm or loss of life, as well as enormous damage or loss. Such fields of applications include, without limitation, medical, aerospace, communications infrastructure, atomic energy control. Kyocera expressly disclaims any and all liability resulting in any way to the use of the module in such applications.
- 2. Customer agrees to indemnify, defend and hold Kyocera harmless from and against any and all actions, claims, damages, liabilities, awards, costs, and expenses, including legal expenses, resulting from or arising out of Customer's use, or sale for use, or Kyocera modules in applications.

Caution

1. Kyocera shall have the right, which Customer hereby acknowledges, to immediately scrap or destroy tooling for Kyocera modules for which no Purchase Orders have been received from the Customer in a two-year period.



Spec No.	Part No.	Page
TQ3C-8EAF0-E1YAZ10-00	TCG104XGLPAPNN-AN40	-

Revision record

Designed by: Engineering dept. Confirmed by: QA depends of the prepared Checked Approved Checked Approved Rev.No. Date Page Descriptions	
Rev.No. Date Page Descriptions	
Rev.No. Date Page Descriptions	



Spec No.	Part No.	Page
TQ3C-8EAF0-E1YAZ10-00	TCG104XGLPAPNN-AN40	1

1. Application

This document defines the specification of TCG104XGLPAPNN-AN40. (RoHS Compliant)

2. Construction and outline

LCD : Transmissive color dot matrix type TFT

Backlight system : LED

Polarizer : Anti-Glare treatment

Interface : LVDS

Additional circuit : Timing controller, Power supply (3.3V input)

With constant current circuit for LED Backlight(12V input)

3. Mechanical specifications

Item	Specification	Unit
Outline dimensions 1)	230(W)×180.2(H)×10.5(D)	mm
Active area	210.432(W)×157.824(H) (26.3cm/10.4 inch(Diagonal))	mm
Dot format	1,024×(B,G,R)(W)×768(H)	dot
Dot pitch	(0.0685)(W)×(0.2055)(H)	mm
Base color 2)	Normally Black	-
Mass	480	g

- 1) Projection not included. Please refer to outline for details.
- 2) Due to the characteristics of the LCD material, the color varies with environmental temperature.



Spec No.	Part No.	Page
TQ3C-8EAF0-E1YAZ10-00	TCG104XGLPAPNN-AN40	2

4. Absolute maximum ratings

4-1. Electrical absolute maximum ratings

Item		Symbol	Min.	Max.	Unit
Supply voltage(+3.3V)		$V_{ m DD}$	-0.3	3.95	V
Supply voltage(+12V)		$V_{\rm IN}$	-0.3	14.0	V
	RxINi+, RxINi- (i=0,1,2,3)	V_{I1}	-0.3	V _{DD} +0.3	V
Input signal	CK IN+, CK IN-	V_{I2}	-0.3	V _{DD} +0.3	V
Voltage 1)	MODE, SC	V_{I3}	-0.3	V _{DD} +0.3	V
	BLBRT, BLEN	V_{I4}	-0.3	V_{IN}	V

1) V_{DD} must be supplied correctly within the range described in 5-1.

4-2. Environmental absolute maximum ratings

Item		Symbol	Min.	Max.	Unit
Operating temperature (Ambient)	1)	Top(Ambient)	-30	80	$^{\circ}\mathrm{C}$
Operating temperature (Panel)	2)	T _{OP} (Panel)	-30	80	$^{\circ}\mathrm{C}$
Storage temperature	3)	Tsto	-30	80	$^{\circ}\mathrm{C}$
Operating humidity	4)	Нор	10	5)	%RH
Storage humidity	4)	${ m H}_{ m STO}$	10	5)	%RH
Vibration		-	6)	6)	-
Shock		-	7)	7)	-

- 1) Operating temperature means a temperature which operation shall be guaranteed. Since display performance is evaluated at 25°C, another temperature range should be confirmed.
- 2) Panel surface temperature (all the surface).
- 3) Temp. = -30° C < 48h, Temp. = 80° C < 168h

Store LCD at normal temperature/humidity. Keep them free from vibration and shock. An LCD that is kept at a low or a high temperature for a long time can be defective due to other conditions, even if the low or high temperature satisfies the standard.

(Please refer to "Precautions for Use" for details.)

- 4) Non-condensing
- 5) Temp. ≤ 40°C, 85%RH Max.

Temp. > 40°C, Absolute humidity shall be less than 85%RH at 40°C.

6)

Frequency	10∼55 Hz	Acceleration value
Vibration width	0.15mm	$(0.3\sim 9 \text{ m/s}^2)$
Interval	10-55-10	Hz 1 minutes

2 hours in each direction X, Y, Z (6 hours total) EIAJ ED-2531

7) Acceleration: 490 m/s², Pulse width: 11 ms

3 times in each direction: $\pm X$, $\pm Y$, $\pm Z$

EIAJ ED-2531



Spec No.	Part No.	Page
TQ3C-8EAF0-E1YAZ10-00	TCG104XGLPAPNN-AN40	3

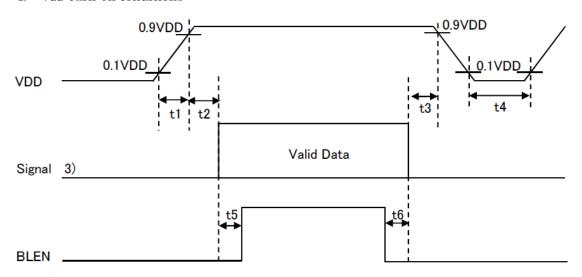
5. Electrical characteristics

5-1. LCD

Temp. = $-30 \sim 80$ °C

						remp.	
Item		Symbol	Condition	Min.	Тур.	Max.	Unit
Supply voltage	1)	V_{DD}	-	3.0	3.3	3.6	V
Current consumption		I_{DD}	2)	-	270	350	mA
Permissive input ripple vo	ltage	V_{RP}	V _{DD} =3.3V	-	-	100	mVp-p
T 1 1.	o)	$V_{\rm IL}$	"Low" level	0	-	$0.3V_{\mathrm{DD}}$	V
Input signal voltage	3)	V _{IH}	"High" level	$0.7 V_{\mathrm{DD}}$	-	$V_{ m DD}$	V
T 4 1 1 4		I_{OL}	V ₁₃ =0V	-10	-	10	μ A
Input leak current		Іон	V _{I3} =3.3V	-	-	400	μΑ
LVDS Input voltage	4)	$V_{\rm L}$	-	0	-	1.9	V
Differential input voltage		V_{ID}	-	200	-	600	mV
Differential input	4) 5)	V_{TL}	"Low" level	V _{CM} -100	-	-	mV
threshold voltage	4) 5)	V_{TH}	"High" level	-	-	V _{CM} +100	mV
Terminator		R_1	-	-	100	-	Ω
		t1	-	0.1	-	20	ms
		t2	-	10	-	-	ms
77	4) a)	t3	-	0	-	-	ms
V _{DD} -turn-on conditions	1) 6)	t4	-	2	-	-	s
		t5	-	200	-	-	ms
		t6	-	200	-	-	ms

1) V_{DD}-turn-on conditions

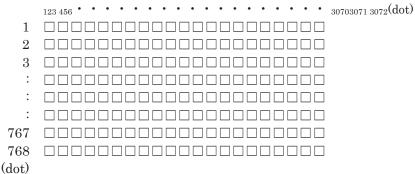




Spec No.	Part No.	Page
TQ3C-8EAF0-E1YAZ10-00	TCG104XGLPAPNN-AN40	4

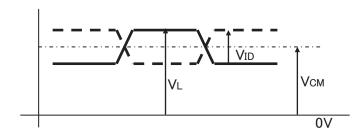
2) Display pattern:

$$V_{DD} = 3.3V$$
, Temp. = 25°C



3) Input signal: MODE, SC

4) Input signal : RxIN3+, RxIN3-, RxIN2+, RxIN2-, RxIN1+, RxIN1-, RxIN0+, RxIN0-CK IN+, CK IN-



5) V_{CM}: LVDS Common mode voltage (V_{CM}=1.25V

6) Please power on LVDS transmitter at the same time as VDD, or LVDS transmitter should be powered on first.



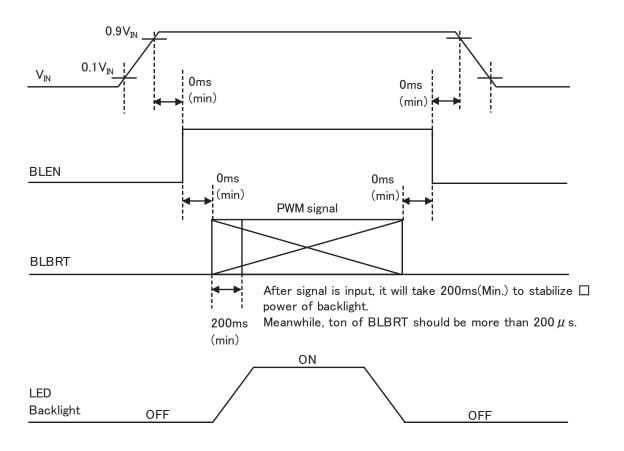
Spec No.	Part No.	Page
TQ3C-8EAF0-E1YAZ10-00	TCG104XGLPAPNN-AN40	5

5-2. Constant current circuit for LED Backlight

Temp. = $-30 \sim 80$ °C

Item	Symbol	Condition	Min.	Тур.	Max.	Unit
Supply voltage 1)	V _{IN}	-	10.8	12.0	13.2	V
Current consumption	I_{IN}	2)	-	870	1,120	mA
Permissive input ripple voltage	$V_{\mathrm{RP_BL}}$	V _{IN} =12.0V	-	-	100	mVp-p
DI DDT Issued signs of scale one	V _{IL_BLBRT}	"Low" level	0	-	0.8	V
BLBRT Input signal voltage	V _{IH_BLBRT}	"High" level	2.3	-	$V_{\rm IN}$	V
BLBRT Input pull-down resistance	R _{IN_BLBRT}	-	100	300	500	kΩ
DI EN Issant simual salta sa	V _{IL_BLEN}	"Low" level	0	-	0.8	V
BLEN Input signal voltage	V _{IH_BLEN}	"High" level	2.3	-	$V_{\rm IN}$	V
BLEN Input pull-down resistance	R _{IN_BLEN}	-	100	300	500	kΩ
PWM Frequency 3)	fрwм	-	200	-	10k	Hz
		f_{PWM} =200Hz	1	-	100	%
PWM Duty ratio 3)	$\mathrm{D}_{\mathrm{PWM}}$	f _{PWM} =2kHz	10	-	100	%
		f _{PWM} =10kHz	50	-	100	%
Operating life time 4), 5)	Т	Temp.=25°C	-	70,000	-	h

1) V_{IN}-turn-on conditions

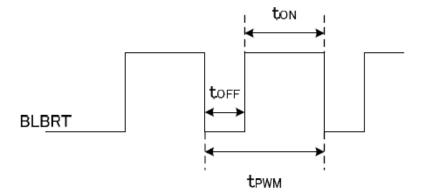


2) $V_{IN} = 12V$, Temp. = 25°C, $D_{PWM} = 100\%$



Spec No.	Part No.	Page
TQ3C-8EAF0-E1YAZ10-00	TCG104XGLPAPNN-AN40	6

3) PWM Timing Diagram



ton, toff $\geq 50 \,\mu$ s.

In case of lower frequency, the deterioration of the display quality, flicker etc., may occur.

- 4) When brightness decrease 50% of minimum brightness.

 The average life of a LED will decrease when the LCD is operating at higher temperatures.
- 5) Life time is estimated data.(Condition: IF=(80)mA, Ta=25°C in chamber).



Spec No.	Part No.	Page
TQ3C-8EAF0-E1YAZ10-00	TCG104XGLPAPNN-AN40	7

6. Optical characteristics

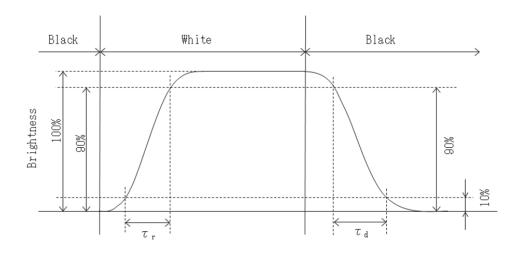
Measuring spot = ϕ 6.0mm, Temp. = 25°C

Item		Symbol	Condition	Min.	Тур.	Max.	Unit
D (*	Rise	τr	$\theta = \phi = 0^{\circ}$	-	18	-	ms
Response time	Down	τd	$\theta = \phi = 0^{\circ}$	-	12	-	ms
		θ upper		-	85	-	1
Viewing angle r	ange	θ LOWER	CD > 10	-	85	-	deg.
View direction		ϕ left	CR≧10	-	85	-	1
		ϕ right		-	85	-	deg.
Contrast ratio		CR	$\theta = \phi = 0^{\circ}$	490	700	-	-
Brightness		L	IF=(80)mA/Line	910	1300	-	cd/m²
	Red	X	$\theta = \phi = 0$ °	0.550	0.600	0.650	
		У		0.300	0.350	0.400	
	C	X	0 1 00	0.285	0.335	0.385	
Chromaticity	Green	У	$\theta = \phi = 0$ °	0.520	0.570	0.620	
coordinates	D1	X	$\theta = \phi = 0^{\circ}$	0.100	0.150	0.200	-
	Blue	У	$\theta = \phi = 0$	0.070	0.120	0.170	
		X	0 1 00	0.265	0.315	0.365	
		У	$\theta = \phi = 0$ °	0.290	0.340	0.390	

6-1. Definition of contrast ratio

 $CR(Contrast ratio) = \frac{Brightness with all pixels "White"}{Brightness with all pixels "Black"}$

6-2. Definition of response time

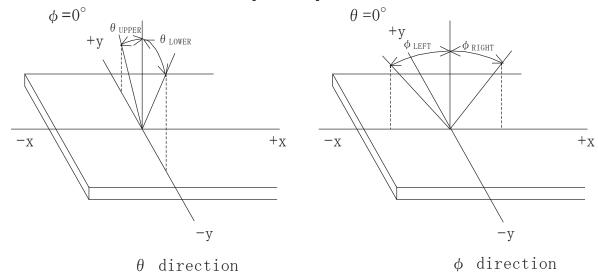




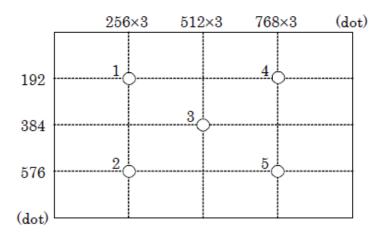
Spec No.	Part No.	Page
TQ3C-8EAF0-E1YAZ10-00	TCG104XGLPAPNN-AN40	8

6-3. Definition of viewing angle





6-4. Brightness measuring points



- 1) Rating is defined as the white brightness at center of display screen(3).
- 2) 5 minutes after LED is turned on. (Ambient Temp.=25°C)



Spec No.	Part No.	Page
TQ3C-8EAF0-E1YAZ10-00	TCG104XGLPAPNN-AN40	9

7. Interface signals

7-1. Interface signals

No.	Symbol	Description	Note
1	$V_{ m DD}$	+3.3V power supply	
2	$V_{ m DD}$	+3.3V power supply	
3	GND	GND	
4	GND	GND	
5	RxIN0-	LVDS receiver signal CH0(-)	LVDS
6	RxIN0+	LVDS receiver signal CH0(+)	LVDS
7	GND	GND	
8	RxIN1-	LVDS receiver signal CH1(-)	LVDS
9	RxIN1+	LVDS receiver signal CH1(+)	LVDS
10	GND	GND	
11	RxIN2-	LVDS receiver signal CH2(-)	LVDS
12	RxIN2+	LVDS receiver signal CH2(+)	LVDS
13	GND	GND	
14	CK IN1-	LVDS receiver signal CK(-)	LVDS
15	CK IN1+	LVDS receiver signal CK(+)	LVDS
16	GND	GND	
17	RxIN3-	LVDS receiver signal CH3(-)	LVDS
18	RxIN3+	LVDS receiver signal CH3(+)	LVDS
19	MODE	Bit data select signal(GND: 6bit mode, High: 8bit mode)	
20	SC	Scan direction control(GND: Normal、High: Reverse)	1)

LVDS receiver : Embedded in ASIC

 $Matching\ LVDS\ transmitter\ :\ THC63LVDM83R (THine\ Electronics)\ or\ compatible$

1) Scanning

SC:GND SC:High







Spec No.	Part No.	Page
TQ3C-8EAF0-E1YAZ10-00	TCG104XGLPAPNN-AN40	10

7-2. LED

No.	Symbol	Description	Note
1	V_{IN}	+12V power supply	
2	$V_{\rm IN}$	+12V power supply	
3	BLBRT	PWM signal(Brightness adjustment)	
4	BLEN	ON/OFF terminal voltage	
5	GND	GND	
6	GND	GND	



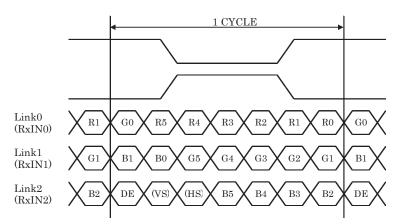
Spec No.	Part No.	Page
TQ3C-8EAF0-E1YAZ10-00	TCG104XGLPAPNN-AN40	11

7-3. Data mapping(6bit input)

1) Location of MODE (THC63LVDM83R(THine Electronics) or compatible)

		MODE
Transi		MODE
Pin No.	Data	= L(GND)
51	TA0	R0(LSB)
52	TA1	R1
54	TA2	R2
55	TA3	R3
56	TA4	R4
3	TA5	R5(MSB)
4	TA6	G0(LSB)
6	TB0	G1
7	TB1	G2
11	TB2	G3
12	TB3	G4
14	TB4	G5(MSB)
15	TB5	B0(LSB)
19	TB6	B1
20	TC0	B2
22	TC1	В3
23	TC2	B4
24	TC3	B5(MSB)
27	TC4	(HS)
28	TC5	(VS)
30	TC6	DE
50	TD0	GND
2	TD1	GND
8	TD2	GND
10	TD3	GND
16	TD4	GND
18	TD5	GND
25	TD6	(NA)

MODE=L(GND)



DE: DATA ENABLE

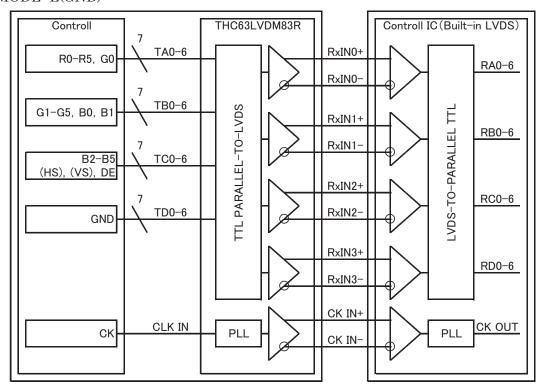
 $\begin{array}{l} HS: H_{SYNC} \\ VS: V_{SYNC} \end{array}$



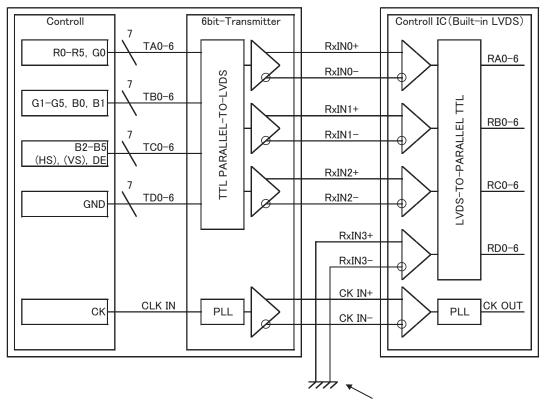
Spec No.	Part No.	Page
TQ3C-8EAF0-E1YAZ10-00	TCG104XGLPAPNN-AN40	12

2) Block Diagram

MODE=L(GND)



When using "6-bit Transmitter", please connect the unused channel of the control IC receiver as described in the diagram below.



Please connect RxIn3+/RxIn3- to GND.



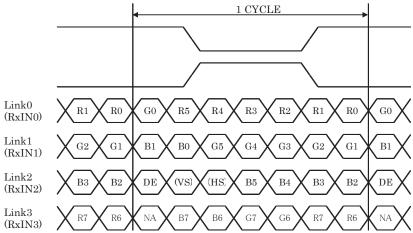
Spec No.	Part No.	Page
TQ3C-8EAF0-E1YAZ10-00	TCG104XGLPAPNN-AN40	13

7-4. Data mapping(8bit input)

1) Location of MODE (THC63LVDM83R(THine Electronics) or compatible)

Trans	smitter	MODE
Pin No.	Data	= H(3.3V)
51	TA0	R0(LSB)
52	TA1	R1
54	TA2	R2
55	TA3	R3
56	TA4	R4
3	TA5	R5
4	TA6	G0(LSB)
6	TB0	G1
7	TB1	G2
11	TB2	G3
12	TB3	G4
14	TB4	G5
15	TB5	B0(LSB)
19	TB6	B1
20	TC0	B2
22	TC1	В3
23	TC2	B4
24	TC3	B5
27	TC4	(HS)
28	TC5	(VS)
30	TC6	DE
50	TD0	R6
2	TD1	R7(MSB)
8	TD2	G6
10	TD3	G7(MSB)
16	TD4	B6
18	TD5	B7(MSB)
25	TD6	(NA)

MODE = H(3.3V)



DE: DATA ENABLE

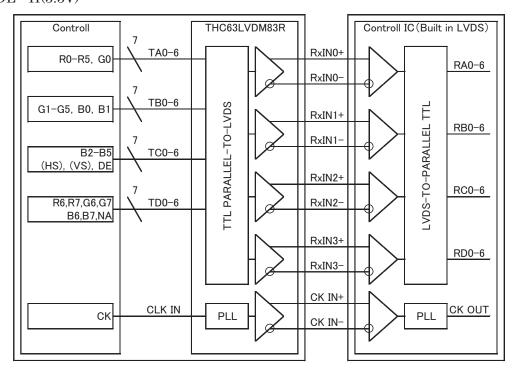
 $HS: H_{SYNC}$ $VS: V_{SYNC}$



Spec No.	Part No.	Page
TQ3C-8EAF0-E1YAZ10	0-00 TCG104XGLPAPNN-AN40	14

2) Block Diagram

MODE = H(3.3V)





Spec No.	Part No.	Page
TQ3C-8EAF0-E1YAZ10-00	TCG104XGLPAPNN-AN40	15

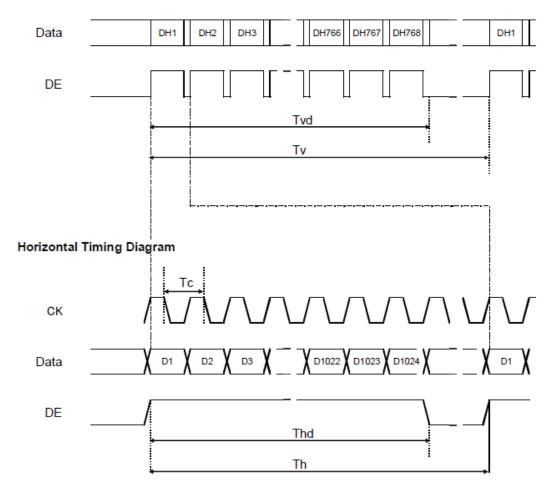
8. Input timing characteristics

8-1. Timing characteristics

Item		Symbol	Min.	Typ.	Max.	Unit	Note
Clock (CK)	Frequency	1/Tc	52	65	71	MHz	
Enable signal (DE) Horizontal Period Horizontal display perio	Havigantal David	Th	1,114	1,344	1,400	Dot	
	norizontal Period		15.7	20.7	23.7	μ s	1)
	Horizontal display period	Thd	1,024		Тс		
(DE)	Vertical Period	Tv	778	806	845	Line	
	Vertical display period	Tvd	768		Th		
Refresh rate		fv	50	60	70	Hz	2)

- 1) Please set a clock frequency, a vertical dormant period, and the horizontal dormant period so that the Horizontal Period should not reach less than Min. value.
- 2) If the refresh rate reach less than Min. value, the deterioration of the display quality, flicker etc., may occur.(fv=1/Tv)

Vertical Timing Diagram





Spec No.	Part No.	Page
TQ3C-8EAF0-E1YAZ10-00	TCG104XGLPAPNN-AN40	16

8-2. Input Data Signals and Display position on the screen

D1, DH1 D2, DH1 D3, DH1 D1, DH2 D2, DH2 D3, DH2		D1024, DH1
	B G R	
	БСК	
D1, DH768 D2, DH768 D3, DH768		



Spec No.	Part No.	Page
TQ3C-8EAF0-E1YAZ10-00	TCG104XGLPAPNN-AN40	17

9. Lot number identification

The lot number shall be indicated on the back of the backlight case of each LCD.

No1. - No5. above indicate

- 1. Year code
- 2. Month code
- 3. Date
- 4. Version Number
- 5. Country of origin (Japan or China)

Year	2015	2016	2017	2018	2019	2020
Code	5	6	7	8	9	0

Month	Jan.	Feb.	Mar.	Apr.	May	Jun.
Code	1	2	3	4	5	6

Month	Jul.	Aug.	Sep.	Oct.	Nov.	Dec.
Code	7	8	9	X	Y	Z

10. Warranty

10-1. Incoming inspection

Please inspect the LCD within one month after your receipt.

10-2. Production warranty

Kyocera warrants its LCD's for a period of 12 months from the ship date. Kyocera shall, by mutual agreement, replace or re-work defective LCD's that are shown to be Kyocera's responsibility.



Spec No.	Part No.	Page
TQ3C-8EAF0-E1YAZ10-00	TCG104XGLPAPNN-AN40	18

11. Precautions for use

11-1. Installation of the LCD

- 1) A transparent protection plate shall be added to protect the LCD and its polarizer.
- 2) The LCD shall be installed so that there is no pressure on the LSI chips.
- 3) Since this product is wide viewing product, occurrence level of in-plane unevenness by the external stress is different compared to current normal viewing product. So there is a possibility that in-plane unevenness will be occurred by over twist, strain giving by attaching to LCD, and over pressure to touch panel. Please be careful of stress when designing the housing.
- 4) A transparent protection sheet is attached to the polarizer. Please remove the protection film slowly before use, paying attention to static electricity.

11-2. Static electricity

- 1) Since CMOS ICs are mounted directly onto the LCD glass, protection from static electricity is required.
- 2) Workers should use body grounding. Operator should wear ground straps.

11-3. LCD operation

- 1) The LCD shall be operated within the limits specified. Operation at values outside of these limits may shorten life, and/or harm display images.
- 2) Please select the best display pattern based on your evaluation because flicker, lines or nonuniformity or unevenness can be visible depending on display patterns.

11-4. Storage

- The LCD shall be stored within the temperature and humidity limits specified.
 Store in a dark area, and protect the LCD from direct sunlight or fluorescent light.
- 2) Always store the LCD so that it is free from external pressure onto it.

11-5. Usage

- 1) <u>DO NOT</u> store in a high humidity environment for extended periods. Polarizer degradation bubbles, and/or peeling off of the polarizer may result.
- 2) The front polarizer is easily scratched or damaged. Prevent touching it with any hard material, and from being pushed or rubbed.
- 3) The LCD screen may be cleaned by wiping the screen surface with a soft cloth or cotton pad using a little Ethanol.
- 4) Water may cause damage or discoloration of the polarizer. Clean condensation or moisture from any source immediately.
- 5) Always keep the LCD free from condensation during testing. Condensation may permanently spot or stain the polarizer.
- 6) Do not disassemble LCD because it will result in damage.
- 7) This Kyocera LCD has been specifically designed for use in general electronic devices, but not for use in a special environment such as usage in an active gas. Hence, when the LCD is supposed to be used in a special environment, evaluate the LCD thoroughly beforehand and do not expose the LCD to chemicals such as an active gas.
- 8) Please do not use solid-base image pattern for long hours because a temporary afterimage may appear. We recommend using screen saver etc. in cases where a solid-base image pattern must be used.
- 9) Liquid crystal may leak when the LCD is broken. Be careful not to let the fluid go into your eyes and mouth. In the case the fluid touches your body; rinse it off right away with water and soap.



Spec No.	Part No.	Page
TQ3C-8EAF0-E1YAZ10-00	TCG104XGLPAPNN-AN40	19

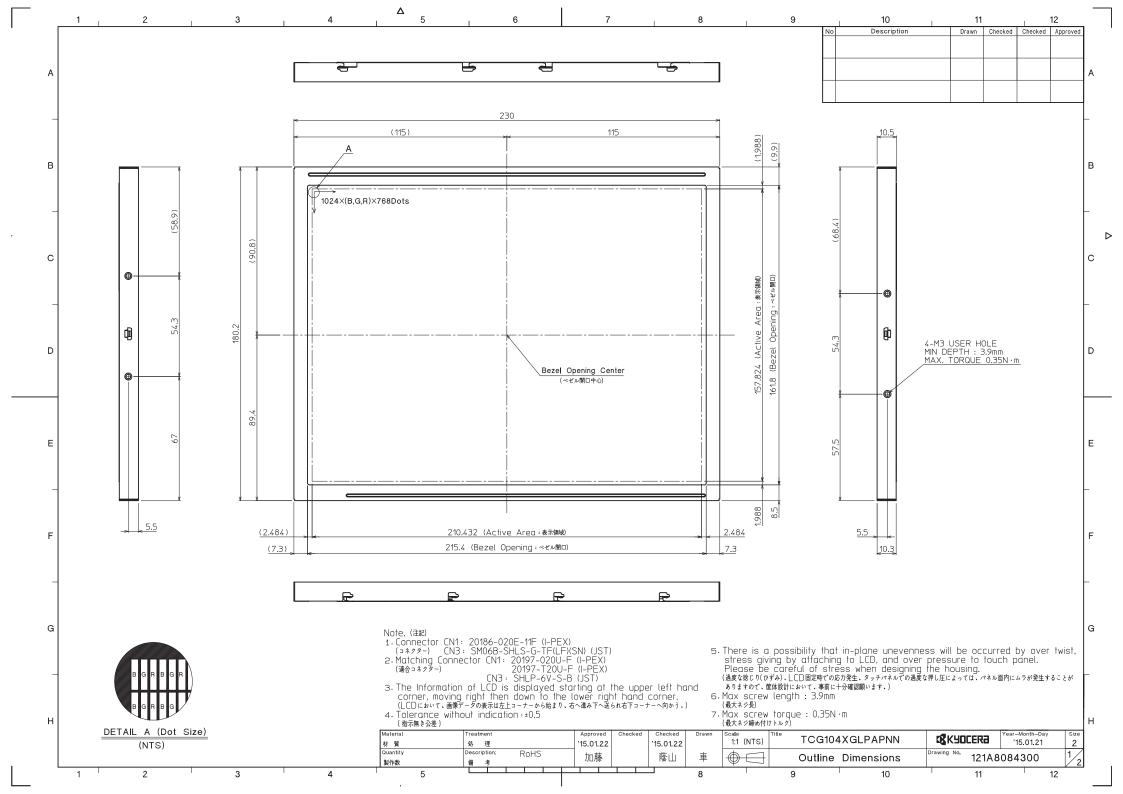
12. Reliability test data

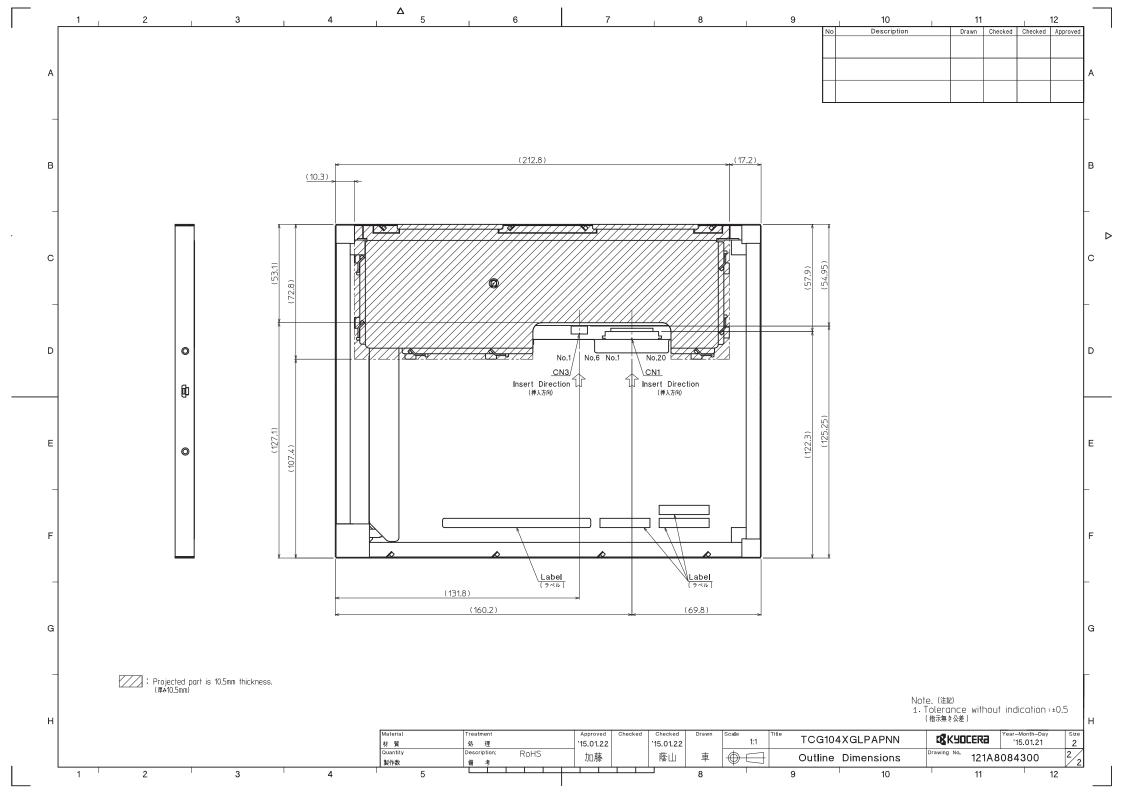
Test item	Test condition	Test time	Jud	gement
High temp. atmosphere	80°C	240h	Display function Display quality Current consumption	: No Defect : No Defect : No Defect
Low temp. atmosphere	-30°C	240h	Display function Display quality Current consumption	: No Defect : No Defect : No Defect
High temp. humidity atmosphere	40°C 90% RH	240h	Display function Display quality Current consumption	: No Defect : No Defect : No Defect
Temp. cycle	-30°C 0.5h R.T. 0.5h 80°C 0.5h	10cycles	Display function Display quality Current consumption	: No Defect : No Defect : No Defect
High temp. operation	80°C	500h	Display function Display quality Current consumption	: No Defect : No Defect : No Defect

- 1) Each test item uses a test LCD only once. The tested LCD is not used in any other tests.
- 2) The LCD is tested in circumstances in which there is no condensation.
- 3) The reliability test is not an out-going inspection.
- 4) The result of the reliability test is for your reference purpose only.

 The reliability test is conducted only to examine the LCD's capability.







ı	Spec No.	TQ3C-8EAF0-E2YAZ10-00
	Date	January 16, 2015

KYOCERA INSPECTION STANDARD

TYPE: TCG104XGLPAPNN-AN40

KYOCERA DISPALY CORPORATION

Original	Designed by : Engineering dept.			dept. Confirmed by : QA dept.		
Issue Date	Prepared	Checked	Approved	Checked	Approved	
January 16, 2015	M. Koyama	Y. Yamazaki	W. Yano	O. Sato	I Hamay 8	



ı	Spec No.	Part No.	Page
	TQ3C-8EAF0-E2YAZ10-00	TCG104XGLPAPNN-AN40	-

Revision record

	Data	Designed	Designed by : Engineering dept.		Confirmed by	: QA dept.	
	Date	Prepar	ed	Checked	Approved	Checked	Approved
Rev.No.	Date	Page			Descripti	ons	



Spec No.	Part No.	Page
TQ3C-8EAF0-E2YAZ10-00	TCG104XGLPAPNN-AN40	1

Visuals specification 1) Note

1) Note						
			Note			
General	reviewe 2. This ins	er identified anomalies not defined within this inspection standard shall be d by Kyocera, and an additional standard shall be determined by mutual consent. Spection standard about the image quality shall be applied to any defect within the eviewing area and shall not be applicable to outside of the area. Son conditions : 500 Lux min.				
		ion distance	300 mm.			
	Temper		: 25 ± 5°C			
	Direction		: Directly above			
Definition of inspection item	Dot defect	Black dot defect Black dot defect White dot (Circular/foreign particle) Adjacent dot	The dot is constantly "on" when power applied to the LCD, even when all "Black" data sent to the screen. Inspection tool: 5% Transparency neutral density filter. Count dot: If the dot is visible through the filter. Don't count dot: If the dot is not visible through the filter. There is an electrode in the middle of the dot and one dot is shown in the left drawing. RGBRGBRGB Coot drawing> The dot is constantly "off" when power applied to the LCD, even when all "White" data sent to the screen. Similar size compared to bright dot. Pixel works electrically, however, circular/foreign particle makes dot appear to be "on" even when all "Black" data is sent to the screen. Adjacent dot defect is defined as two or more bright dot defects or black dot defects. RGBRGBRGBRGB RGBRGBRGB RGBRGBRGB RGBRGBRGB dot defect dot defect			
	External inspection	Bubble, Scratch, Foreign particle (Polarizer, Cell, Backlight) Appearance inspection	Visible operating (all pixels "Black" or "White") and non operating. Does not satisfy the value at the spec.			
	Others	CFL wires	Damaged to the CFL wires, connector, pin, functional failure or appearance failure.			
	Definition of size	Definition of circles defined at the definition of circles defined at the definition of circles at the	Definition of linear size			



Spec No.	Part No.	Page
TQ3C-8EAF0-E2YAZ10-00	TCG104XGLPAPNN-AN40	2

2) Standard

2) Standa	rd							
Classification		Inspection item		Judgement standard				
Defect	Dot	Bright dot defect		Acceptable number : 4		: 4	Į.	
(in LCD	defect	et		Bright dot spacing : 5 mm		or more		
glass)		Black dot defect		Acceptable number : 5				
				Black dot spacing : 5 mm or more				
		2 dot join	Bright dot	Acceptable number : 2				
			defect	Acceptable number		. 2		
			Black dot defect	Acceptable number : 3				
		3 or more dots join		Acceptable number : 0				
		Total dot defects		Acceptable number : 5 Max				
	Others	White dot, Dark dot		*				
		(Circle)		Size (mm)		Acceptable number		
				d ≦ 0.2		(Neglected)		
				$0.2 < d \le 0.4$		5		
					$0.4 < d \leq 0.5$		3	
				0.5 < d	0.5 < d		0	
External	inspection	Polarizer (Scratch)					
(Defect on				Width (mm)	Length (h (mm) Acceptable number		
Polarizer or				$W \leq 0.1$			(Neglected)	
between Polarizer				$0.1 < W \le 0.3$	$\begin{array}{c} \text{L} \leq 5.0 \\ \text{5.0} < \text{L} \end{array}$		(Neglected)	
and LCD glass)				$0.1 < W \ge 0.3$			0	
and Bob glassy				0.3 < W	_		0	
		Polarizer (Bubble)					
				Size (mm)		Acceptable number		
				d ≦ 0.2		(Neglected)		
				$0.2 < d \le 0.3$		5		
				$0.3 < d \le 0.5$		3		
				0.5 < d		0		
		Foreign pa	ırticle					
		(Circular shape)		Size (mm)		Acceptable number		
				$d \leq 0.2$		(Neglected)		
				$0.2 < d \le 0.4$		5		
				$0.4 < d \le 0.5$		3		
				0.5 < d		0		
		Foreign particle						
		(Linear shape) Scratch		Width (mm)	Length (mm)		Acceptable number	
				$W \leq 0.03$			(Neglected)	
					L ≤ 2.0		(Neglected)	
				$0.03 < W \le 0.1$	$2.0 < L \le 4.0$ 4.0 < L		3	
							0	
				0.1 < W	-		(According to	
							circular shape)	

