

Spec No.	TQ3C-8EAF0-E1YAA240-00
Date	January 31, 2018

TYPE: TCG070WVLPAANN-AN50-SA

< 7.0 inch WVGA transmissive color TFT
with LED backlight>

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KYOCERA DISPLAY CORPORATION

This specification is subject to change without notice. Consult Kyocera before ordering.

Original	Designed by: Engineering dept.			Confirmed by: QA dept.	
Issue Date	Prepared	Checked	Approved	Checked	Approved
January 31, 2018	S. Mori	K.Edo	G. Matsumoto	H.Sumi	K. Sinjeme



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Warning

- 1. This Kyocera LCD module has been specifically designed for use only in electronic devices and industrial machines in the area of audio control, office automation, industrial control, home appliances, etc. The module should not be used in applications where the highest level of safety and reliability are required and module failure or malfunction of such module results in physical harm or loss of life, as well as enormous damage or loss. Such fields of applications include, without limitation, medical, aerospace, communications infrastructure, atomic energy control. Kyocera expressly disclaims any and all liability resulting in any way to the use of the module in such applications.
- 2. Customer agrees to indemnify, defend and hold Kyocera harmless from and against any and all actions, claims, damages, liabilities, awards, costs, and expenses, including legal expenses, resulting from or arising out of Customer's use, or sale for use, or Kyocera modules in applications.

Caution

1. Kyocera shall have the right, which Customer hereby acknowledges, to immediately scrap or destroy tooling for Kyocera modules for which no Purchase Orders have been received from the Customer in a two-year period.



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Revision record

	Date	Designo			Confirmed by	: QA dept.	
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1. Application

This document defines the specification of TCG070WVLPAANN-AN50-SA. (RoHS Compliant)

2. Construction and outline

LCD : Transmissive color dot matrix type TFT

Backlight system : LED

Polarizer : Anti-Glare treatment

Additional circuit : Timing controller, Power supply (3.3V input)

(without constant current circuit for LED Backlight)

3. Mechanical specifications

Item	Specification	Unit
Outline dimensions 1)	165(W)×(104.4)(H)×8.2(D)	mm
Active area	152.4(W)×91.44(H) (17.8cm/7.0 inch(Diagonal))	mm
Dot format	800×(R,G,B)(W)×480(H)	dot
Dot pitch	0.0635(W)×0.1905(H)	mm
Base color 2)	Normally White	-
Mass	195	g

- 1) Projection not included. Please refer to outline for details.
- 2) Due to the characteristics of the LCD material, the color varies with environmental temperature.



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4. Absolute maximum ratings

4-1. Electrical absolute maximum ratings

Item		Symbol	Min.	Max.	Unit
Supply voltage		V_{DD}	-0.3	4.5	V
Input signal voltage	1)	$V_{\rm IN}$	-0.3	4.50	V
LED forward current	2) 3)	IF	-	100	mA

- 1) Input signal: CK, R0~R5, G0~G5, B0~B5, H_{SYNC}, V_{SYNC}, ENAB, CM, SC
- 2) For each "AN-CA"
- 3) Do not apply reversed voltage.

4-2. Environmental absolute maximum ratings

Item		Symbol	Min.	Max.	Unit
Operating temperature	1)	T_{OP}	-20	70	$^{\circ}\mathrm{C}$
Storage temperature	2)	T_{STO}	-30	80	$^{\circ}\mathrm{C}$
Operating humidity	3)	Нор	10	4)	%RH
Storage humidity	3)	H_{STO}	10	4)	%RH
Vibration		-	5)	5)	-
Shock		-	6)	6)	-

- 1) Operating temperature means a temperature which operation shall be guaranteed. Since display performance is evaluated at 25°C, another temperature range should be confirmed.
- 2) Temp. = -30°C < 48h, Temp. = 80°C < 168hStore LCD at normal temperature/humidity. Keep them free from vibration and shock. An LCD that is kept at a low or a high temperature for a long time can be defective due to other conditions, even if the low or high temperature satisfies the standard. (Please refer to "Precautions for Use" for details.)
- 3) Non-condensing
- 4) Temp. ≤ 40°C, 85%RH Max.

Temp. > 40°C, Absolute humidity shall be less than 85%RH at 40°C.

5)

Frequency	10∼55 Hz	Acceleration value
Vibration width	0.15mm	$(0.3\sim 9 \text{ m/s}^2)$
Interval	10-55-10	Hz 1 minutes

2 hours in each direction X, Y, Z (6 hours total) EIAJ ED-2531

6) Acceleration: 490 m/s², Pulse width: 11 ms

3 times in each direction: $\pm X$, $\pm Y$, $\pm Z$

EIAJ ED-2531



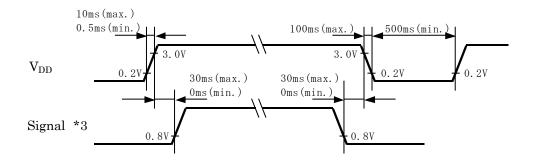
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5. Electrical characteristics

Temp. = $-20 \sim 70$ °C

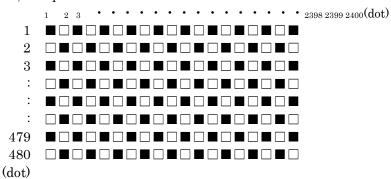
Item		Symbol	Condition	Min.	Тур.	Max.	Unit
Supply voltage 1)		$V_{ m DD}$	-	3.0	3.3	3.6	V
Current consumption		$I_{ m DD}$	2)	-	180	235	mA
Permissive input ripple voltage		V_{RP}	-	-	-	100	mVp-p
	0)	$ m V_{IL}$	"Low" level	0	•	0.8	V
I and almost and an	3)	V_{IH}	"High" level	2.0	-	$ m V_{DD}$	V
Input signal voltage		$ m V_{IL}$	"Low" level	0	-	$0.3~\mathrm{V_{DD}}$	V
	4)	V_{IH}	"High" level	$0.7~\mathrm{V_{DD}}$	-	$V_{ m DD}$	V

1) V_{DD} -turn-on conditions



2) Display pattern:

 $V_{\rm DD} = 3.3 \text{V}, \text{ Temp.} = 25 ^{\circ}\text{C}$



3) Input signal : CK, R0 \sim R5, G0 \sim G5, B0 \sim B5, H_{SYNC}, V_{SYNC}, ENAB, CM

4) Input signal: SC



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6. Optical characteristics

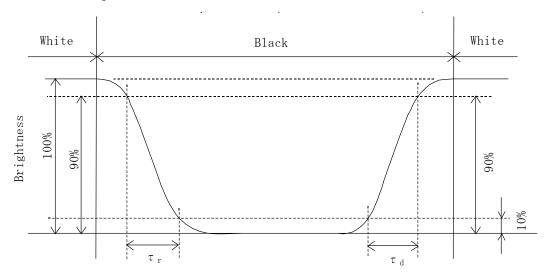
Measuring spot = ϕ 6.0mm, Temp. = 25°C

Item		Symbol	Condition	Min.	Тур.	Max.	Unit	
D	Rise	Τr	$\theta = \phi = 0$ °	-	5	-	ms	
Response time Down		τd	$\theta = \phi = 0$ °	-	25	-	ms	
		θ upper		-	60	-		
Viewing angle View direction	range	θ lower	CD > 10	-	80	-	\deg .	
: 12 o'clock (Gray inversion)		ϕ LEFT	CR≧10	-	80	-	1	
		ϕ RIGHT		-	80	-	deg.	
Contrast ratio		CR	$\theta = \phi = 0^{\circ}$	700	1000	-	-	
Brightness		L	IF=60mA/Line	490	700	-	cd/m²	
Luminance(Br	Luminance(Brightness)		-	70	-	-	%	
	Red	X	$\theta = \phi = 0^{\circ}$	0.550	0.600	0.650		
		У		0.300	0.350	0.400		
	Green	X	$\theta = \phi = 0^{\circ}$	0.280	0.330	0.380		
Chromaticity	Green	У	$\theta = \phi = 0$	0.525	0.575	0.625		
coordinates	Dl	X	$\theta = \phi = 0^{\circ}$	0.100	0.150	0.200	-	
	Blue	У	σ – φ –υ	0.070	0.120	0.170		
	White	X	$\theta = \phi = 0^{\circ}$	0.255	0.305	0.355	1	
	wnite	У	υ – φ –υ	0.280	0.330	0.380		

6-1. Definition of contrast ratio

 $\label{eq:cross-$

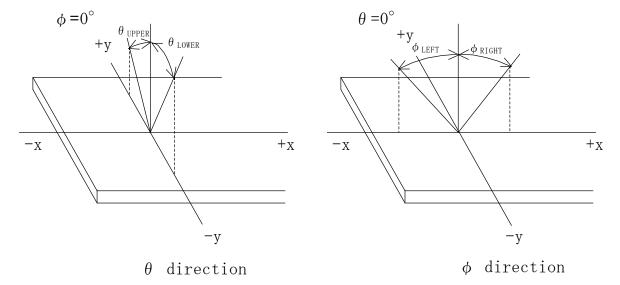
6-2. Definition of response time



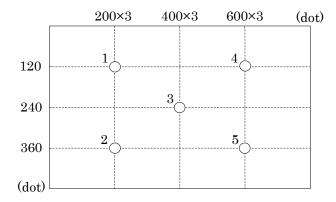


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6-3. Definition of viewing angle



6-4. Brightness measuring points



- 1) Rating is defined as the white brightness at center of display screen(3).
- 2) The brightness uniformity is calculated by using following formula.

Brightness uniformity =
$$\frac{\text{Minimum brightness from 1 to 5}}{\text{Maximum brightness from 1 to 5}} \times 100 \, [\%]$$

3) 5 minutes after LED is turned on. (Ambient Temp.=25°C)



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7. Interface signals

7-1. LCD

No.	Symbol	Description	Level
1	AN1	Anode1	
2	AN2	Anode2	
3	CA1	Cathode1	
4	CA2	Cathode2	
5	$V_{ m DD}$	3.3V power supply	
6	V_{DD}	3.3V power supply	
7	CM	Mode select signal (High or Open: Necessity of V·H _{SYNC} , GND: Uunecessity of V·H _{SYNC})	
8	ENAB	Data Enable (positive)	
9	Vsync	Vertical synchronous signal (negative)(fix low or high: when CM fixed to GND)	
10	${ m H}_{ m SYNC}$	Horizontal synchronous signal (negative) (fix low or high: when CM fixed to GND)	
11	GND	GND	
12	B5	BLUE data signal (MSB)	
13	B4	BLUE data signal	
14	В3	BLUE data signal	
15	GND	GND	
16	B2	BLUE data signal	
17	B1	BLUE data signal	
18	В0	BLUE data signal (LSB)	
19	GND	GND	
20	G5	GREEN data signal (MSB)	
21	G4	GREEN data signal	
22	G3	GREEN data signal	
23	GND	GND	
24	G2	GREEN data signal	
25	G1	GREEN data signal	
26	G0	GREEN data signal (LSB)	
27	GND	GND	
28	R5	RED data signal (MSB)	
29	R4	RED data signal	
30	R3	RED data signal	
31	GND	GND	
32	R2	RED data signal	
33	R1	RED data signal	
34	R0	RED data signal (LSB)	
35	SC	Scan direction control(GND or Open: Normal、High: Reverse)	1)
36	GND	GND	
37	GND	GND	
38	CK	Sampling clock	
39	GND	GND	
40	GND	GND	

LCD connector : IMSA-9681S-40A-GF (IRISO)

Recommended matching FFC or FPC : 0.5mm pitch



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1) Scanning

SC: GND or Open



SC: High





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8. Input timing characteristics

8-1-1. Timing characteristics

	Item	Symbol	Min.	Тур.	Max.	Unit	Note
	Frequency	Fck	29.88	33.2	36.52	MHz	
Clock	Period	Тс	27.4	30.1	33.5	ns	
(CK)	High time	Tch	12	-	-	ns	
	Low time	Tel	12	-	-	ns	
Data (R0~R5,G0~G5,	Set up time	Tds	5	-	-	ns	
B0~B5)	Hold time	Tdh	10	-	-	ns	
Data Enable	Set up time	Tes	5	-	-	ns	
(ENAB)	Hold time	Teh	10	-	-	ns	
	Set up time	Ths	5	-	-	ns	
	Hold time	Thh	10	-	-	ns	
Horizontal sync.	Period	Th -	944	1056	1088	Тс	
Signal			-	31.8	-	μs	
(H_{SYNC})	Pulse width	Thp	4	128	-	Тс	
	Front porch	Thf	-	40	-	Тс	
	Back porch	Thb	7	88	-	Тс	
Horizontal display	period	Thd		800		Тс	
	Daviad	Tv	516	525	534	Th	
Vertical sync.	Period	10	14.7	16.6	17.4	ms	
Signal	Pulse width	Tvp	1	2	-	Th	
$(V_{ m SYNC})$	Front porch	Tvf		11	-	Th	
	Back porch	Tvb	4	32	-	Th	
Vertical display per	riod	Tvd		480		Th	

- 1) If the display is used under the condition which is out of specifications such as higher clock frequency than specified value, there is a possibility phenomenon such as display error including white display, malfunction and no image may occur.
 - Please use the display under the conditions written in the specification.
- 2) In case of lower frequency, the deterioration of the display quality, flicker etc., may occur.
- 3) If CK is fixed to "H" or "L" level for certain period while ENAB is supplied, the panel may be damaged.
- 4) When dimming LED by PWM, please adjust LCD operating signal timing and LED driving frequency, to optimize the display quality. There is a possibility that flicker is observed by the interference of LCD operating signal timing and LED driving condition (especially driving frequency), even if the condition satisfies above timing specification.
- 5) Do not make Tv, Th, and Thp fluctuate.
- 6) CK count of each Horizontal Scanning Time should be always the same.

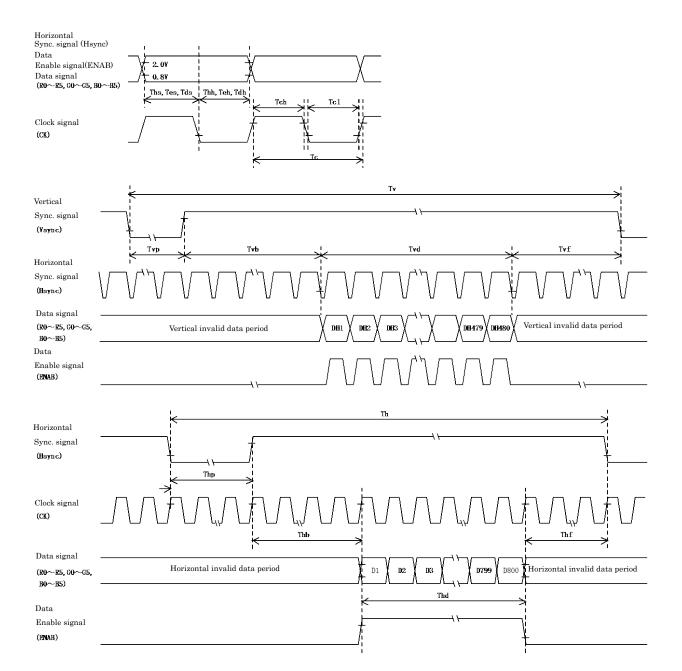
 Vertical invalid data period should be "n" X "Horizontal Scanning Time" . (n: integer)

 Frame period should be always the same.



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8-1-2. Input timing characteristics





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8-2. CM: GND (Uunecessity of V·H_{SYNC})

8-2-1. Timing characteristics

	Item	Symbol	Min.	Тур.	Max.	Unit	Note
	Frequency	Fck	29.88	33.2	36.52	MHz	
Clock	Period	Тс	27.4	30.1	33.5	ns	
(CK)	High time	Tch	12	-	-	ns	
	Low time	Tcl	12	-	-	ns	
Data (R0~R5,G0~G5,	Set up time	Tds	5	-	-	ns	
B0~B5)	Hold time	Tdh	10	-	-	ns	
	Set up time	Tes	5	-	-	ns	
	Hold time	Teh	10	-	-	ns	
	D : 1	Th	1024	1056	1088	Тс	
Enable	Period	ın	-	31.8	-	μs	
(ENAB)	Horizontal display period	Thd		800		Тс	
	Period	Tv	487	525	550	Th	
	reriod	1 V	14.7	16.6	17.4	ms	
	Vertical display period	Tvd		480		Th	

- 1) If the display is used under the condition which is out of specifications such as higher clock frequency than specified value, there is a possibility phenomenon such as display error including white display, malfunction and no image may occur.
 - Please use the display under the conditions written in the specification.
- 2) In case of lower frequency, the deterioration of the display quality, flicker etc., may occur.
- 3) If CK is fixed to "H" or "L" level for certain period while ENAB is supplied, the panel may be damaged.
- 4) When dimming LED by PWM, please adjust LCD operating signal timing and LED driving frequency, to optimize the display quality. There is a possibility that flicker is observed by the interference of LCD operating signal timing and LED driving condition (especially driving frequency), even if the condition satisfies above timing specification.
- 5) Do not make Tv, Th, and Thp fluctuate.
- 6) CK count of each Horizontal Scanning Time should be always the same.

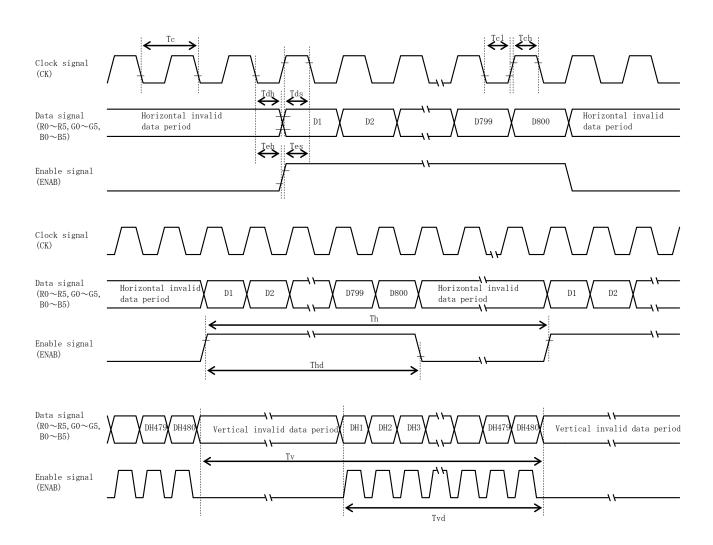
 Vertical invalid data period should be "n" X "Horizontal Scanning Time". (n: integer)

 Frame period should be always the same.

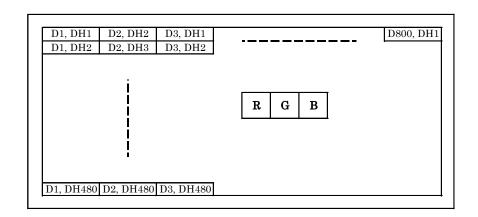


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8-2-2. Input timing characteristics



8-3. Input Data Signals and Display position on the screen





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9. Backlight characteristics

Item		Symbol	Min.	Тур.	Max.	Unit	Note
Forward current	1)	IF	-	60	-	mA	Ta=-20~70°C
			-	18.9	22.1	V	IF=60mA, Ta=-20°C
Forward voltage	1)	VF	-	18.0	21.2	V	IF=60mA, Ta=25℃
			-	17.4	20.7	V	IF=60mA, Ta=70℃
Operating life time	2), 3)	Т	-	50,000	-	h	IF=60mA, Ta=25℃

- 1) For each "AN-CA"
- 2) When brightness decrease 50% of minimum brightness.

 The average life of a LED will decrease when the LCD is operating at higher temperatures.
- 3) Life time is estimated data.(Condition : IF=60mA, Ta=25 $^{\circ}$ C in chamber).
- 4) An input current below 15mA may reduce the brightness uniformity of the LED backlight. This is because the amount of light from each LED chip is different. Therefore, please evaluate carefully before finalizing the input current.



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10. Lot number identification

The lot number shall be indicated on the back of the backlight case of each LCD.

TCG070WVLPAANN-AN50-SA
$$- \Box \Box - \Box \Box - \Box$$
 MADE IN $\Box \Box \Box \Box \Box$ $\downarrow \downarrow \downarrow \downarrow \downarrow$ \downarrow \downarrow \downarrow 5

No1. - No5. above indicate

- 1. Year code
- 2. Month code
- 3. Date
- 4. Version Number
- 5. Country of origin (Japan or China)

Year	2018	2019	2020	2021	2022	2023
Code	8	9	0	1	2	3

Month	Jan.	Feb.	Mar.	Apr.	May	Jun.
Code	1	2	3	4	5	6

Month	Jul.	Aug.	Sep.	Oct.	Nov.	Dec.
Code	7	8	9	X	Y	Z

11. Warranty

11-1. Incoming inspection

Please inspect the LCD within one month after your receipt.

11-2. Production warranty

Kyocera warrants its LCD's for a period of 12 months from the ship date. Kyocera shall, by mutual agreement, replace or re-work defective LCD's that are shown to be Kyocera's responsibility.



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12. Precautions for use

12-1. Installation of the LCD

- 1) A transparent protection plate shall be added to protect the LCD and its polarizer
- 2) The LCD shall be installed so that there is no pressure on the LSI chips.
- 3) The LCD shall be installed flat, without twisting or bending.
- 4) A transparent protection sheet is attached to the polarizer. Please remove the protection film slowly before use, paying attention to static electricity.

12-2. Static electricity

- 1) Since CMOS ICs are mounted directly onto the LCD glass, protection from static electricity is required.
- 2) Workers should use body grounding. Operator should wear ground straps.

12-3. LCD operation

1) The LCD shall be operated within the limits specified. Operation at values outside of these limits may shorten life, and/or harm display images.

12-4. Storage

- The LCD shall be stored within the temperature and humidity limits specified.
 Store in a dark area, and protect the LCD from direct sunlight or fluorescent light.
- 2) Always store the LCD so that it is free from external pressure onto it.

12-5. Usage

- 1) <u>DO NOT</u> store in a high humidity environment for extended periods. Polarizer degradation bubbles, and/or peeling off of the polarizer may result.
- 2) The front polarizer is easily scratched or damaged. Prevent touching it with any hard material, and from being pushed or rubbed.
- 3) The LCD screen may be cleaned by wiping the screen surface with a soft cloth or cotton pad using a little Ethanol.
- 4) Water may cause damage or discoloration of the polarizer. Clean condensation or moisture from any source immediately.
- 5) Always keep the LCD free from condensation during testing. Condensation may permanently spot or stain the polarizer.
- 6) Do not pull the LED lead wires and do not bend the root of the wires. Housing should be designed to protect LED lead wires from external stress.
- 7) Do not disassemble LCD because it will result in damage.
- 8) This Kyocera LCD has been specifically designed for use in general electronic devices, but not for use in a special environment such as usage in an active gas. Hence, when the LCD is supposed to be used in a special environment, evaluate the LCD thoroughly beforehand and do not expose the LCD to chemicals such as an active gas.
- 9) Please do not use solid-base image pattern for long hours because a temporary afterimage may appear. We recommend using screen saver etc. in cases where a solid-base image pattern must be used.
- 10) Liquid crystal may leak when the LCD is broken. Be careful not to let the fluid go into your eyes and mouth. In the case the fluid touches your body; rinse it off right away with water and soap.



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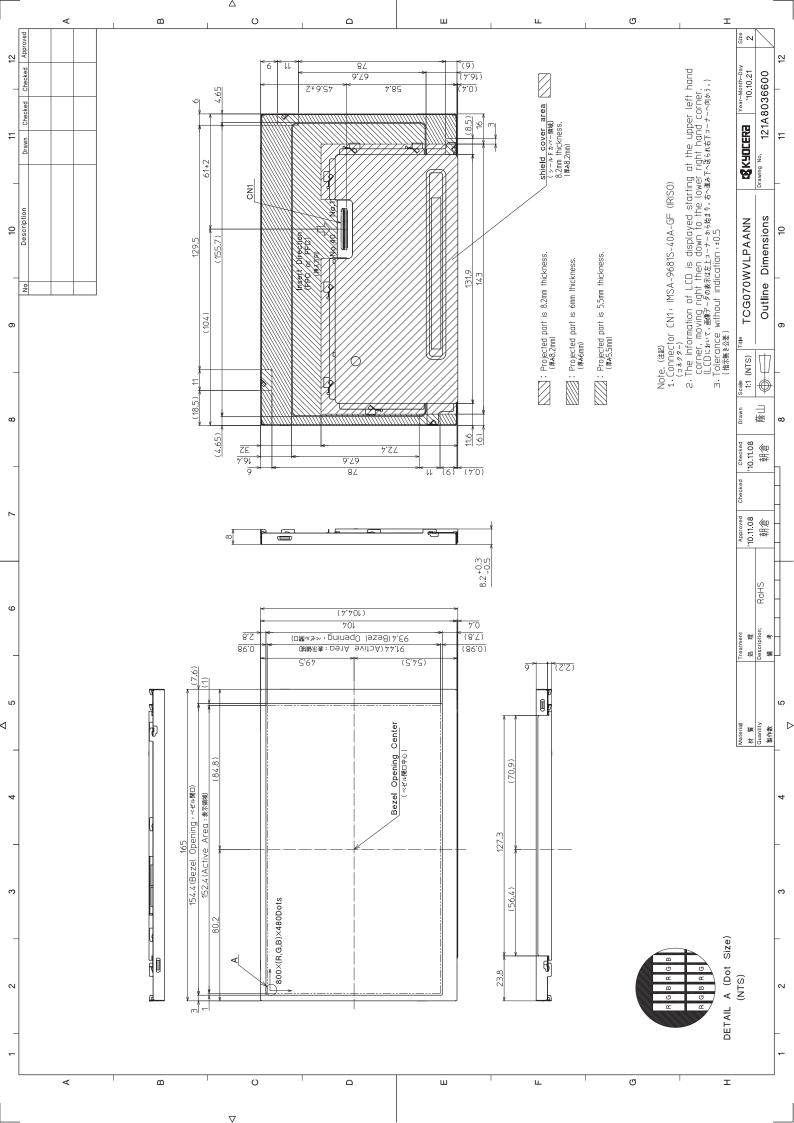
13. Reliability test data

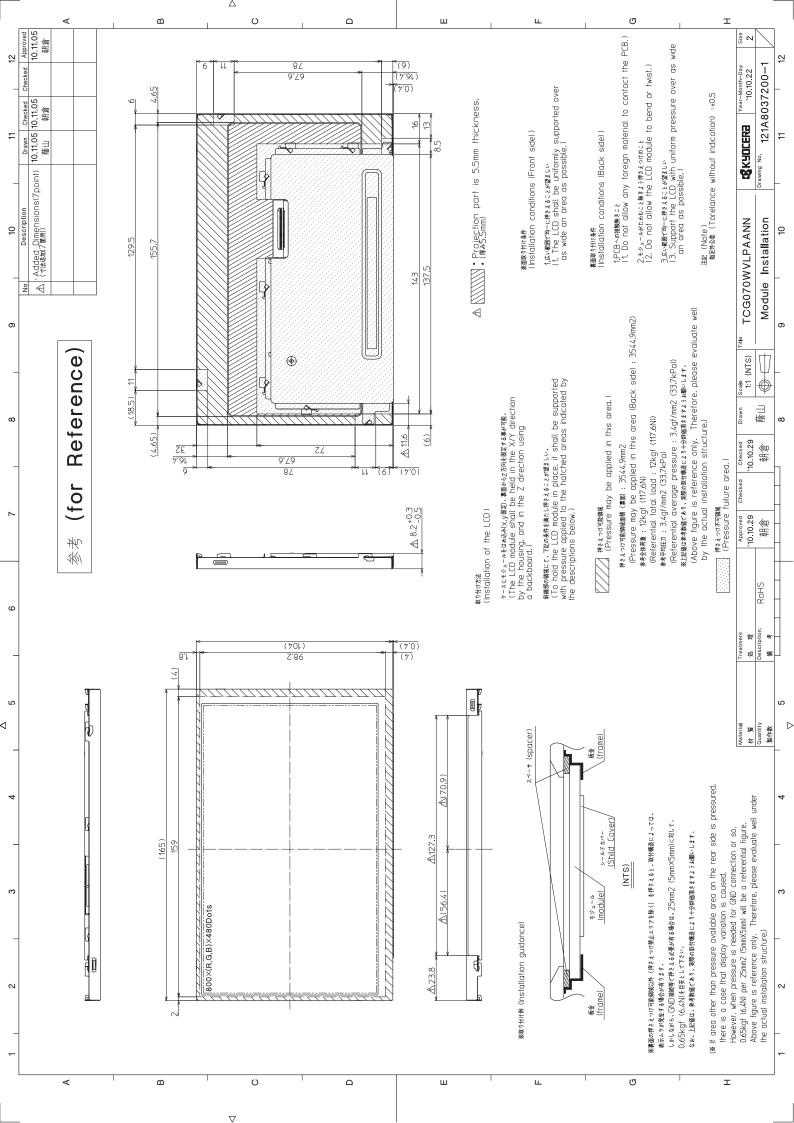
Test item	Test condition	Test time	Jud	gement
High temp. atmosphere	80°C	240h	Display function Display quality Current consumption	: No defect : No defect : No defect
Low temp. atmosphere	-30°C	240h	Display function Display quality Current consumption	: No defect : No defect : No defect
High temp. humidity atmosphere	40°C 90% RH	240h	Display function Display quality Current consumption	: No defect : No defect : No defect
Temp. cycle	-30°C 0.5h R.T. 0.5h 80°C 0.5h	10cycles	Display function Display quality Current consumption	: No defect : No defect : No defect
High temp. operation	70°C	500h	Display function Display quality Current consumption	: No defect : No defect : No defect

- 1) Each test item uses a test LCD only once. The tested LCD is not used in any other tests.
- 2) The LCD is tested in circumstances in which there is no condensation.
- 3) The reliability test is not an out-going inspection.
- 4) The result of the reliability test is for your reference purpose only.

 The reliability test is conducted only to examine the LCD's capability.







参考(for Reference)

IRISO 製 9681 シリーズコネクタの取り扱い上の注意 Precautions when using IRISO.9681 series connector

操作方法

使用上の注意点

FPC/FFC挿入方法 FPC/FFC insertion

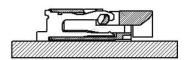
①カバー先端を上方向に上げて開けて下さい。(カバーは回転動作をします)

① pull up the cover tip to open up. (the cover will rotate to operate)

カバーの先端部分を親指や人差し指の爪により、矢印方向に眺ね上げる感じでロック解除を行って下さい。破損の原因となりますので、水平方向には押さないで下さい。

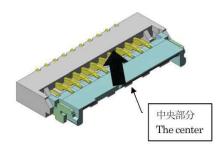
To release the lock, flip the lock to a direction of arrow with the nail of pointer or thumb.

Please Don't push the cover horizontally; it causes damage.



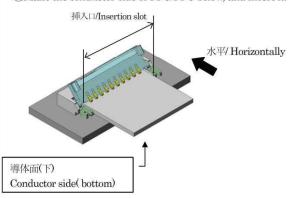
補足 addition

カバー中央部分を上方向へ跳ね上げてロック解除を行って下さい。 Flip the center part of cover to release the lock.



②FPC/FFC の導体面を下にして挿入して下さい。

②Make the conductor side of FPC/FFC below, and insert it.



補足 addition

FPC/FFC の挿入は、カバーを 130° 開いた状態で、挿入口に対して水平になる様、挿入して下さい。カバーが倒れない様、手で軽く支えますとより挿入し易くなります。

To insert a FPC/FFC, open the cover in 130° , and insert the FPC/FFC horizontally to an insertion slot.

Supporting the cover lightly by hand will be the way to insert easily.

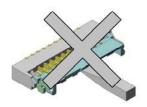
9681シリーズは、小型・薄型である為、強度は強くありませんので、取り扱いには十分注意して下さい。

Please handle with fragile care.

9681 series are small and thin, so the strength are little short. 作業の際は、手袋及びアースバンドを着用して下さい。

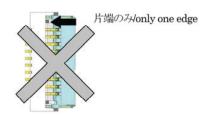
Please wear gloves and a ground belt when the time of the work. ロック解除の際に、ドライバー等先端が細く硬い工具を使用しての操作は行わないで下さい。変形・破損する事があります。

In case of releasing the lock, please don't use hard tools with thin tip, like a driver. It can be deformed and damaged.



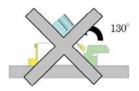
ロック解除時、カバー片端(左 or 右)のみに力を加えてロック解除を行わないで下さい。変形・破損する事があります。

In case of releasing the lock, please don't make a force on the one edge of cover. It can be deformed and damaged.



カバーは 130°以上開かない構造の為、更に後ろへ強い力を加えないで下さい。変形・破損する事があります。

The cover is structured not to open more than 130° , so please don't add a strong force backward. It can be deformed and damaged.



FPC/FFCは、挿入口に正しく挿入して下さい。斜め挿入等、正しく挿入されていない場合は、導通不良の原因となります。

Please insert FPC/FFC in insertion slot properly. If it's not inserted properly, like leaned insertion, it will cause a bad connection.

FPC/FFCは、弊社推奨サイズを使用して下さい。弊社推奨サイズ以外を使用した場合は品質保証出来ません。

Please use our preferred size of FPC/FFC. We can not certify the quality except using our recommended size of FPC/FFC.

操作方法

FPCのロック方法

The method to lock the FPC

①カバーを回転させてロックして下さい。

①Turn down the cover to lock it.



補足/addition

ロック後、カバー両端を軽く押すと、カバーの半ロックを防止できます。

After locking, to push the both edge of cover with light force can prevent a half lock

開閉作業の際は、コンタクトに触れないで下さい。変形による接触 不良の原因となります。

Please don't touch the contact while opening and shutting the cover. It causes bad connection by deformed contact.

使用上の注意点

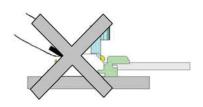
ロック操作の際に下図の矢印方向に強い力を加えてカバーを押さないで下さい。変形・破損の原因となります。

In case of lock operation, please don't push the cover strongly to the direction of arrow. It causes deformation and damage.

水平方向に押す /Pushing in a horizontal direction



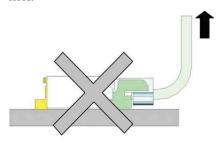
根元を押す /Pushing the base



その他/Others

コネクタの構造上、上方向への引張強度は強くありませんので、上方向へ強い力を加えないで下さい。使用上、FPC/FFC に引張力が加わる場合は、上方向の力がコネクタに加わらない様、FPC/FFCをテープ等で固定して下さい。

As a structure of connector, the strength to upper direction is little short. So please don't make a force in above direction. In case of necessary to draw a FPC/FFC out, Please fix the FPC/FFC with a tape to protect the connector from an upper force.



カバーをロックした状態で、FPC/FFC に引張力を加えないで下さい。FPC/FFC 導体面の削れ、及び半挿入状態による導通不良の原因となります。

Please don't draw the FPC/FFC out while the cover is locked. It causes scraping the conductor surface and bad connection by half insertion.

Spec No.	TQ3C-8EAF0-E2YAA240-00
Date	January 31, 2018

KYOCERA INSPECTION STANDARD

TYPE: TCG070WVLPAANN-AN50-SA

KYOCERA DISPLAY CORPORATION

Original	Designed by:	Engineering de	Confirmed by : QA dept.		
Issue Date	Prepared	Checked	Approved	Checked	Approved
January 31, 2018	S. Mori	K.Edo	G Matsumoto	H.Sumi	K. Siyeme



Ş	Spec No.	Part No.	Page
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Revision record

Designed by : Engineering dept. Confirmed by : QA	Confirmed by : QA dept.		
Date Designed by Engineering dept. Commined by QA	oroved		
Rev.No. Date Page Descriptions			



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Visuals specification

1) Note

1) Note		Note				
General	1. Custom	Note mer identified anomalies not defined within this inspection standard shall be				
General		ner identified anomalies not defined within this inspection standard shall be ed by Kyocera, and an additional standard shall be determined by mutual consent.				
			•			
		This inspection standard about the image quality shall be applied to any defect within the				
		effective viewing area and shall not be applicable to outside of the area.				
	3. Inspection conditions					
	Lumina		: 500 Lux min.			
	Inspection distance Temperature		: 300 mm. : 25 ± 5℃			
	Direction		: Directly above			
Definition of	Dot defect	Bright dot defect	The dot is constantly "on" when power applied to the			
inspection			LCD, even when all "Black" data sent to the screen.			
item			Inspection tool: 5% Transparency neutral density filter.			
			Count dot: If the dot is visible through the filter.			
			Don't count dot: If the dot is not visible through the			
			filter.			
			RGBRGBRGB There is an electrode in the middle of the dot			
			RGBRGB and one dot is shown in the left drawing.			
			R G B R G B R G G S < dot drawing>			
		Black dot defect	The dot is constantly "off" when power applied to the			
			LCD, even when all "White" data sent to the screen.			
			Similar size compared to bright dot.			
		White dot	Pixel works electrically, however, circular/foreign			
		(Circular/foreign	particle makes dot appear to be "on" even when all			
		particle)	"Black" data is sent to the screen.			
		Adjacent dot	Adjacent dot defect is defined as two or more bright dot			
			defects or black dot defects.			
			RGBRGBRGB			
			RGBRGBRGB			
			R G B R G B			
			Waldidalidal			
	External	Bubble, Scratch,	Visible operating (all pixels "Black" or "White") and non			
	inspection	Foreign particle	operating.			
		(Polarizer, Cell, Backlight)				
		Appearance inspection	Does not satisfy the value at the spec.			
	Others	CFL wires	Damaged to the CFL wires, connector, pin, functional			
			failure or appearance failure.			
	Definition	Definition of circle size Definition of linear size				
	of size					
	01 5120					
		d = (a + b)	0)/2			



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2) Standard

2) Standard								
Classification Inspection item		Judgement standard						
Defect	efect Dot Bright dot defect		Acceptable number : 4		: 4			
(in LCD defect				Bright dot spacing : 5 mm		: 5 mm	n or more	
glass)		Black dot defect		Acceptable number : 5				
				Black dot spacing	Black dot spacing : 5		5 mm or more	
		2 dot join Bright dot defect		Acceptable number : 2		: 2		
			Black dot defect	Acceptable number		:3		
		3 or more dots join		Acceptable number : 0				
		Total dot d	lefects	Acceptable number : 5 Max			x	
	Others	White dot,	Dark dot					
		(Circle)		Size (mm)		Acceptable number		
				d ≦	0.2	(Neglected)		
				0.2 < d ≦		5		
				0.4 < d ≦	0.5		3	
				0.5 < d			0	
	inspection	Polarizer (Scratch)			. 1		
(Defect or				Width (mm)	Length (1	mm)	Acceptable number	
Polarizer				W ≦ 0.1		- - 0	(Neglected)	
between I				$0.1 < W \le 0.3$	$\begin{array}{c c} L \leq 5.0 \\ \hline 5.0 < L \end{array}$		(Neglected)	
and LCD	glass)			0.3 < W	5.0 < L		0	
		Dolowigon ((Dubble)	0.0 (11			0	
		Polarizer (Bubble)		Size (mm)		Acceptable number		
				$d \leq 0.2$		(Neglected)		
				$0.2 < d \le 0.3$		5		
				$0.3 < d \le 0.5$		3		
				0.5 < d		0		
		Foreign pa	rticle					
		(Circular shape)		Size (mm)		Acceptable number		
				d ≤ 0.2		(Neglected)		
				$0.2 < d \le 0.4$		5		
				$0.4 < d \le 0.5$		3		
				0.5 < d			0	
		Foreign pa	ırticle					
		(Linear shape)		Width (mm)	Length	(mm)	Acceptable number	
		Scratch		W ≤ 0.03			(Neglected)	
					$\begin{array}{c cccc} & L \leq 2.0 \\ \hline 2.0 < L \leq 4.0 \\ \hline \end{array}$		(Neglected)	
				$0.03 < W \le 0.1$			3	
				0.1 ()	4.0 < L		0	
				0.1 < W	_		(According to	
							circular shape)	
		Color varia	ation	Not to be significantly	visible.			
		(Mura)		Consultation shall be	held as nece	ssary.		

