

# PRODUCT SPECIFICATION

## 10.1" TFT LCD MODULE

**MODEL: T101128800-A8WMN-001 Ver: 1.1**

**ROHS**

< ◇ > Preliminary Specification

< ◆ > Finally Specification

<b>CUSTOMER'S APPROVAL</b>	
<b>CUSTOMER :</b>	
<b>SIGNATURE:</b>	<b>DATE:</b>

<b>APPROVED BY</b>	<b>PM REVIEWED</b>	<b>PD REVIEWED</b>	<b>PREPARED BY</b>

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### Revision History

Revision	Date	Originator	Detail	Remarks
1.0	2016.12.26	ZFY	Initial Release	
1.1	2018.04.23	ZFY	Modify many details	P23/P24

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## 1. General Description

The specification is a transmissive type color active matrix liquid crystal display (LCD) which uses amorphous thin film transistor (TFT) as switching devices. This product is composed of a TFT-LCD panel, driver ICs and a backlight unit.

## 2. Module Parameter

Features	Details	Unit
Display Size(Diagonal)	10.1"	
LCD type	IPS TFT	
Display Mode	Transmissive/ Normally Black	
Resolution	1280 RGB x 800	Pixels
View Direction	FULL VIEWING	Best Image
Module Outline	228.21 (H) x 148.86(V) x 2.39(T) (Note1 )	mm
Active Area	216.96 (H) x 135.6(V)	mm
Pixel Size	169.5 (H) x 169.5(V)	um
Pixel Arrangement	Pixels RGB stripe arrangement	
Surface treatment	Anti-glare	
Display Colors	16.7M	
Interface	8 bits-LVDS interface	
With or Without Touch Panel	Without	
Operating Temperature	<b>-20~65</b>	°C
Storage Temperature	<b>-40~85</b>	°C
Weight	(145)	g

Note 1: Exclusive hooks, posts , FFC/FPC tail etc.

## 3. Absolute Maximum Ratings

$V_{SS}=0V, T_a=25^{\circ}C$

Item	Symbol	Min.	Max.	Unit
Supply Voltage	VDD	3	3.6	V
Storage temperature	$T_{STG}$	-40	+85	°C
Operating temperature	$T_{OP}$	-20	+65	°C

Note 1: If  $T_a$  below  $50^{\circ}C$ , the maximal humidity is 90%RH, if  $T_a$  over  $50^{\circ}C$ , absolute humidity should be less than 60%RH.

Note 2: The response time will be extremely slow when the operating temperature is around  $-10^{\circ}C$ , and the back ground will become darker at high temperature operating.

#### 4. DC Characteristics

Item	Symbol	Min.	Typ.	Max.	Unit
Supply Voltage	VDD	3.0	3.3	3.6	V
Power supply current	IDD	-	-	260	mA
Positive-going Input Threshold Voltage	VIH	-	-	100	mV
Negative-going Input Threshold Voltage	VIL	-100	-	-	mV
Differential input common mode voltage	Vcom	-	1.2	-	V

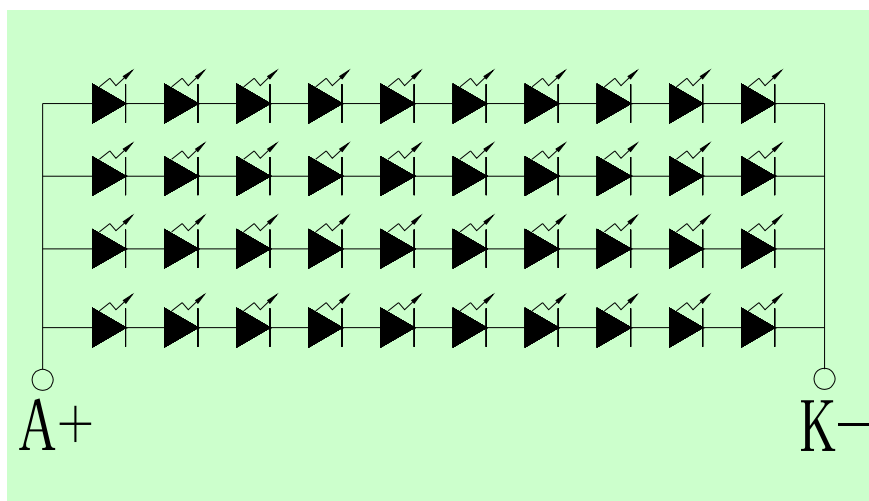
#### 5. Backlight Characteristic

##### 5.1. Backlight Characteristic

Item	Symbol	Condition	Min	Typ	Max	Unit
Forward Voltage	V <sub>F</sub>	T <sub>a</sub> =25 °C, I <sub>F</sub> =20mA/LED	-	-	31	V
Forward Current	I <sub>F</sub>	T <sub>a</sub> =25 °C	-	17.2*4	20*4	mA
Power dissipation	P <sub>D</sub>				2480	mW
Uniformity	Avg		-	80	-	%
LED working life(25°C)	-	T <sub>a</sub> =25 °C, I <sub>F</sub> =20mA/LED	15000	-	-	Hrs
Drive method	Constant current					
LED Configuration	40 White LEDs(10 LEDs in one string and 4 groups in parallel)					

Note: LED life time defined as follows: The final brightness is at 50% of original brightness.  
The environmental conducted under ambient air flow, at T<sub>a</sub>=25±2 °C,60%RH±5%, I<sub>F</sub>=20mA.

##### 5.2. Backlighting circuit



## 6. Optical Characteristics

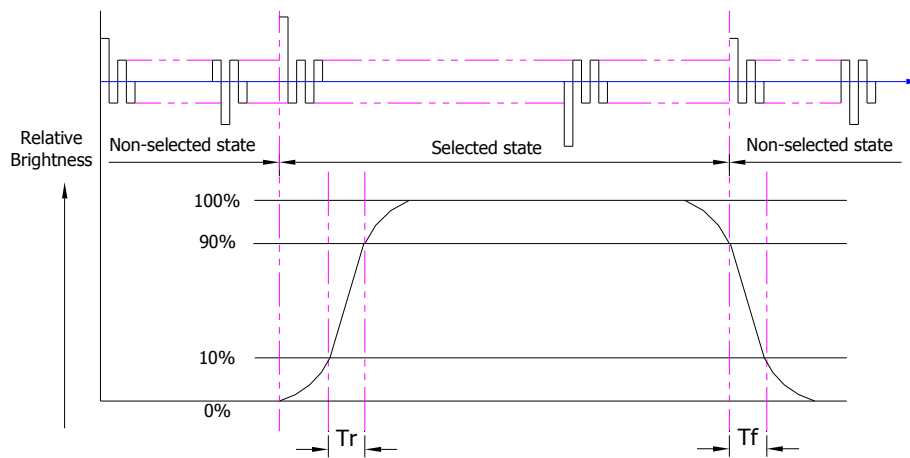
### 6.1. Optical Characteristics

Ta=25°C, V<sub>DD</sub> =3.3V

	Item	Symbol	Condition	Specification			Unit
				Min.	Typ.	Max.	
Backlight On (Transmissive Mode)	Luminance on TFT(I <sub>f</sub> =20mA/LED)	Lv	Normally viewing angle θx = φY =0°	360	450	-	cd/m <sup>2</sup>
	Contrast ratio(See 6.3)	CR		600	800	-	
	Response time (See 6.2)	TR+TF		-	30	-	ms
	Chromaticity Transmissive (See 6.5)	Red	X <sub>R</sub>		TBD		-
			Y <sub>R</sub>		TBD		
		Green	X <sub>G</sub>		TBD		
			Y <sub>G</sub>		TBD		
		Blue	X <sub>B</sub>		TBD		
			Y <sub>B</sub>		TBD		
	White	X <sub>W</sub>		TBD			
Y <sub>W</sub>			TBD				
Viewing Angle (See 6.4)	Horizontal	θx+	Center CR≥10		80	-	Deg.
		θx-			80	-	
	Vertical	φY+			80	-	
		φY-			80	-	

### 6.2. Definition of Response Time

#### 6.2.1 Normally Black Type (Negative)

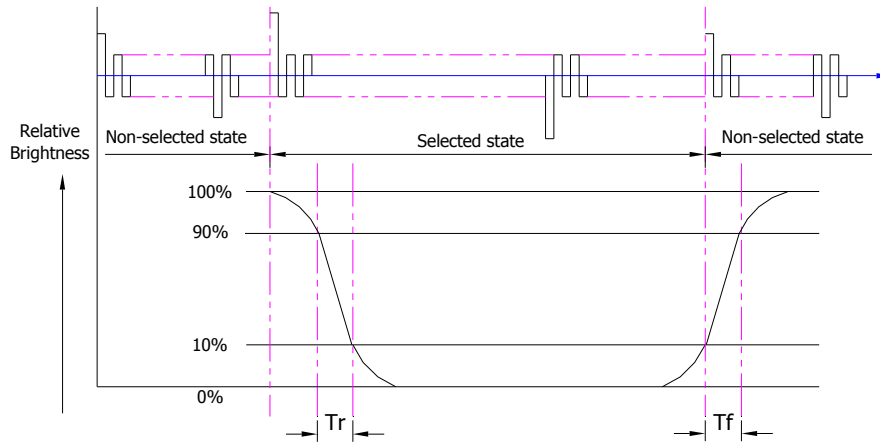


Tr is the time it takes to change from non-selected stage with relative luminance 10% to selected state with relative luminance 90%;

Tf is the time it takes to change from selected state with relative luminance 90% to non-selected state with relative luminance 10%.

Note : Measuring machine: LCD-5100

### 6.2.2 Normally White Type (Positive)



Tr is the time it takes to change from non-selected stage with relative luminance 90% to selected state with relative luminance 10%;

Tf is the time it takes to change from selected state with relative luminance 10% to non-selected state with relative luminance 90%;

Note : Measuring machine: LCD-5100 or EQUI

### 6.3. Definition of Contrast Ratio

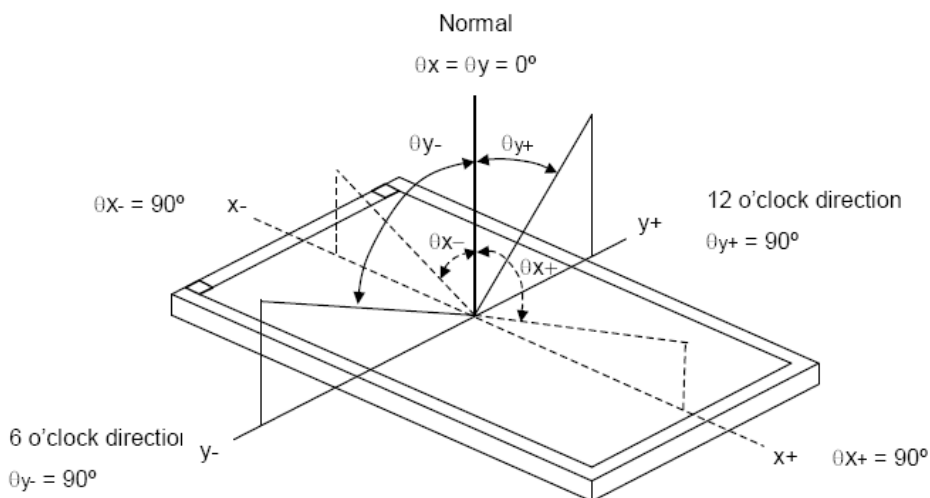
Contrast is measured perpendicular to display surface in reflective and transmissive mode.

The measurement condition is:

Measuring Equipment	Eldim or Equivalent
Measuring Point Diameter	3mm//1mm
Measuring Point Location	Active Area centre point
Test pattern	A: All Pixels white
	B: All Pixel black
Contrast setting	Maximum

Definitions: CR (Contrast) = Luminance of White Pixel / Luminance of Black Pixel

### 6.4. Definition of Viewing Angles



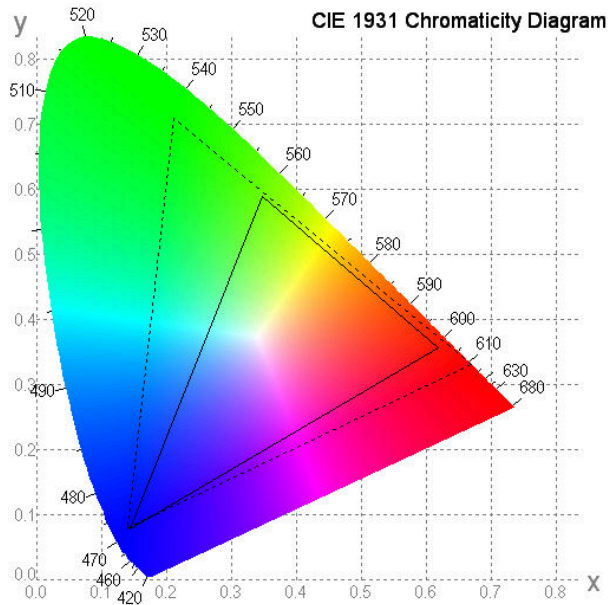
Measuring machine: LCD-5100 or EQUI

### 6.5. Definition of Color Appearance

R,G,B and W are defined by (x, y) on the IE chromaticity diagram

NTSC=area of RGB triangle/area of NTSC triangleX100%

Measuring picture: Red, Green, Blue and White (Measuring machine: BM-7)



### 6.6. Definition of Surface Luminance, Uniformity and Transmittance

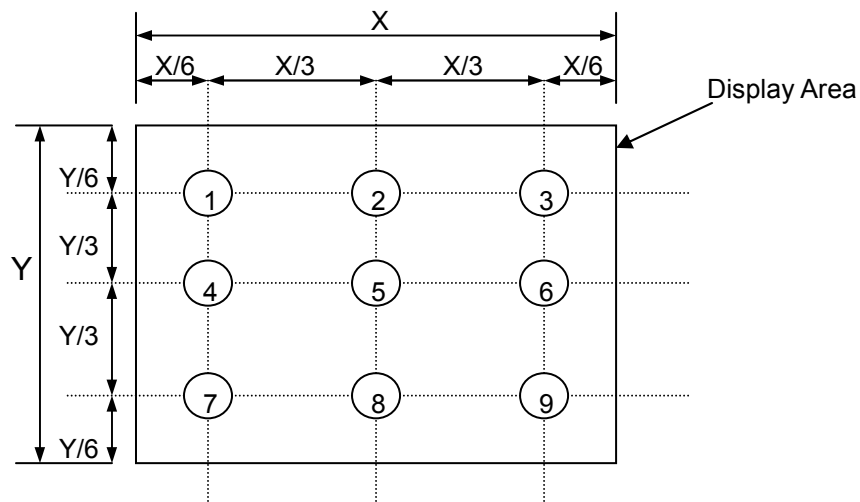
Using the transmissive mode measurement approach, measure the white screen luminance of the display panel and backlight.

6.6.1 Surface Luminance:  $L_V = \text{average} (L_{P1}:L_{P9})$

6.6.2 Uniformity =  $\text{Minimal} (L_{P1}:L_{P9}) / \text{Maximal} (L_{P1}:L_{P9}) * 100\%$

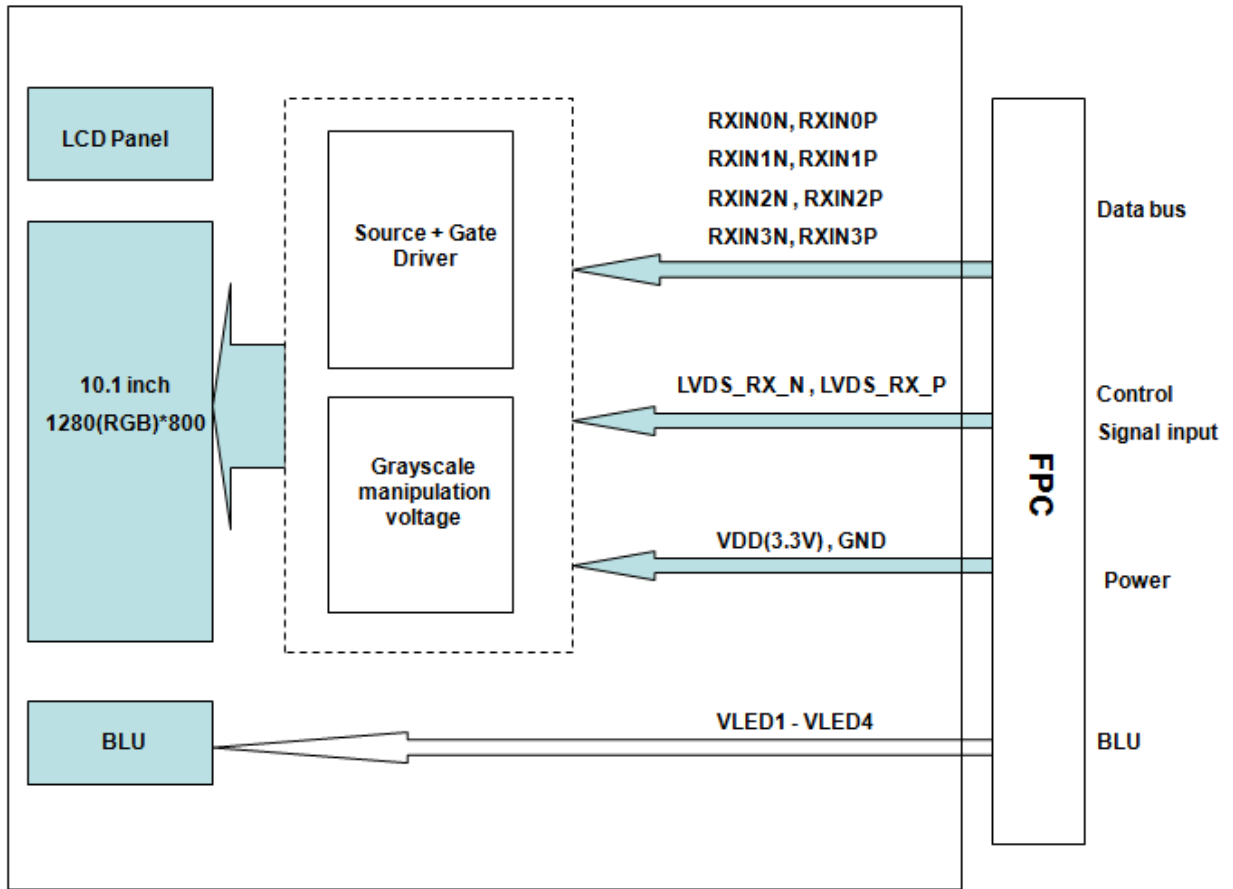
6.6.3 Transmittance =  $L_V \text{ on LCD} / L_V \text{ on Backlight} * 100\%$

Note : Measuring machine: BM-7





## 7. Block Diagram and Power Supply



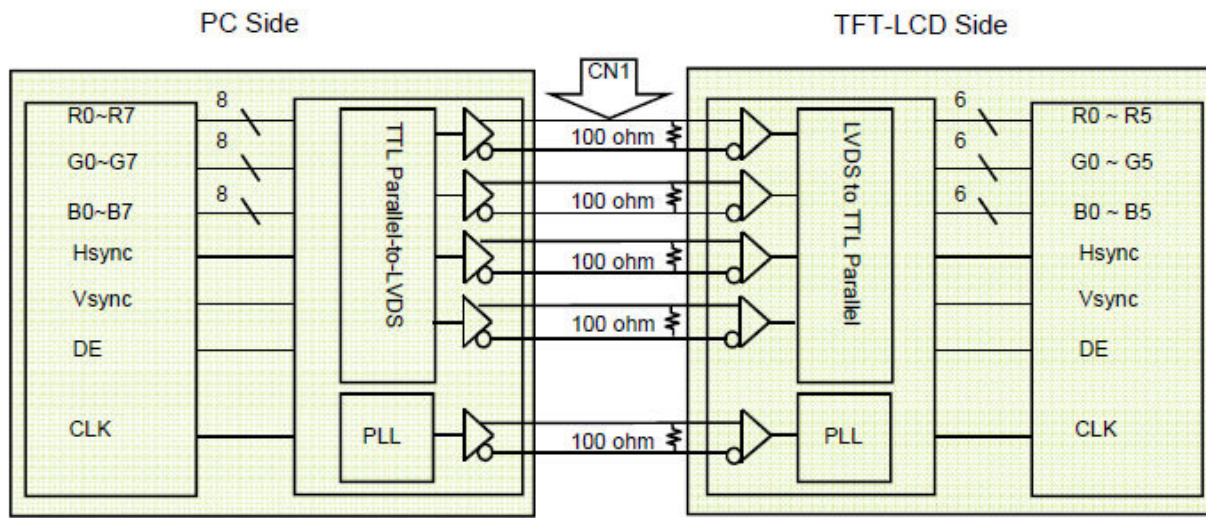
## 8. Interface Pins Definition

Connector model:PF030-B45B-N09

No.	Symbol	Function	Remark
1	GND1	Ground	
2	GND2	Ground	
3	NC1	No connection	
4	3.3V	Logic power 3.3V	
5	3.3V	Logic power 3.3V	
6	3.3V	Logic power 3.3V	
7	3.3V	Logic power 3.3V	
8	3.3V	Logic power 3.3V	
9	WPN	No connection	
10	SCL	No connection	
11	SDA	No connection	
12	GND3	Ground	
13	GND4	Ground	
14	GND5	Ground	
15	RXin3N	-LVDS differential data, (R6,R7,G6,G6,B6,B7)	
16	RXin3P	+LVDS differential data, (R6,R7,G6,G6,B6,B7)	
17	GND6	Ground	
18	LVDS_RX_N	- LVDS differential clock input	
19	LVDS_RX_P	+ LVDS differential clock input	
20	GND7	Ground	
21	RXin2N	-LVDS differential data, (B2-B5, HS, VS, DE)	
22	RXin2P	+LVDS differential data, (B2-B5, HS, VS, DE)	
23	GND8	Ground	
24	RXin1N	-LVDS differential data, (G1-G5, B0-B1)	
25	RXin1P	+LVDS differential data, (G1-G5, B0-B1)	
26	GND9	Ground	
27	RXin0N	-LVDS differential data, (R0-R5, G0)	
28	RXin0P	+LVDS differential data, (R0-R5, G0)	
29	GND10	Ground	
30	GND11	Ground	
31	NC2	No connection	
32	FB1	LED FB1	
33	FB2	LED FB2	
34	FB3	LED FB3	
35	FB4	LED FB4	
36	FB5	No connection	
37	FB6	No connection	
38	NC3	No connection	
39	VLED1	LED Power supply Voltage	

40	VLED2	LED Power supply Voltage	
41	VLED3	LED Power supply Voltage	
42	VLED4	LED Power supply Voltage	
43	VLED5	LED Power supply Voltage	
44	NC4	No connection	
45	GND12	Ground	

Note1: LVDS Interface

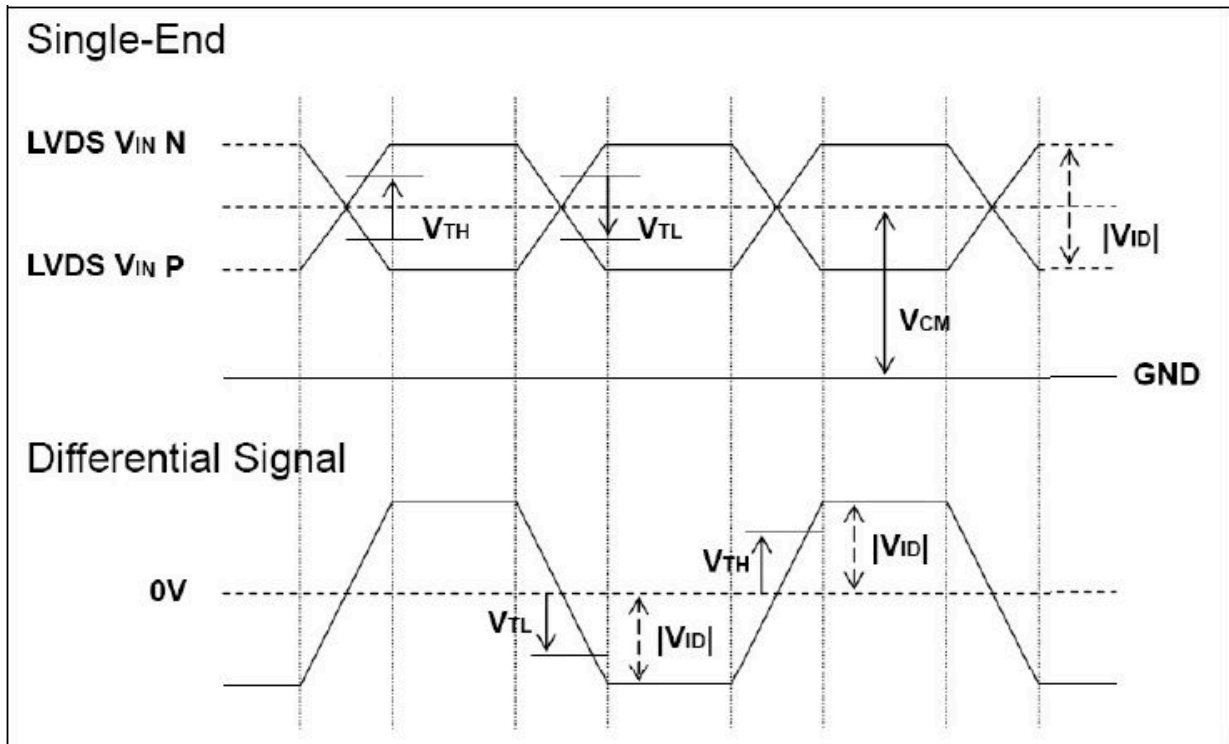


Note2: LED connector

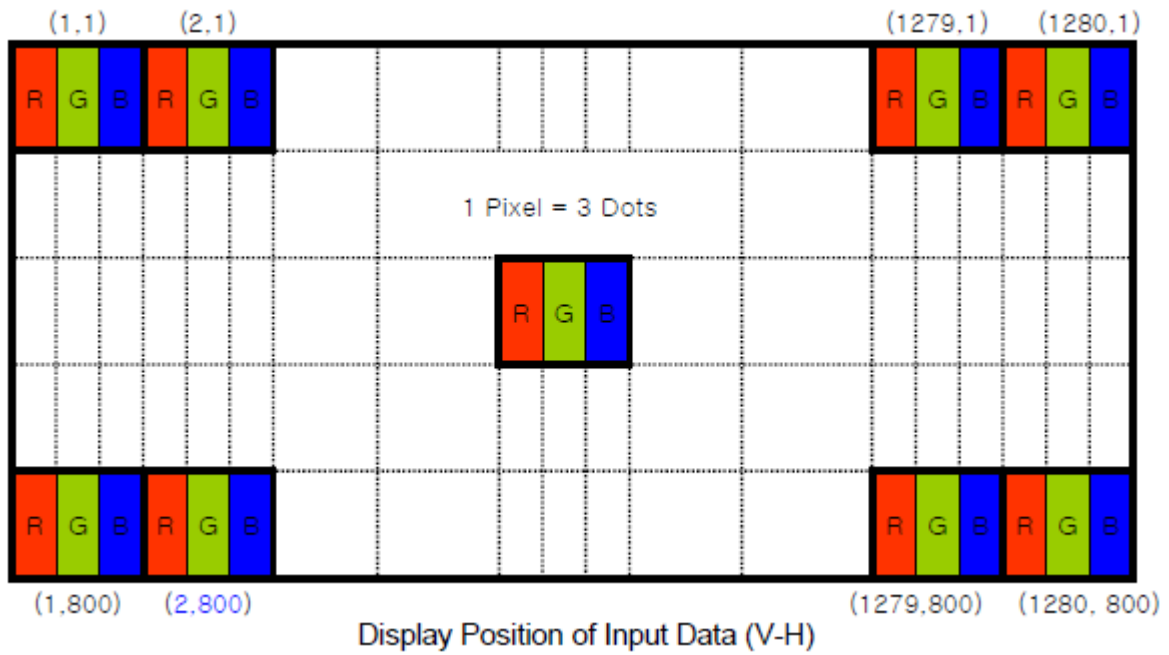
Pin No.	Symbol	For Signal Connector
1	VLEDN1	LED Cathode Power Supply
2	VLEDN2	LED Cathode Power Supply
3	VLEDN3	LED Cathode Power Supply
4	VLEDN4	LED Cathode Power Supply
5	NC	No Connection
6	NC	No Connection
7	VLED	LED Anode Power Supply
8	VLED	LED Anode Power Supply
9	VLED	LED Anode Power Supply

## 9. Timing Characteristics

### 1) LVDS Input signal

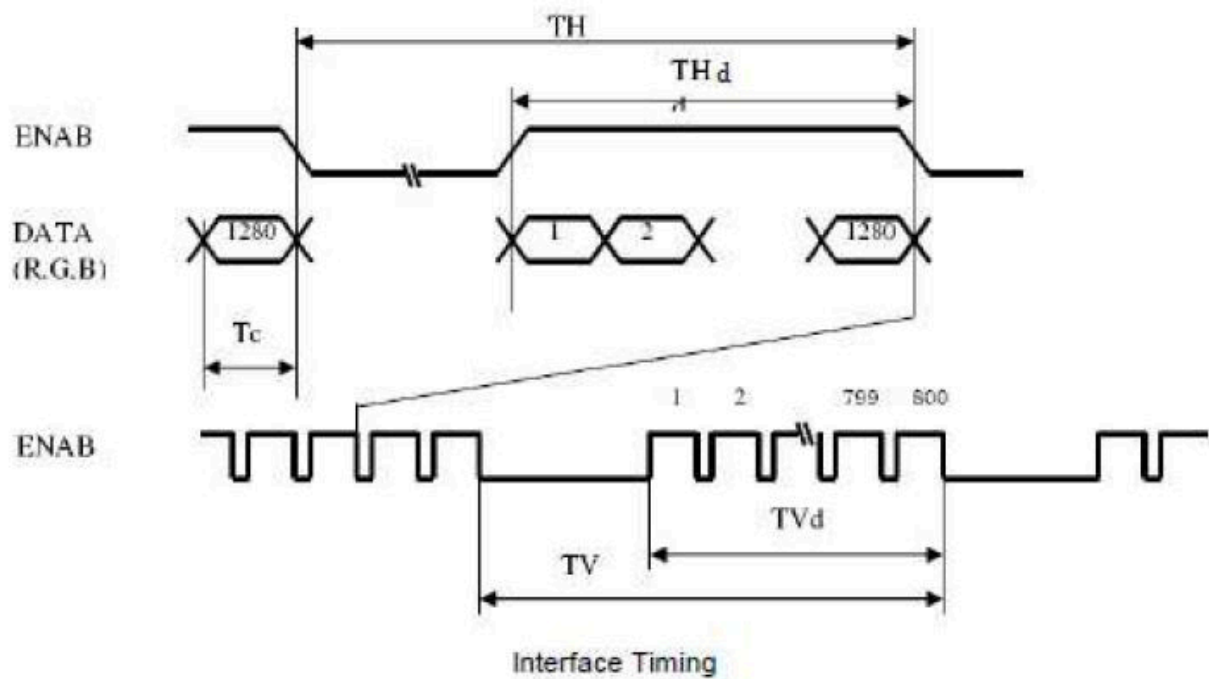


### 2) Data input format

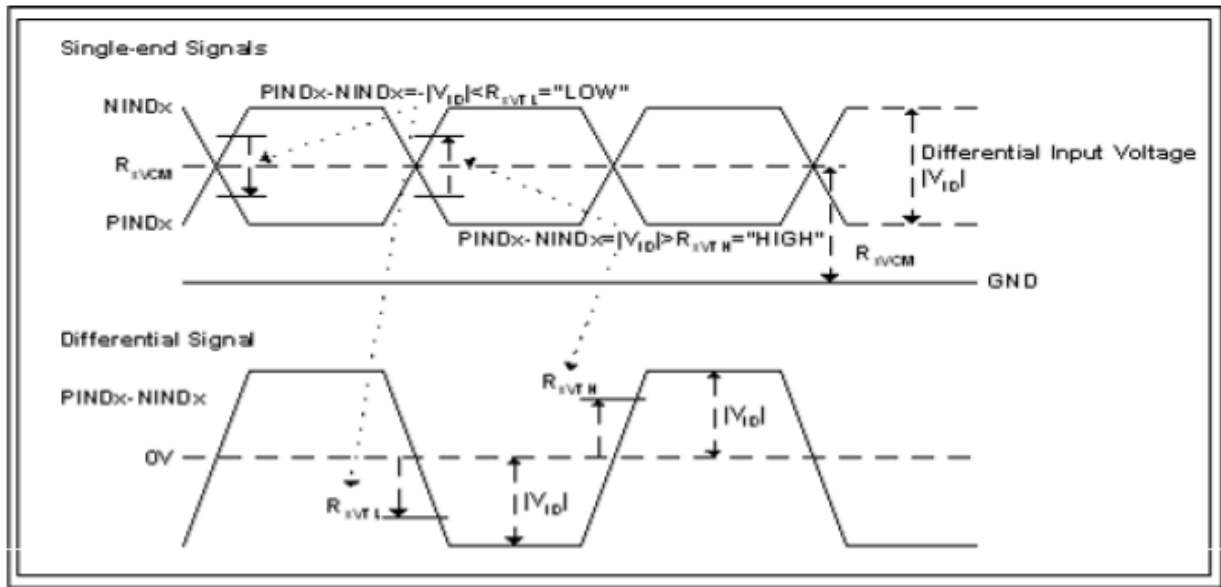


3) TIMING SPECIFICATION (DE mode)

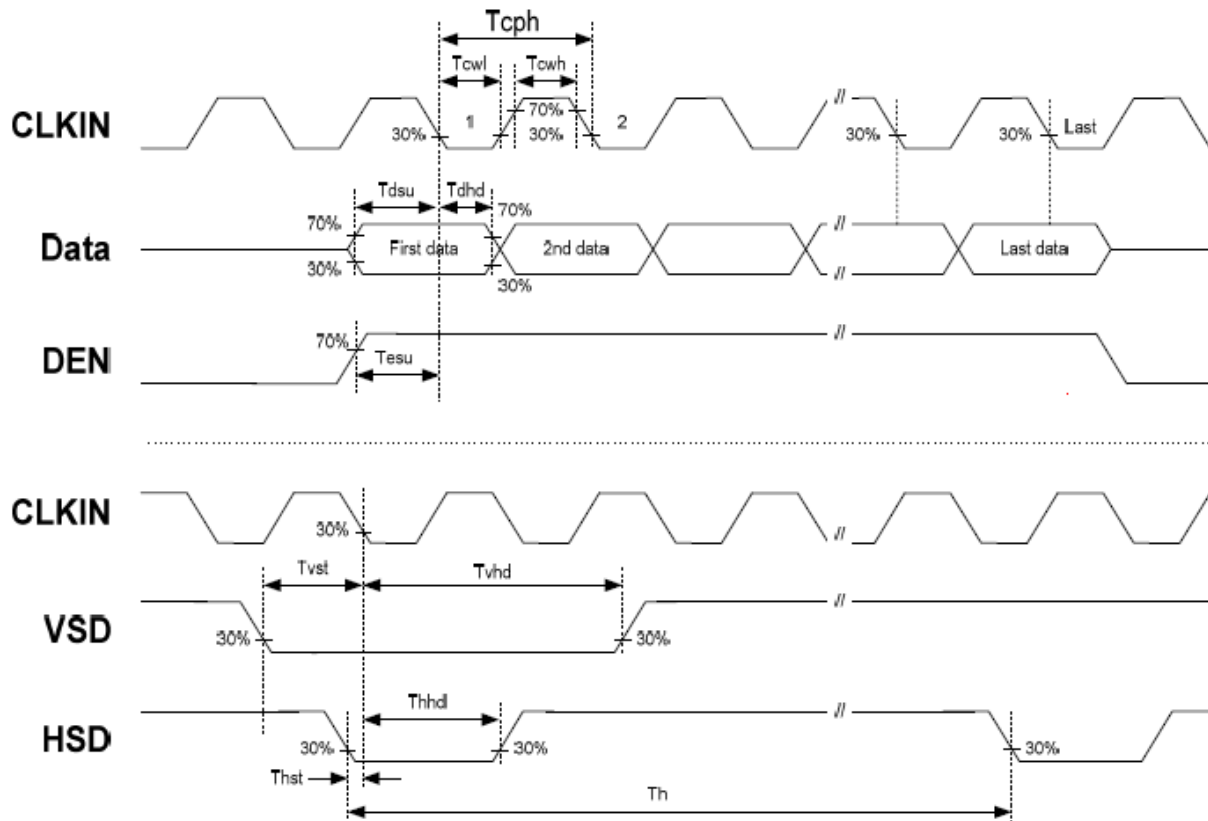
Signal	Item	Symbol	Min	Type	Max	Unit
DCLK	Frequency	1/TC	60	65	80	MHz
	Cycle	Tc	16.66	15.38	12.5	ns
DE	Horizontal Period	THd	1280	1280	1280	Tc
	Horizontal Cycle	TH	1310	1330	1560	Tc
		TH_time	19.5	20.46	21.83	ns
	Vertical Period	TVd	800	800	800	Tc
	Vertical Cycle	TV	-	812	-	Tc



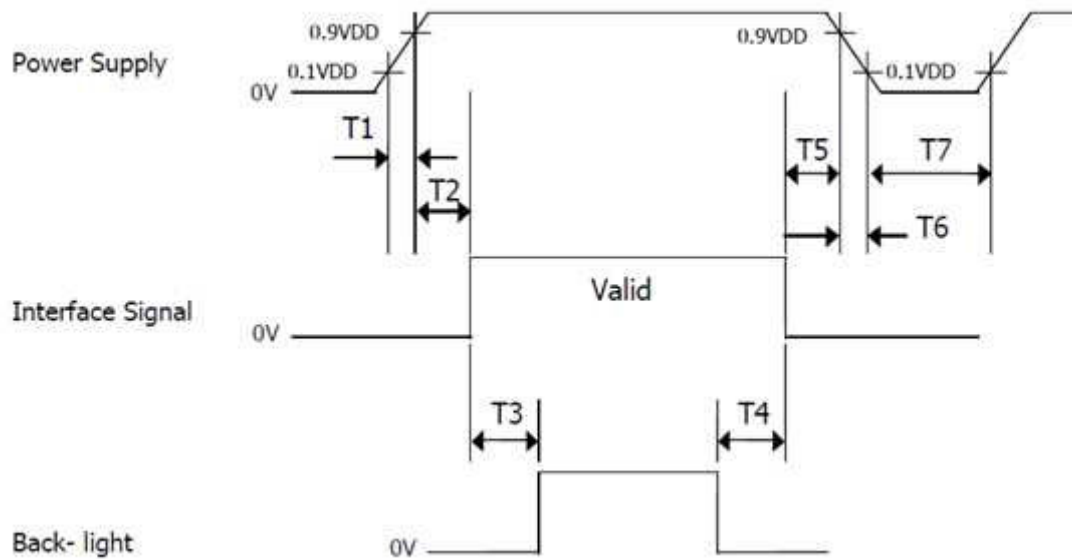
4) LVDS Rx Interface Timing Parameter



5) Input Clock and Data Timing Diagram



## 10. Power sequence



Parameter	Values			Units
	Min	Typ	Max	
T1	0	-	10	ms
T2	0	-	50	ms
T3	200	-	-	ms
T4	200	-	-	ms
T5	0.5	-	50	ms
T6	0	-	10	ms
T7	500	-	-	ms

### Notes:

1. When the power supply VDD is 0V, keep the level of input signals on the low or keep high impedance.
2. Do not keep the interface signal high impedance when power is on. Back Light must be turn on after power for logic and interface signal are valid.

## 11. Quality Assurance

### 11.1.Purpose

This standard for Quality Assurance assures the quality of LCD module products supplied to customer.

### 11.2.Standard for Quality Test

#### 11.2.1. Sampling Plan:

GB2828.1-2012

Single sampling, general inspection level II

#### 11.2.2. Sampling Criteria:

Visual inspection: AQL 1.5%

Electrical functional: AQL 0.65%.

#### 11.2.3. Reliability Test:

Detailed requirement refer to Reliability Test Specification.

### 11.3.Nonconforming Analysis & Disposition

#### 11.3.1. Nonconforming analysis:

11.3.1.1. Customer should provide overall information of non-conforming sample for their complaints.

11.3.1.2. After receipt of detailed information from customer, the analysis of nonconforming parts usually should be finished in one week.

11.3.1.3. If can not finish the analysis on time, customer will be notified with the progress status.

#### 11.3.2. Disposition of nonconforming:

11.3.2.1. Non-conforming product over PPM level will be replaced.

11.3.2.2. The cause of non-conformance will be analyzed. Corrective action will be discussed and implemented.

### 11.4.Agreement Items

Shall negotiate with customer if the following situation occurs:

11.4.1. There is any discrepancy in standard of quality assurance.

11.4.2. Additional requirement to be added in product specification.

11.4.3. Any other special problem.



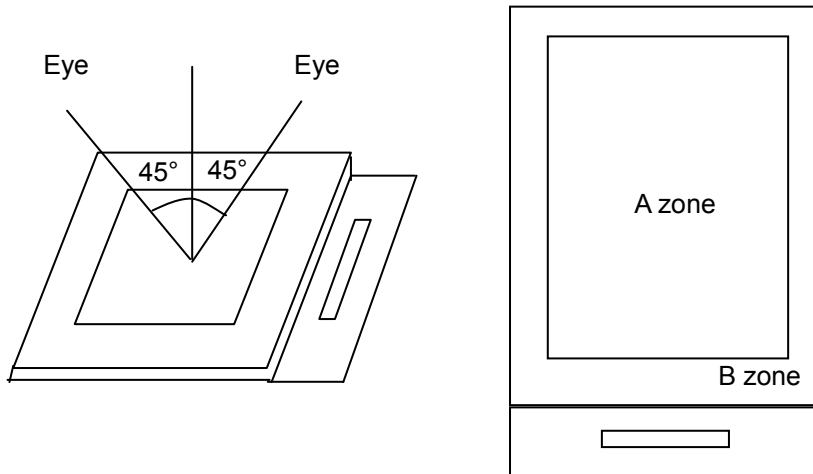
**11.5. Standard of the Product Visual Inspection**

11.5.1. Appearance inspection:

11.5.1.1. The inspection must be under illumination about 1000 – 1500 lx, and the distance of view must be at 30cm ± 2cm.

11.5.1.2. The viewing angle should be 45° from the vertical line without reflection light or follows customer's viewing angle specifications.

11.5.1.3. Definition of area: A Zone: Active Area, B Zone: Viewing Area,



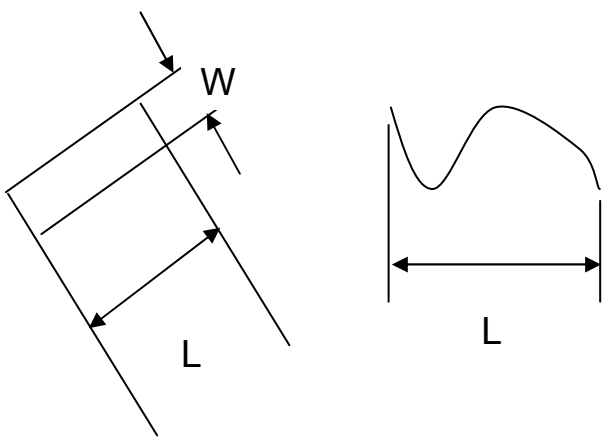
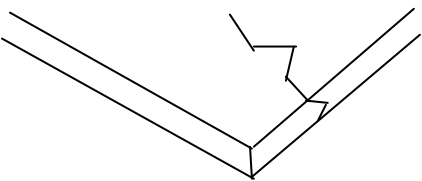
11.5.2. Basic principle:

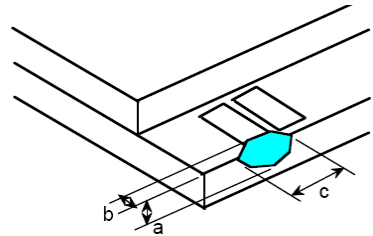
11.5.2.1. A set of sample to indicate the limit of acceptable quality level must be discussed by both us and customer when there is any dispute happened.

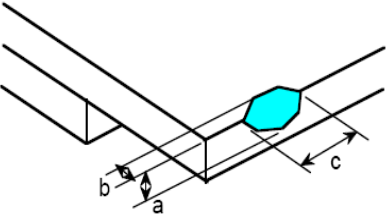
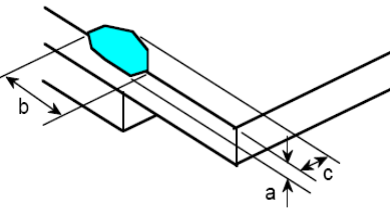
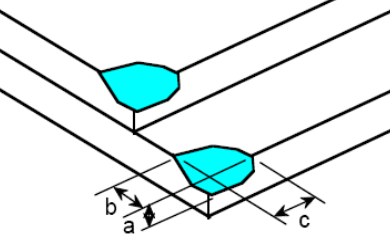
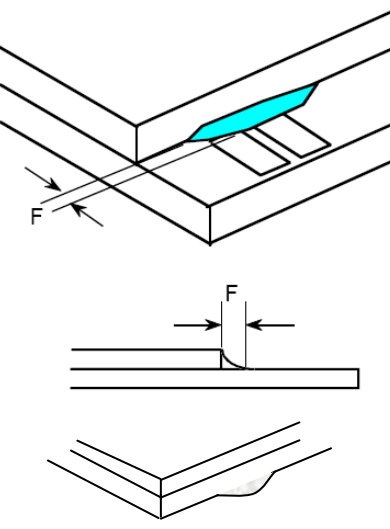
11.5.2.2. New item must be added on time when it is necessary.

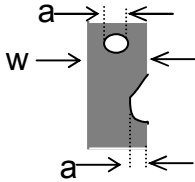
**11.6. Inspection Specification**

No.	Item	Criteria (Unit: mm)															
01	Black / White spot Foreign material (Round type) Pinholes Stain Particles inside cell. (Minor defect)	<p><math>\phi = (a + b) / 2</math></p> <p>Distance between 2 defects should more than 5mm apart.</p>	<table border="1"> <thead> <tr> <th>Size</th> <th>Area</th> <th>Acc. Qty</th> </tr> </thead> <tbody> <tr> <td><math>\phi \leq 0.20</math></td> <td></td> <td>Ignore</td> </tr> <tr> <td><math>0.20 &lt; \phi \leq 0.50</math></td> <td></td> <td><math>N \leq 3</math></td> </tr> <tr> <td><math>0.50 &lt; \phi</math></td> <td></td> <td>0</td> </tr> </tbody> </table>	Size	Area	Acc. Qty	$\phi \leq 0.20$		Ignore	$0.20 < \phi \leq 0.50$		$N \leq 3$	$0.50 < \phi$		0		
Size	Area	Acc. Qty															
$\phi \leq 0.20$		Ignore															
$0.20 < \phi \leq 0.50$		$N \leq 3$															
$0.50 < \phi$		0															
02	Electrical Defect (Minor defect)	<table border="1"> <thead> <tr> <th rowspan="2">Bright dot</th> <th>Display Area</th> <th>Total</th> <th rowspan="3">Note1</th> </tr> </thead> <tbody> <tr> <td><math>N \leq 2</math></td> <td><math>N \leq 2</math></td> </tr> <tr> <th>Dark dot</th> <td><math>N \leq 4</math></td> <td><math>N \leq 4</math></td> </tr> <tr> <th>Total dot</th> <td><math>N \leq 4</math></td> <td><math>N \leq 4</math></td> </tr> </tbody> </table>	Bright dot	Display Area	Total	Note1	$N \leq 2$	$N \leq 2$	Dark dot	$N \leq 4$	$N \leq 4$	Total dot	$N \leq 4$	$N \leq 4$			
Bright dot	Display Area	Total		Note1													
	$N \leq 2$	$N \leq 2$															
Dark dot	$N \leq 4$	$N \leq 4$															
Total dot	$N \leq 4$	$N \leq 4$															
		Remark: 1. Bright dot caused by scratch and foreign object accords to item 1.															

<p>03</p>	<p>Black and White line Scratch Foreign material (Line type) (Minor defect)</p>	 <table border="1" data-bbox="635 745 1259 1010"> <thead> <tr> <th>Length</th> <th>Width</th> <th>Acc. Qty</th> </tr> </thead> <tbody> <tr> <td>/</td> <td><math>W \leq 0.1</math></td> <td>Ignore</td> </tr> <tr> <td><math>L \leq 2.5</math></td> <td><math>0.1 &lt; W \leq 0.2</math></td> <td>3</td> </tr> <tr> <td><math>L &gt; 2.5</math></td> <td><math>0.2 &lt; W</math></td> <td>0</td> </tr> <tr> <td colspan="2">Total</td> <td>3</td> </tr> </tbody> </table> <p>Distance between 2 defects should more than 3mm apart. Scratches not viewable through the back of the display are acceptable.</p>	Length	Width	Acc. Qty	/	$W \leq 0.1$	Ignore	$L \leq 2.5$	$0.1 < W \leq 0.2$	3	$L > 2.5$	$0.2 < W$	0	Total		3
Length	Width	Acc. Qty															
/	$W \leq 0.1$	Ignore															
$L \leq 2.5$	$0.1 < W \leq 0.2$	3															
$L > 2.5$	$0.2 < W$	0															
Total		3															
<p>04</p>	<p>Glass Crack (Minor defect)</p>	 <p>Crack is potential to enlarge, any type is not allowed.</p>															

<p>05</p>	<p>Glass Chipping Pad Area: (Minor defect)</p> 	<table border="1" data-bbox="861 1680 1332 1854"> <thead> <tr> <th>Length and Width</th> <th>Acc. Qty</th> </tr> </thead> <tbody> <tr> <td><math>c &gt; 3.0, b &lt; 1.0</math></td> <td>1</td> </tr> <tr> <td><math>c &lt; 3.0, b &lt; 1.0</math></td> <td>3</td> </tr> <tr> <td colspan="2"><math>a &lt; \text{Glass Thickness}</math></td> </tr> </tbody> </table>	Length and Width	Acc. Qty	$c > 3.0, b < 1.0$	1	$c < 3.0, b < 1.0$	3	$a < \text{Glass Thickness}$	
Length and Width	Acc. Qty									
$c > 3.0, b < 1.0$	1									
$c < 3.0, b < 1.0$	3									
$a < \text{Glass Thickness}$										

<p>06</p>	<p>Glass Chipping Rear of Pad Area: (Minor defect)</p> 	<table border="1"> <thead> <tr> <th>Length and Width</th> <th>Acc. Qty</th> </tr> </thead> <tbody> <tr> <td><math>c &gt; 3.0, b &lt; 1.0</math></td> <td>1</td> </tr> <tr> <td><math>c &lt; 3.0, b &lt; 1.0</math></td> <td>2</td> </tr> <tr> <td><math>c &lt; 3.0, b &lt; 0.5</math></td> <td>4</td> </tr> <tr> <td colspan="2" style="text-align: center;"><math>a &lt; \text{Glass Thickness}</math></td> </tr> </tbody> </table>	Length and Width	Acc. Qty	$c > 3.0, b < 1.0$	1	$c < 3.0, b < 1.0$	2	$c < 3.0, b < 0.5$	4	$a < \text{Glass Thickness}$	
Length and Width	Acc. Qty											
$c > 3.0, b < 1.0$	1											
$c < 3.0, b < 1.0$	2											
$c < 3.0, b < 0.5$	4											
$a < \text{Glass Thickness}$												
<p>07</p>	<p>Glass Chipping Except Pad Area: (Minor defect)</p> 	<table border="1"> <thead> <tr> <th>Length and Width</th> <th>Acc. Qty</th> </tr> </thead> <tbody> <tr> <td><math>c &gt; 3.0, b &lt; 1.0</math></td> <td>1</td> </tr> <tr> <td><math>c &lt; 3.0, b &lt; 1.0</math></td> <td>2</td> </tr> <tr> <td><math>c &lt; 3.0, b &lt; 0.5</math></td> <td>4</td> </tr> <tr> <td colspan="2" style="text-align: center;"><math>a &lt; \text{Glass Thickness}</math></td> </tr> </tbody> </table>	Length and Width	Acc. Qty	$c > 3.0, b < 1.0$	1	$c < 3.0, b < 1.0$	2	$c < 3.0, b < 0.5$	4	$a < \text{Glass Thickness}$	
Length and Width	Acc. Qty											
$c > 3.0, b < 1.0$	1											
$c < 3.0, b < 1.0$	2											
$c < 3.0, b < 0.5$	4											
$a < \text{Glass Thickness}$												
<p>08</p>	<p>Glass Corner Chipping: (Minor defect)</p> 	<table border="1"> <thead> <tr> <th>Length and Width</th> <th>Acc. Qty</th> </tr> </thead> <tbody> <tr> <td><math>c &lt; 3.0, b &lt; 3.0</math></td> <td>Ignore</td> </tr> <tr> <td colspan="2" style="text-align: center;"><math>a &lt; \text{Glass Thickness}</math></td> </tr> </tbody> </table>	Length and Width	Acc. Qty	$c < 3.0, b < 3.0$	Ignore	$a < \text{Glass Thickness}$					
Length and Width	Acc. Qty											
$c < 3.0, b < 3.0$	Ignore											
$a < \text{Glass Thickness}$												
<p>09</p>	<p>Glass Burr: (Minor defect)</p> 	<table border="1"> <thead> <tr> <th>Length</th> <th>Acc. Qty</th> </tr> </thead> <tbody> <tr> <td><math>F &lt; 1.0</math></td> <td>Ignore</td> </tr> </tbody> </table> <p>Glass burr don't affect assemble and module dimension.</p>	Length	Acc. Qty	$F < 1.0$	Ignore						
Length	Acc. Qty											
$F < 1.0$	Ignore											

10	<p>FPC Defect: (Minor defect)</p> 	<p>10.1 Dent, pinhole width <math>a &lt; w/3</math>. (w: circuitry width.) 10.2 Open circuit is unacceptable. 10.3 No oxidation, contamination and distortion.</p>								
11	Bubble on Polarizer (Minor defect)	<table border="1" data-bbox="737 577 1206 748"> <thead> <tr> <th>Diameter</th> <th>Acc. Qty</th> </tr> </thead> <tbody> <tr> <td><math>\varphi \leq 0.30</math></td> <td>Ignore</td> </tr> <tr> <td><math>0.30 &lt; \varphi \leq 0.50</math></td> <td><math>N \leq 2</math></td> </tr> <tr> <td><math>0.50 &lt; \varphi</math></td> <td><math>N=0</math></td> </tr> </tbody> </table>	Diameter	Acc. Qty	$\varphi \leq 0.30$	Ignore	$0.30 < \varphi \leq 0.50$	$N \leq 2$	$0.50 < \varphi$	$N=0$
Diameter	Acc. Qty									
$\varphi \leq 0.30$	Ignore									
$0.30 < \varphi \leq 0.50$	$N \leq 2$									
$0.50 < \varphi$	$N=0$									
12	Dent on Polarizer (Minor defect)	<table border="1" data-bbox="737 819 1206 990"> <thead> <tr> <th>Diameter</th> <th>Acc. Qty</th> </tr> </thead> <tbody> <tr> <td><math>\varphi \leq 0.25</math></td> <td>Ignore</td> </tr> <tr> <td><math>0.25 &lt; \varphi \leq 0.50</math></td> <td><math>N \leq 4</math></td> </tr> <tr> <td><math>0.50 &lt; \varphi</math></td> <td>None</td> </tr> </tbody> </table>	Diameter	Acc. Qty	$\varphi \leq 0.25$	Ignore	$0.25 < \varphi \leq 0.50$	$N \leq 4$	$0.50 < \varphi$	None
Diameter	Acc. Qty									
$\varphi \leq 0.25$	Ignore									
$0.25 < \varphi \leq 0.50$	$N \leq 4$									
$0.50 < \varphi$	None									
13	Bezel	<p>13.1 No rust, distortion on the Bezel. 13.2 No visible fingerprints, stains or other contamination.</p>								
14	Touch Panel	<p>D: Diameter W: width L: length 14.1 Spot: <math>D &lt; 0.25</math> is acceptable <math>0.25 \leq D \leq 0.4</math> 2dots are acceptable and the distance between defects should more than 10 mm. <math>D &gt; 0.4</math> is unacceptable 14.2 Dent: <math>D &gt; 0.40</math> is unacceptable 14.3 Scratch: <math>W \leq 0.03</math>, <math>L \leq 10</math> is acceptable, <math>0.03 &lt; W \leq 0.10</math>, <math>L \leq 10</math> is acceptable Distance between 2 defects should more than 10 mm. <math>W &gt; 0.10</math> is unacceptable.</p>								
15	PCB	<p>15.1 No distortion or contamination on PCB terminals. 15.2 All components on PCB must same as documented on the BOM/component layout. 15.3 Follow IPC-A-600F.</p>								
16	Soldering	Follow IPC-A-610C standard								

17	Electrical Defect (Major defect)	<p>The below defects must be rejected.</p> <p>17.1 Missing vertical / horizontal segment, 17.2 Abnormal Display. 17.3 No function or no display. 17.4 Current exceeds product specifications. 17.5 LCD viewing angle defect. 17.6 No Backlight. 17.7 Dark Backlight. 17.8 Touch Panel no function.</p>
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Remark: LCD Panel Broken shall be rejected. Defect out of LCD viewing area is acceptable.

**11.7. Classification of Defects**

11.7.1. Visual defects (Except no / wrong label) are treated as minor defect and electrical defect is major.

11.7.2. Two minor defects are equal to one major in lot sampling inspection.

**11.8. Identification/marketing criteria**

Any unit with illegible / wrong /double or no marking/ label shall be rejected.

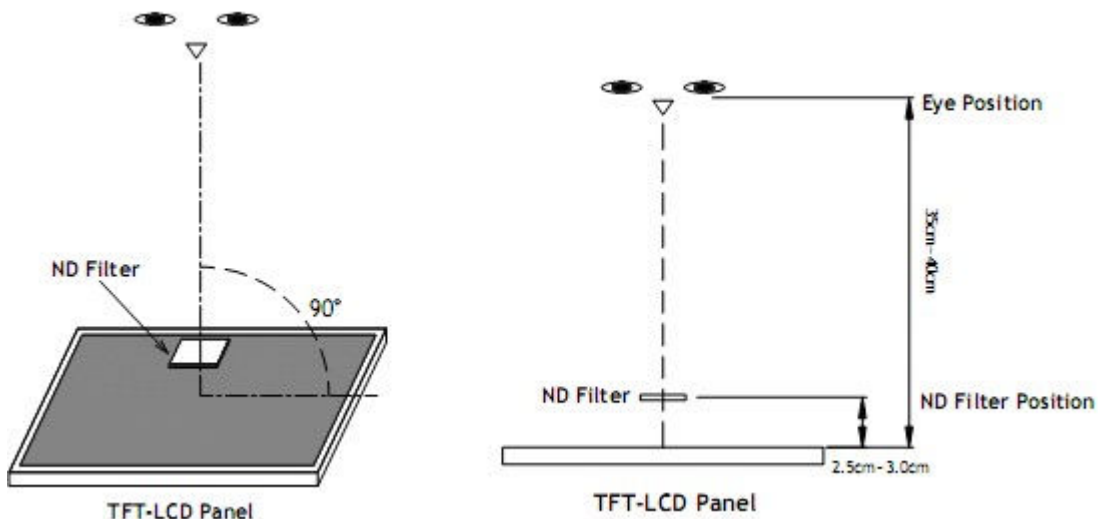
**11.9. Packing**

11.9.1. There should be no damage of the outside carton box, each packaging box should have one identical label.

11.9.2. Modules inside package box should have compliant mark.

11.9.3. All direct package materials shall offer ESD protection

**Note1:** Bright dot is defined as the defective area of the dot is larger than 50% of one sub-pixel area.



Bright dot: The bright dot size defect at black display pattern. It can be recognized by 2% transparency of filter when the distance between eyes and panel is  $350\text{mm} \pm 50\text{mm}$ .

Dark dot: Cyan, Magenta or Yellow dot size defect at white display pattern. It can be recognized by 5% transparency of filter when the distance between eyes and panel is  $350\text{mm} \pm 50\text{mm}$ .

**Note2:** Mura on display which appears darker / brighter against background brightness on parts of display area.

## 12. Reliability Specification

No	Item	Condition	Quantity	Criteria
1	High Temperature Operating	<b>65°C, 96Hrs</b>	2	GB/T2423.2-2008
2	Low Temperature Operating	<b>-20°C, 96Hrs</b>	2	GB/T2423.1-2008
3	High Humidity	<b>60°C, 90%RH, 96Hrs</b>	2	GB/T2423.3-2006
4	High Temperature Storage	<b>85°C, 96Hrs</b>	2	GB/T2423.2-2008
5	Low Temperature Storage	<b>-40°C, 96Hrs</b>	2	GB/T2423.1-2008
6	Thermal Cycling Test	-20°C, 60min~65°C, 60min, 20 cycles.	2	GB/T2423.22-2012
7	Packing vibration	Frequency range:10Hz~50Hz Acceleration of gravity:5G X,Y,Z 30 min for each direction.	2	GB/T5170.14-2009
8	Electrical Static Discharge	Air:±4KV 150pF/330Ω 5 times Contact:±2KV 150pF/330Ω 5 times	2	GB/T17626.2-2006
9	Drop Test (Packaged)	Height:80 cm,1 corner, 3 edges, 6 surfaces.	2	GB/T2423.8-1995

Note1. No deflection cosmetic and operational function allowable.

Note2. Total current Consumption should be below double of initial value

## 13. Precautions and Warranty

### 13.1. Safety

- 13.1.1. The liquid crystal in the LCD is poisonous. Do not put it in your mouth. If the liquid crystal touches your skin or clothes, wash it off immediately using soap and water.
- 13.1.2. Since the liquid crystal cells are made of glass, do not apply strong impact on them. Handle with care.

### 13.2. Handling

- 13.2.1. Reverse and use within ratings in order to keep performance and prevent damage.
- 13.2.2. Do not wipe the polarizer with dry cloth, as it might cause scratch. If the surface of the LCD needs to be cleaned, wipe it swiftly with cotton or other soft cloth soaked with petroleum IPA, do not use other chemicals.

### 13.3. Storage

- 13.3.1. Do not store the LCD module beyond the specified temperature ranges.
- 13.3.2. Strong light exposure causes degradation of polarizer and color filter

### 13.4. Metal Pin (Apply to Products with Metal Pins)

#### 13.4.1. Pins of LCD and Backlight

13.4.1.1. Solder tip can touch and press on the tip of Pin LEAD during the soldering

#### 13.4.1.2. Recommended Soldering Conditions

Solder Type: Sn96.3~94-Ag3.3~4.3-Cu0.4~1.1

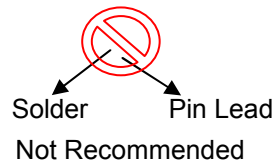
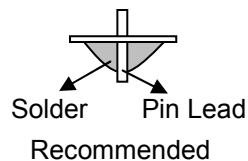
Maximum Solder Temperature: 370℃

Maximum Solder Time: 3s at the maximum temperature

Recommended Soldering Temp: 350±20℃

Typical Soldering Time: ≤3s

#### 13.4.1.3. Solder Wetting



#### 13.4.2. Pins of EL

13.4.2.1. Solder tip can touch and press on the tip of EL leads during soldering.

13.4.2.2. No Solder Paste on the soldering pad on the motherboard is recommended.

#### 13.4.2.3. Recommended Soldering Conditions

Solder type: Nippon Alimit Leadfree SR-34, size 0.5mm

Recommended Solder Temperature: 270~290℃

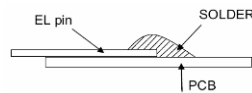
Typical Soldering Time: ≤2s

Minimum solder distance from EL lamp (body):2.0mm

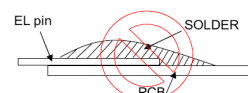
13.4.2.4. No horizontal press on the EL leads during soldering.

13.4.2.5. 180° bend EL leads three times is not allowed.

#### 13.4.2.6. Solder Wetting

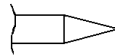


Recommended

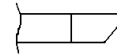


Not Recommended

#### 13.4.2.7. The type of the solder iron:

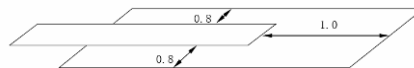


Recommended



Not Recommended

#### 13.4.2.8. Solder Pad



### 13.5. Operation

- 13.5.1. Do not drive LCD with DC voltage
- 13.5.2. Response time will increase below lower temperature
- 13.5.3. Display may change color with different temperature
- 13.5.4. Mechanical disturbance during operation, such as pressing on the display area, may cause the segments to appear “fractured”.
- 13.5.5. Do not connect or disconnect the LCM to or from the system when power is on.
- 13.5.6. Never use the LCM under abnormal condition of high temperature and high humidity.
- 13.5.7. Module has high frequency circuits. Sufficient suppression to the electromagnetic interface shall be done by system manufacturers. Grounding and shielding methods may be important to minimize the interference.
- 13.5.8. Do not display the fixed pattern for long time (we suggest the time not longer than one hour) because it may develop image sticking due to the TFT structure.

### 13.6. Static Electricity

- 13.6.1. CMOS LSIs are equipped in this unit, so care must be taken to avoid the electro-static charge, by ground human body, etc.
- 13.6.2. The normal static prevention measures should be observed for work clothes and benches.
- 13.6.3. The module should be kept into anti-static bags or other containers resistant to static for storage.

### 13.7. Limited Warranty

- 13.7.1. Our warranty liability is limited to repair and/or replacement. We will not be responsible for any consequential loss.
- 13.7.2. If possible, we suggest customer to use up all modules in six months. If the module storage time over twelve months, we suggest that recheck it before the module be used
- 13.7.3. After the product shipped, any product quality issues must be feedback within three months, otherwise, we will not be responsible for the subsequent or consequential events.



## 14. Packaging

TBD

### 15. Outline Drawing

