

PRODUCT SPECIFICATION

7.0" TFT LCD MODULE

MODEL:T070102600-B3WMN-001 Ver:1.6

ROHS

< ◇ > Preliminary Specification

< ◆ > Finally Specification

CUSTOMER'S APPROVAL	
CUSTOMER :	
SIGNATURE:	DATE:

APPROVED BY	PM REVIEWED	PD REVIEWED	PREPARED BY

Revision History

Revision	Date	Originator	Detail	Remarks
1.0	2018.12.17	ZDT	Initial Release	
1.1	2019.01.03	ZDT	Add Weight Add Chromaticity Transmissive	P4 P7
1.2	2019.02.15	ZDT	Modify Temperature Modify DC Characteristics Modify Response time Modify Outline Drawing	P4/P20 P5 P7 P24
1.3	2019.03.25	ZDT	Modify Luminance on TFT Modify Outline Drawing	P6 P24
1.4	2019.07.16	ZDT	Add Data Input Format for LVDS Add Power On/Off Sequence Modify Reliability Specification	P13 P15 P22
1.5	2019.12.04	ZDT	Modify Display Colors	P4
1.6	2020.01.08	ZDT	Add Timing table	P14

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1. General Description

The specification is a transmissive type color active matrix liquid crystal display (LCD) which uses amorphous thin film transistor (TFT) as switching devices. This product is composed of a TFT-LCD panel, driver ICs and a backlight unit.

2. Module Parameter

Features	Details	Unit
Display Size(Diagonal)	7.0"	
LCD type	IPS TFT	
Display Mode	Transmissive/Normally black	
Resolution	1024 RGB x 600	Pixels
View Direction	FULL VIEW	Best Image
Module Outline	164.9(H) x 100(V) x 3.35(T) (Note1)	mm
Active Area	154.21(H) x 85.92(V)	mm
Pixel Pitch	150.6 (H) x 143.2(V)	um
Pixel Arrangement	RGB vertical Stripe	
Polarizer Surface Treatment	Glare	
Display Colors	262K/16.7 M	
Interface	6/8 bit LVDS Interface	
Driver IC	EK79001&EK73215	-
With or Without Touch Panel	Without	
Operating Temperature	-20~70	°C
Storage Temperature	-30~80	°C
Weight	110	g

Note 1: Exclusive hooks, posts, FFC/FPC tail etc.

3. Absolute Maximum Ratings

V_{SS}=0V, Ta=25°C

Item	Symbol	Min.	Max.	Unit
Supply Voltage	VDD	-0.5	5.0	V
Storage temperature	T _{STG}	-30	+80	°C
Operating temperature	T _{OP}	-20	+70	°C

Note 1: If Ta below 50°C, the maximal humidity is 90%RH, if Ta over 50°C, absolute humidity should be less than 60%RH.

Note 2: The response time will be extremely slow when the operating temperature is around -10°C, and the back ground will become darker at high temperature operating.

4. DC Characteristics

Item	Symbol	Min.	Typ.	Max.	Unit
Supply Voltage	VDD	2.3	3.3	3.6	V
	AVDD	-	9.6	-	V
	VGH	-	18	-	V
	VGL	-	-6	-	V
	VCOMH	-	3.3	-	V
	VCOML	-	3.1	-	V
Differential input high threshold voltage	RxVTH	-	-	0.1	V
Differential input low threshold voltage	RxVTL	-0.1	-	-	V
Input voltage range (singled-end)	RxVIN	0	-	2.4	V

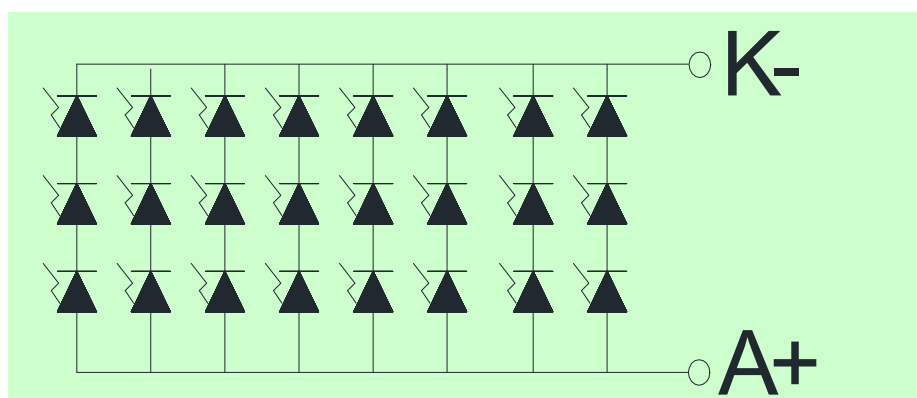
5. Backlight Characteristic

5.1. Backlight Characteristic

Item	Symbol	Condition	Min.	Typ.	Max.	Unit
Forward Voltage	V _F	T _a =25 °C, I _F =20mA/LED	8.4	9.6	10.2	V
Forward Current	I _F	T _a =25 °C, V _F =3.2V/LED	-	160	-	mA
Power dissipation	P _D		-	1536	-	mW
Uniformity	Avg		-	80	-	%
LED working life(25°C)	-		-	30,000	-	Hrs
Drive method	Constant current					
LED Configuration	24 White LEDs (3 LEDs in string and 8 groups in parallel)					

Note1: LED life time defined as follows: The final brightness is at 50% of original brightness.
The environmental conducted under ambient air flow, at T_a=25±2 °C,60%RH±5%, I_F=20mA/LED.

5.2. Backlighting circuit



6. Touch Screen Panel Specifications

Without TP

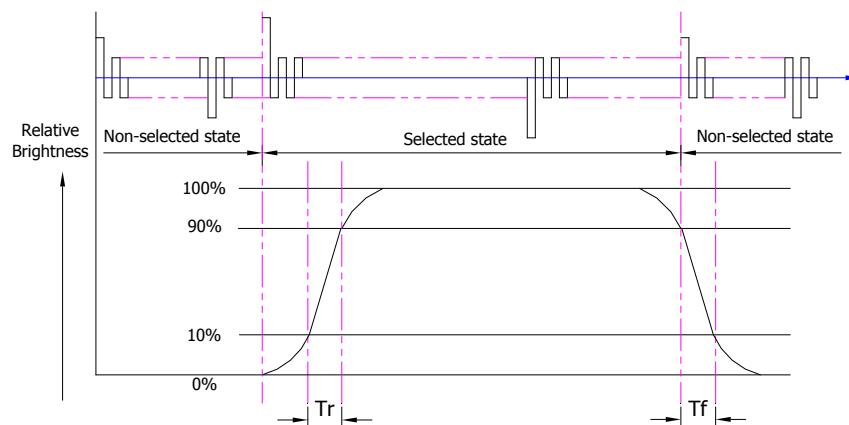
7. Optical Characteristics

Ta=25°C, VDD=3.3V

	Item	Symbol	Condition	Specification			Unit	
				Min.	Typ.	Max.		
Backlight On (Transmissive Mode)	Luminance on TFT($I_f=20\text{mA/LED}$)	Lv		290	370	-	cd/m ²	
	Contrast ratio(See 7.3)	CR		-	800	-		
	Response time (See 7.2)	TR+TF		-	30	40	ms	
	Chromaticity Transmissive (See 7.5)	Red	XR	Center CR≥10	0.539	0.589	0.639	
			YR		0.294	0.344	0.394	
		Green	XG		0.251	0.301	0.351	
			YG		0.527	0.577	0.627	
		Blue	XB		0.096	0.146	0.196	
			YB		0.042	0.092	0.142	
	White	XW	0.225	0.275	0.325			
YW		0.262	0.312	0.362				
Viewing Angle (See 7.4)	Horizontal	θ_{x+}	Center CR≥10	-	85	-	Deg.	
		θ_{x-}		-	85	-		
	Vertical	ϕ_{y+}		-	85	-		
		ϕ_{y-}		-	85	-		
NTSC Ratio(Gamut)				-	50	-	%	

7.1. Definition of Response Time

7.1.1. Normally Black Type (Negative)

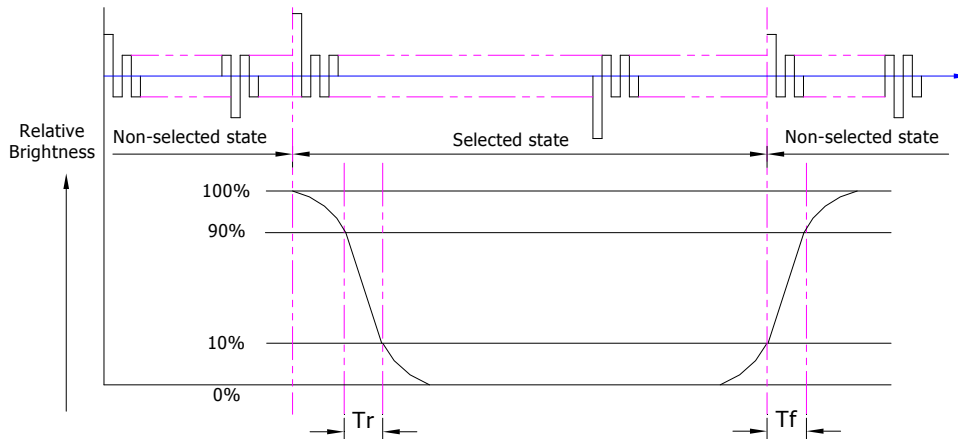


T_r is the time it takes to change from non-selected stage with relative luminance 10% to selected state with relative luminance 90%;

T_f is the time it takes to change from selected state with relative luminance 90% to non-selected state with relative luminance 10%.

Note: Measuring machine: LCD-5100

7.1.2. Normally White Type (Positive)



Tr is the time it takes to change from non-selected state with relative luminance 90% to selected state with relative luminance 10%;

Tf is the time it takes to change from selected state with relative luminance 10% to non-selected state with relative luminance 90%;

Note: Measuring machine: LCD-5100 or EQUI

7.2. Definition of Contrast Ratio

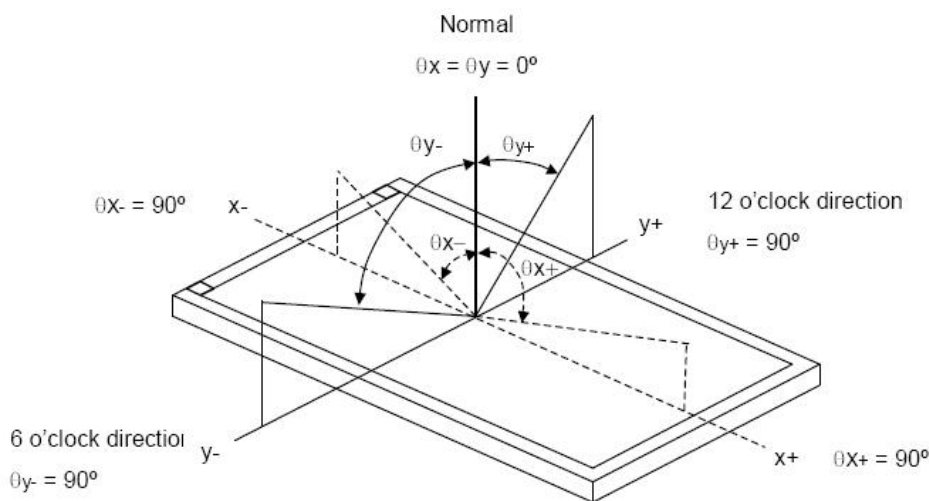
Contrast is measured perpendicular to display surface in reflective and transmissive mode.

The measurement condition is:

Measuring Equipment	Eldim or Equivalent
Measuring Point Diameter	3mm//1mm
Measuring Point Location	Active Area centre point
Test pattern	A: All Pixels white
	B: All Pixel black
Contrast setting	Maximum

Definitions: CR (Contrast) = Luminance of White Pixel / Luminance of Black Pixel

7.3. Definition of Viewing Angles



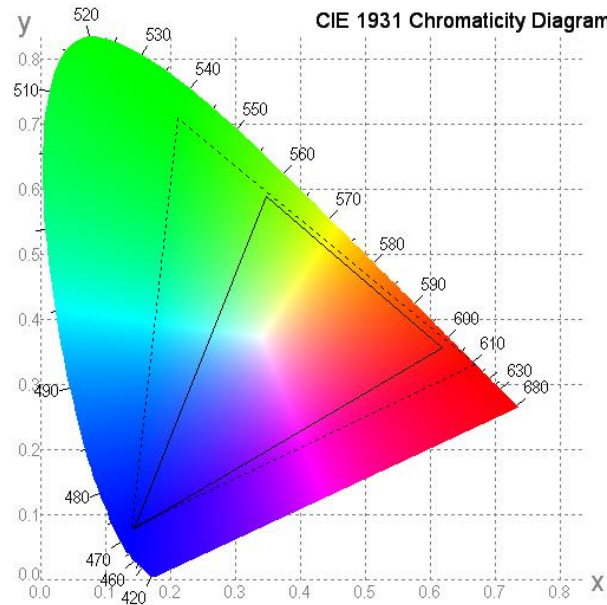
Measuring machine: LCD-5100 or EQUI

7.4. Definition of Color Appearance

R,G,B and W are defined by (x, y) on the IE chromaticity diagram

NTSC=area of RGB triangle/area of NTSC triangleX100%

Measuring picture: Red, Green, Blue and White (Measuring machine: BM-7)



7.5. Definition of Surface Luminance, Uniformity and Transmittance

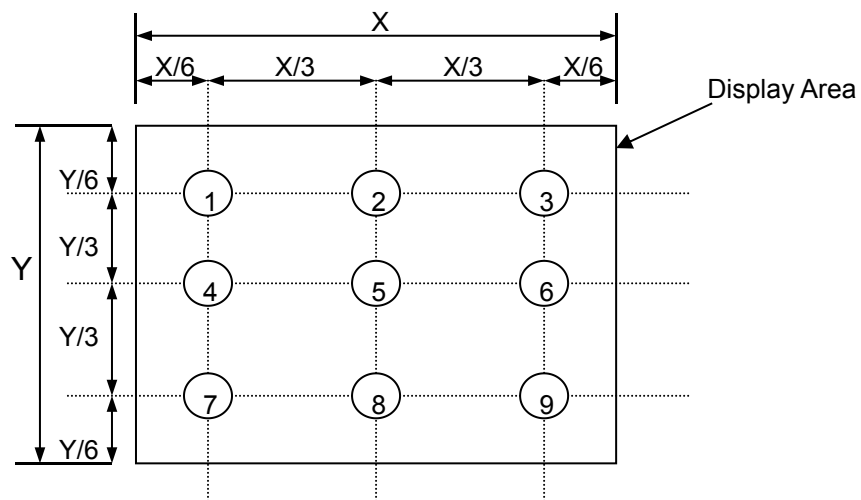
Using the transmissive mode measurement approach, measure the white screen luminance of the display panel and backlight.

7.5.1. Surface Luminance: $L_V = \text{average} (L_{P1}:L_{P9})$

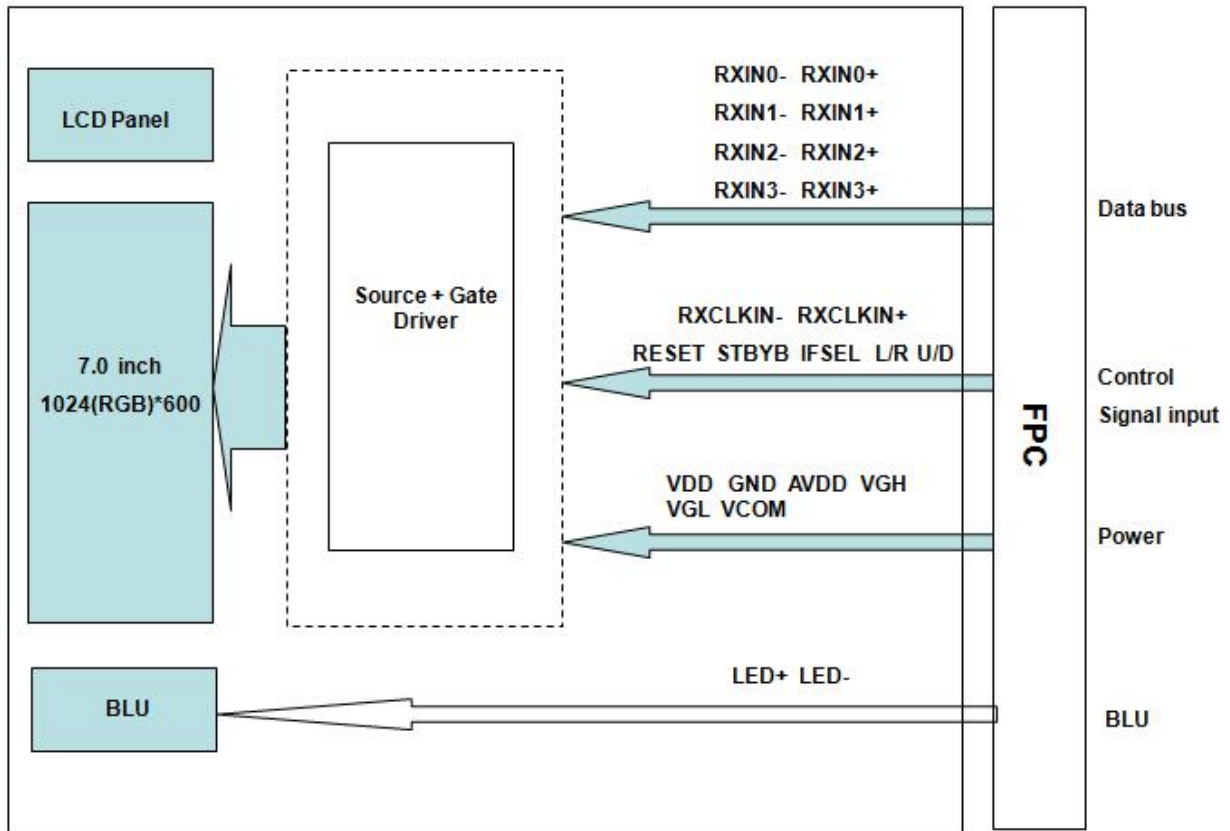
7.5.2. Uniformity = Minimal ($L_{P1}:L_{P9}$) / Maximal ($L_{P1}:L_{P9}$) * 100%

7.5.3. Transmittance = L_V on LCD / L_V on Backlight * 100%

Note: Measuring machine: BM-7



8. Block Diagram and Power Supply



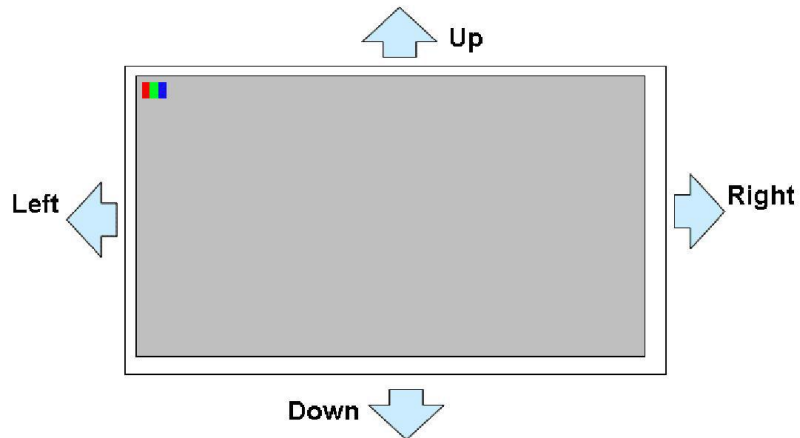
9. Interface Pins Definition

No.	Symbol	Function	Remark
1	VCOM	Common Voltage.	
2	VDD	Power Supply	
3	VDD	Power Supply	
4	NC	Not connected	
5	RESET	Global reset pin	
6	STBYB	Standby mode, Normally pulled high. STBYB="1", normal operation STBYB="0", timing controller, source driver will turn off	
7	GND	Ground	
8	RXIN0-	-LVDS differential data input.	
9	RXIN0+	+LVDS differential data input.	
10	GND	Ground	
11	RXIN1-	-LVDS differential data input.	
12	RXIN1+	+LVDS differential data input.	
13	GND	Ground	
14	RXIN1-	-LVDS differential data input.	
15	RXIN+	+LVDS differential data input.	
16	GND	Ground	
17	RXCLKIN-	-LVDS differential clock input.	
18	RXCLKIN+	+LVDS differential clock input.	
19	GND	Ground	
20	RXIN3-	-LVDS differential data input.	
21	RXIN3+	+LVDS differential data input.	
22	GND	Ground	
23	NC	Not connected	
24	NC	Not connected	
25	GND	Ground	
26	NC	Not connected	
27	DIMO	Backlight CABC controller signal output	
28	IFSEL	"L": 8bit LVDS interface "H":6bit LVDS interface	
29	AVDD	Power for Analog Circuit	
30	GND	Ground	
31	LED-	LED Cathode.	
32	LED-	LED Cathode.	
33	L/R	Horizontal inversion.	NOTE1
34	U/D	Vertical inversion.	NOTE1
35	VGL	Gate OFF Voltage.	
36	NC	Not connected	
37	NC	Not connected	
38	VGH	Gate ON Voltage.	

39	LED+	LED Anode.	
40	LED+	LED Anode.	

Note 1:

SHLR	UPDN	Data shifting
DVDD	GND	Left→Right · Up→Down(default)
GND	GND	Right→Left · Up→Down
DVDD	DVDD	Left→Right · Down→Up
GND	DVDD	Right→Left · Down→Up

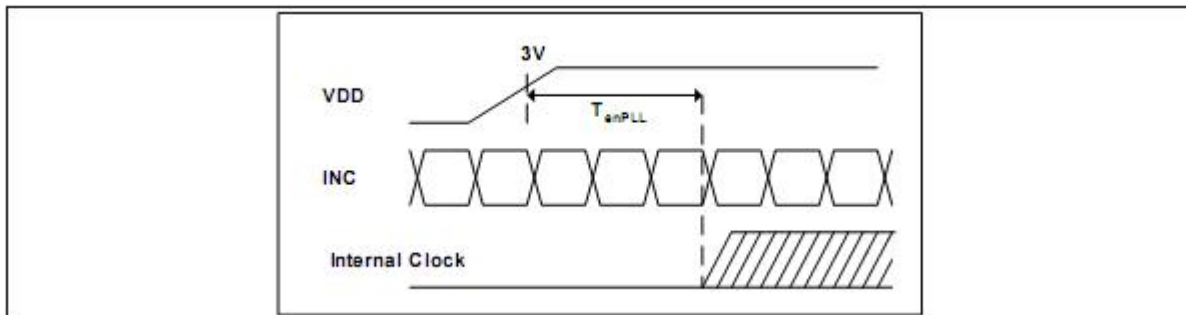
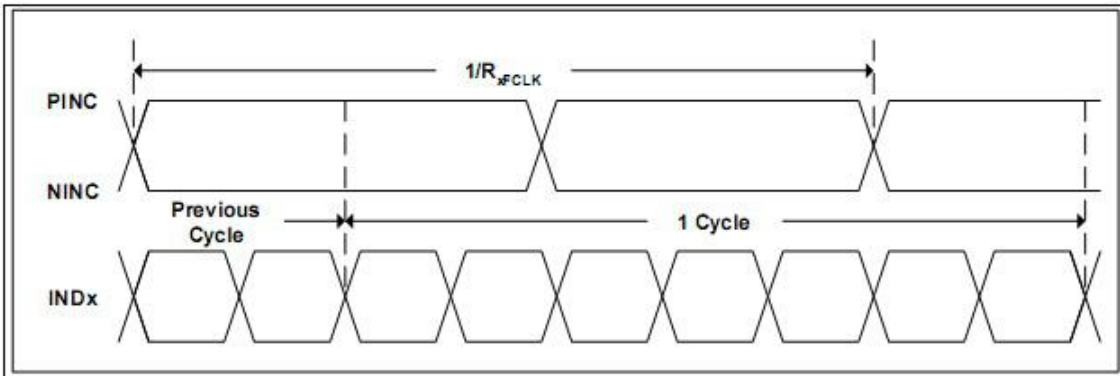


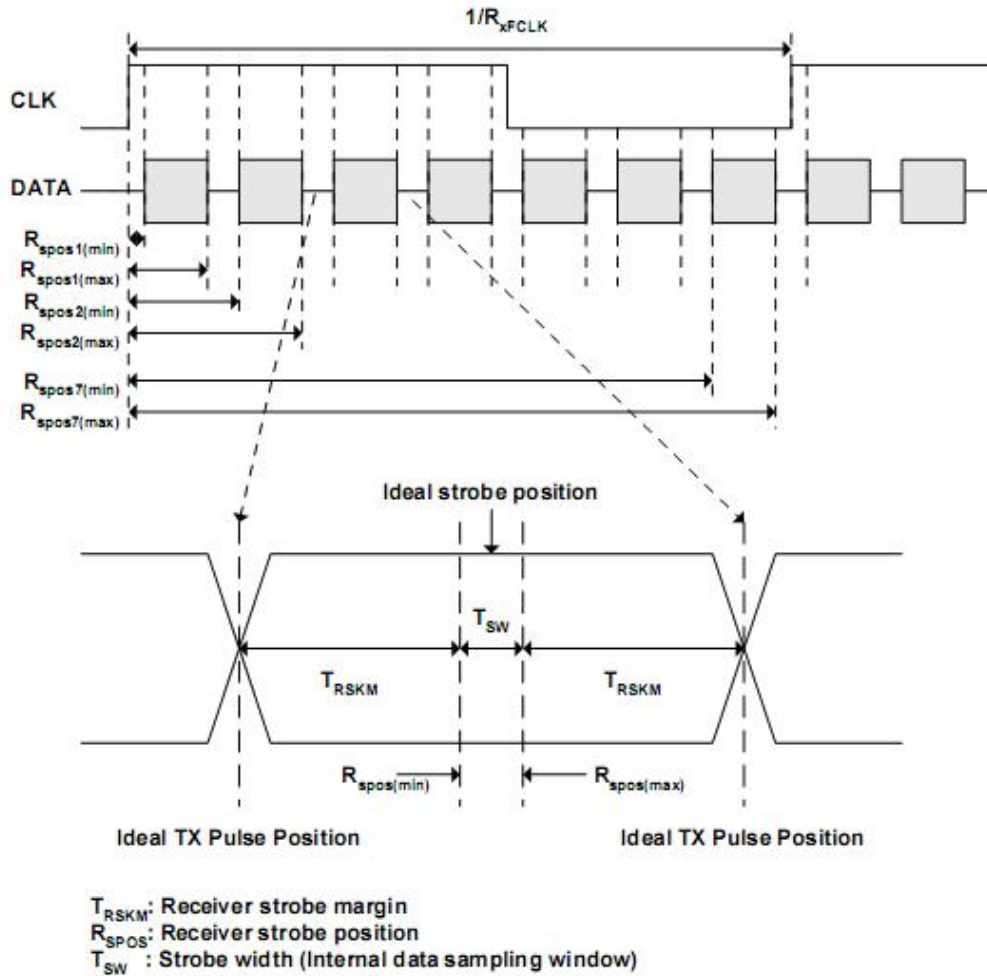
10. AC characteristics

10.1. Timing

LVDS mode

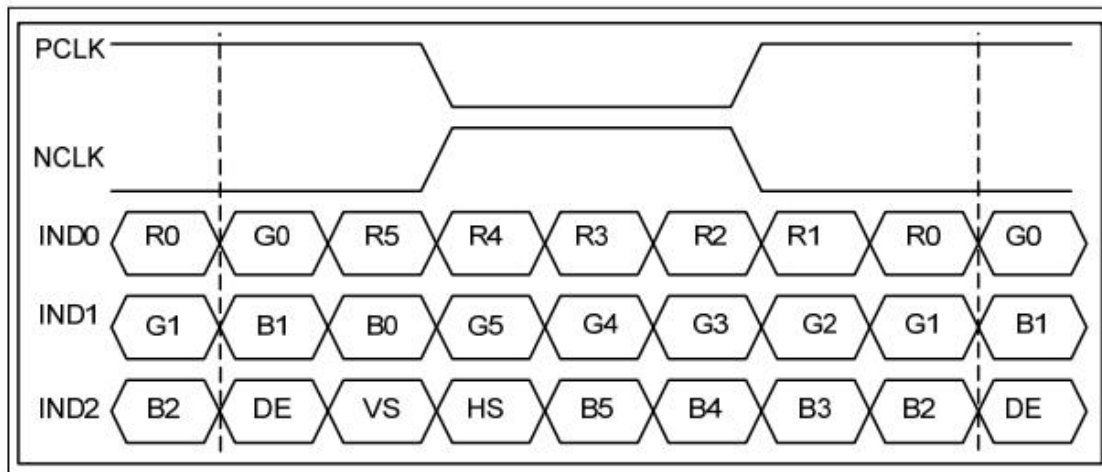
Parameter	Symbol	Min.	Typ.	Max.	Unit	Condition
Clock frequency	R_{xFCLK}	26.2		71	MHz	
Input data skew margin	T_{RSKM}	500			pS	$ V_{ID} = 400mV$ $R_{xVCM} = 1.2V$ $R_{xFCLK} = 71 MHz$
Clock high time	T_{LVCH}		$4/(7 * R_{xFCLK})$		ns	
Clock low time	T_{LVCL}		$3/(7 * R_{xFCLK})$		ns	
PLL wake-up time	T_{enPLL}			150	uS	



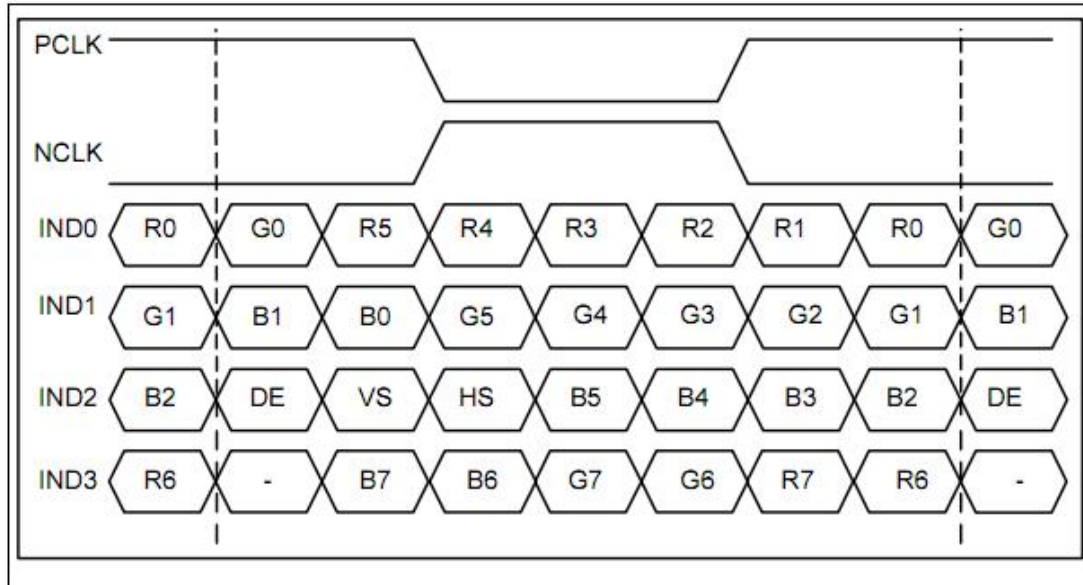


SSC tolerance of LVDS receiver						
Symbol	parameter	condition	Min.	Typ.	Max.	Units
SSC_{MF}	Modulation Frequency		23		93	KHz
SSC_{MR}	Modulation Rate	LVDS clock = 71MHz center spread			± 3	%

10.2.Data Input Format for LVDS



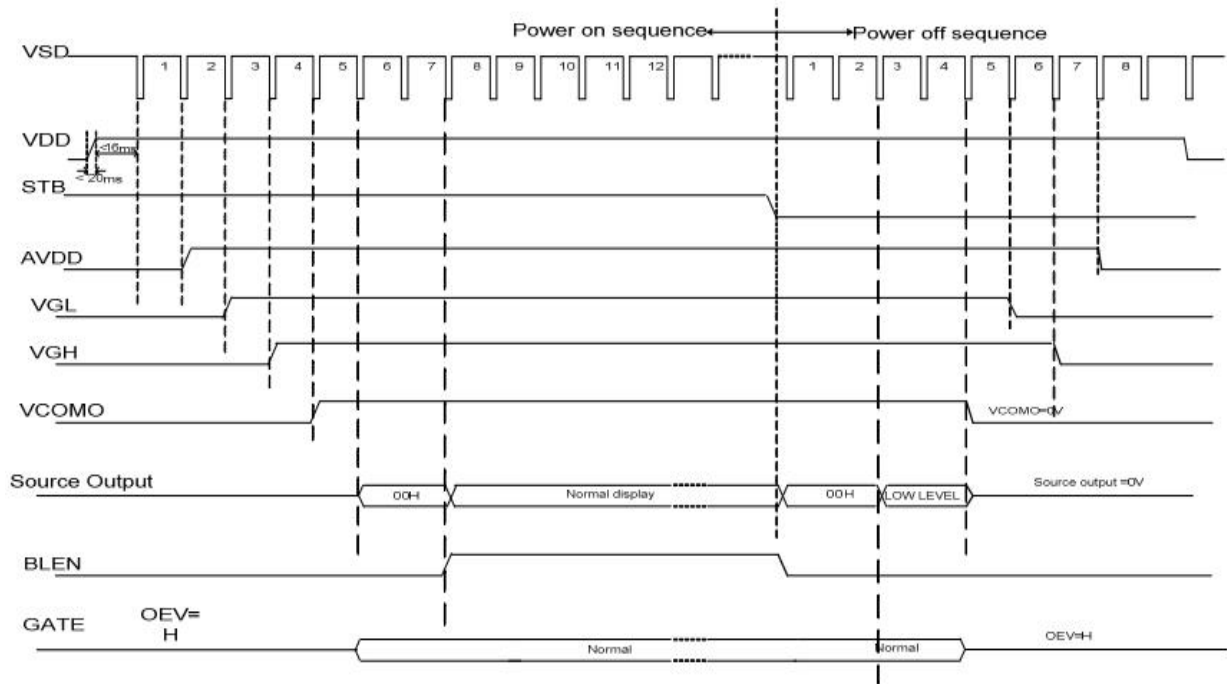
6 bit LVDS



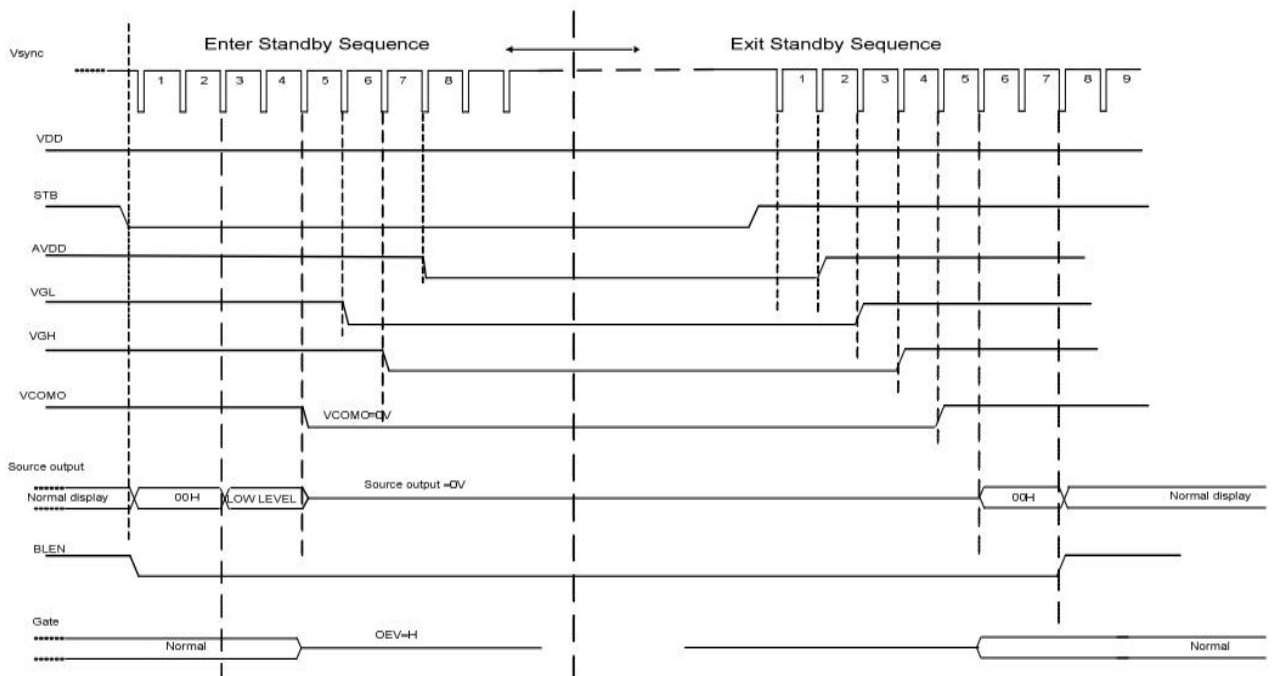
8 bit LVDS

DE mode					
Parameter	Symbol	Value			Unit
		Min.	Typ.	Max.	
DCLK frequency @Frame rate=60hz	fclk	40.8	51.2	67.2	Mhz
Horizontal display area	thd	1024			DCLK
HSYNC period time	th	1114	1344	1400	DCLK
HSYNC blanking	thb+thfp	90	320	376	DCLK
Vertical display area	tvd	600			H
VSYNC period time	tv	610	635	800	H
VSYNC blanking	tvb+tvfp	10	35	200	H

11. Power On/Off Sequence



Power On/Off timing chart



Enter and Exit Standby Mode timing chart

12. Quality Assurance

12.1.Purpose

This standard for Quality Assurance assures the quality of LCD module products supplied to customer.

12.2.Standard for Quality Test

12.2.1. Sampling Plan:

GB2828.1-2012

Single sampling, general inspection level II

12.2.2. Sampling Criteria:

Visual inspection: AQL 1.5

Electrical functional: AQL 0.65.

12.2.3. Reliability Test:

Detailed requirement refer to Reliability Test Specification.

12.3.Nonconforming Analysis & Disposition

12.3.1. Nonconforming analysis:

12.3.1.1. Customer should provide overall information of non-conforming sample for their complaints.

12.3.1.2. After receipt of detailed information from customer, the analysis of nonconforming parts usually should be finished in one week.

12.3.1.3. If cannot finish the analysis on time, customer will be notified with the progress status.

12.3.2. Disposition of nonconforming:

12.3.2.1. Non-conforming product over PPM level will be replaced.

12.3.2.2. The cause of non-conformance will be analyzed. Corrective action will be discussed and implemented.

12.4.Agreement Items

Shall negotiate with customer if the following situation occurs:

12.4.1. There is any discrepancy in standard of quality assurance.

12.4.2. Additional requirement to be added in product specification.

12.4.3. Any other special problem.

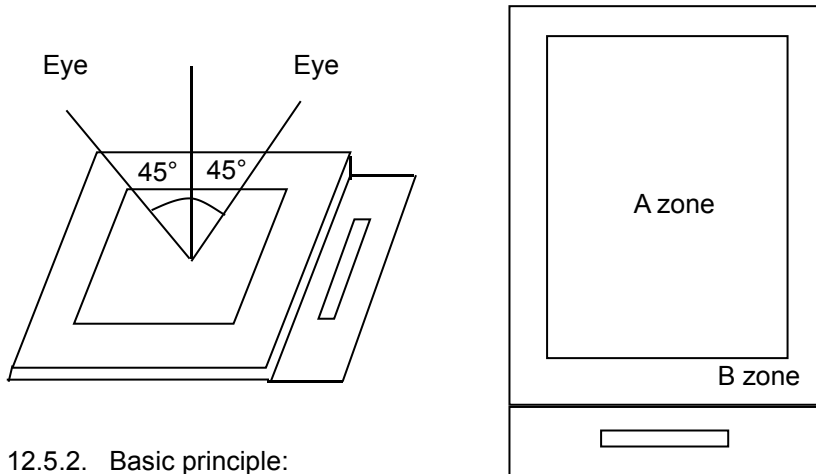
12.5.Standard of the Product Visual Inspection

12.5.1. Appearance inspection:

12.5.1.1. The inspection must be under illumination about 1000 – 1500 lx, and the distance of view must be at 30cm ± 2cm.

12.5.1.2. The viewing angle should be 45° from the vertical line without reflection light or follows customer's viewing angle specifications.

12.5.1.3. Definition of area: A Zone: Active Area, B Zone: Viewing Area,



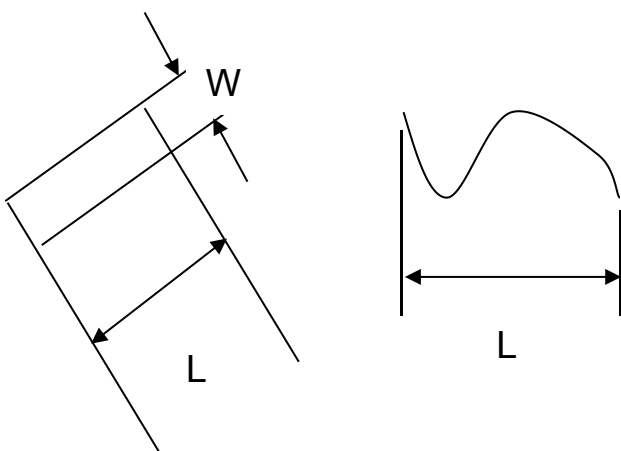
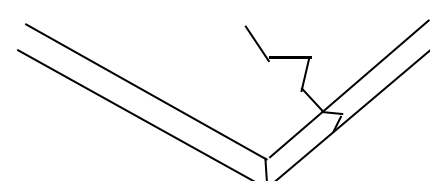
12.5.2. Basic principle:

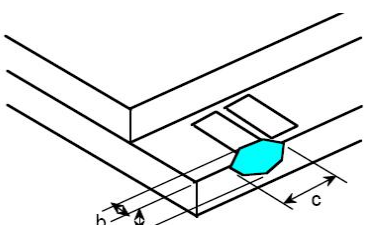
12.5.2.1. A set of sample to indicate the limit of acceptable quality level must be discussed by both us and customer when there is any dispute happened.

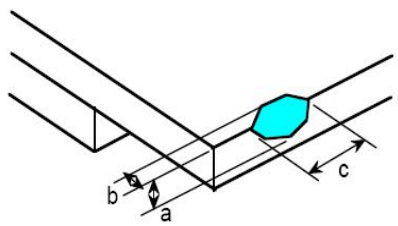
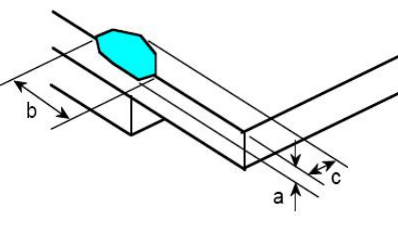
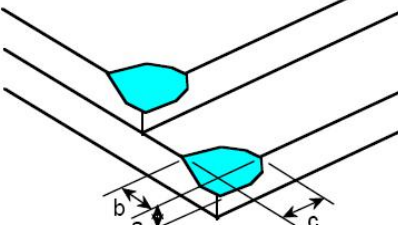
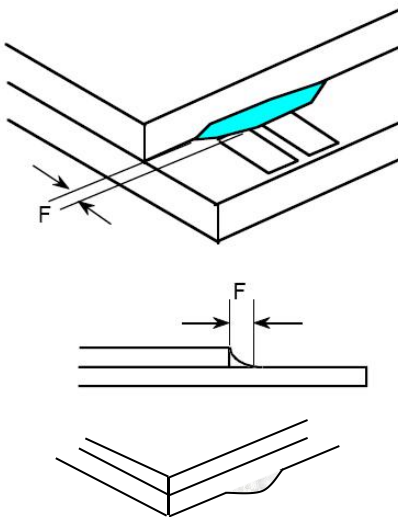
12.5.2.2. New item must be added on time when it is necessary.

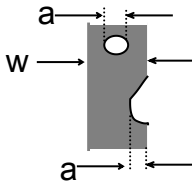
12.6. Inspection Specification for the TFT module

No.	Item	Criteria (Unit: mm)																				
01	Black / White spot Foreign material (Round type) Pinholes Stain Particles inside cell. (Minor defect)	<p>$\phi = (a + b) / 2$</p> <p>Distance between 2 defects should more than 5mm apart.</p>	<table border="1"> <thead> <tr> <th>Size</th> <th>Area</th> <th>Acc. Qty</th> </tr> </thead> <tbody> <tr> <td>$\phi \leq 0.20$</td> <td></td> <td>Ignore</td> </tr> <tr> <td>$0.20 < \phi \leq 0.50$</td> <td></td> <td>$N \leq 3$</td> </tr> <tr> <td>$0.50 < \phi$</td> <td></td> <td>0</td> </tr> </tbody> </table>		Size	Area	Acc. Qty	$\phi \leq 0.20$		Ignore	$0.20 < \phi \leq 0.50$		$N \leq 3$	$0.50 < \phi$		0						
			Size	Area	Acc. Qty																	
$\phi \leq 0.20$		Ignore																				
$0.20 < \phi \leq 0.50$		$N \leq 3$																				
$0.50 < \phi$		0																				
02	Electrical Defect (Minor defect)	<table border="1"> <thead> <tr> <th></th> <th>Display Area</th> <th>Total</th> <th rowspan="3">Note 1</th> </tr> </thead> <tbody> <tr> <td>Bright dot</td> <td>$N \leq 2$</td> <td>$N \leq 2$</td> </tr> <tr> <td>Dark dot</td> <td>$N \leq 4$</td> <td>$N \leq 4$</td> </tr> <tr> <td>Total dot</td> <td>$N \leq 4$</td> <td>$N \leq 4$</td> <td></td> </tr> <tr> <td>Mura</td> <td colspan="2">Not visible through 5% ND filters.</td> <td>Note 2</td> </tr> </tbody> </table> <p>Remark: 1. Bright dot caused by scratch and foreign object accords to item 1.</p>				Display Area	Total	Note 1	Bright dot	$N \leq 2$	$N \leq 2$	Dark dot	$N \leq 4$	$N \leq 4$	Total dot	$N \leq 4$	$N \leq 4$		Mura	Not visible through 5% ND filters.		Note 2
	Display Area	Total	Note 1																			
Bright dot	$N \leq 2$	$N \leq 2$																				
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Total dot	$N \leq 4$	$N \leq 4$																				
Mura	Not visible through 5% ND filters.		Note 2																			

<p>03</p>	<p>Black and White line Scratch Foreign material (Line type) (Minor defect)</p>	 <table border="1" data-bbox="574 739 1197 1008"> <thead> <tr> <th>Length</th> <th>Width</th> <th>Acc. Qty</th> </tr> </thead> <tbody> <tr> <td>/</td> <td>$W \leq 0.1$</td> <td>Ignore</td> </tr> <tr> <td>$L \leq 2.5$</td> <td>$0.1 < W \leq 0.2$</td> <td>3</td> </tr> <tr> <td>$L > 2.5$</td> <td>$0.2 < W$</td> <td>0</td> </tr> <tr> <td colspan="2">Total</td> <td>3</td> </tr> </tbody> </table> <p>Distance between 2 defects should more than 3mm apart. Scratches not viewable through the back of the display are acceptable.</p>	Length	Width	Acc. Qty	/	$W \leq 0.1$	Ignore	$L \leq 2.5$	$0.1 < W \leq 0.2$	3	$L > 2.5$	$0.2 < W$	0	Total		3
Length	Width	Acc. Qty															
/	$W \leq 0.1$	Ignore															
$L \leq 2.5$	$0.1 < W \leq 0.2$	3															
$L > 2.5$	$0.2 < W$	0															
Total		3															
<p>04</p>	<p>Glass Crack (Minor defect)</p>	 <p>Crack is potential to enlarge, any type is not allowed.</p>															

<p>05</p>	<p>Glass Chipping Pad Area: (Minor defect)</p> 	<table border="1" data-bbox="861 1657 1324 1836"> <thead> <tr> <th>Length and Width</th> <th>Acc. Qty</th> </tr> </thead> <tbody> <tr> <td>$c > 3.0, b < 1.0$</td> <td>1</td> </tr> <tr> <td>$c < 3.0, b < 1.0$</td> <td>3</td> </tr> <tr> <td colspan="2">$a < \text{Glass Thickness}$</td> </tr> </tbody> </table>	Length and Width	Acc. Qty	$c > 3.0, b < 1.0$	1	$c < 3.0, b < 1.0$	3	$a < \text{Glass Thickness}$	
Length and Width	Acc. Qty									
$c > 3.0, b < 1.0$	1									
$c < 3.0, b < 1.0$	3									
$a < \text{Glass Thickness}$										

06	<p>Glass Chipping Rear of Pad Area: (Minor defect)</p> 	<table border="1"> <thead> <tr> <th>Length and Width</th> <th>Acc. Qty</th> </tr> </thead> <tbody> <tr> <td>$c > 3.0, b < 1.0$</td> <td>1</td> </tr> <tr> <td>$c < 3.0, b < 1.0$</td> <td>2</td> </tr> <tr> <td>$c < 3.0, b < 0.5$</td> <td>4</td> </tr> <tr> <td colspan="2" style="text-align: center;">$a < \text{Glass Thickness}$</td> </tr> </tbody> </table>	Length and Width	Acc. Qty	$c > 3.0, b < 1.0$	1	$c < 3.0, b < 1.0$	2	$c < 3.0, b < 0.5$	4	$a < \text{Glass Thickness}$	
Length and Width	Acc. Qty											
$c > 3.0, b < 1.0$	1											
$c < 3.0, b < 1.0$	2											
$c < 3.0, b < 0.5$	4											
$a < \text{Glass Thickness}$												
07	<p>Glass Chipping Except Pad Area: (Minor defect)</p> 	<table border="1"> <thead> <tr> <th>Length and Width</th> <th>Acc. Qty</th> </tr> </thead> <tbody> <tr> <td>$c > 3.0, b < 1.0$</td> <td>1</td> </tr> <tr> <td>$c < 3.0, b < 1.0$</td> <td>2</td> </tr> <tr> <td>$c < 3.0, b < 0.5$</td> <td>4</td> </tr> <tr> <td colspan="2" style="text-align: center;">$a < \text{Glass Thickness}$</td> </tr> </tbody> </table>	Length and Width	Acc. Qty	$c > 3.0, b < 1.0$	1	$c < 3.0, b < 1.0$	2	$c < 3.0, b < 0.5$	4	$a < \text{Glass Thickness}$	
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$a < \text{Glass Thickness}$												
08	<p>Glass Corner Chipping: (Minor defect)</p> 	<table border="1"> <thead> <tr> <th>Length and Width</th> <th>Acc. Qty</th> </tr> </thead> <tbody> <tr> <td>$c < 3.0, b < 3.0$</td> <td>Ignore</td> </tr> <tr> <td colspan="2" style="text-align: center;">$a < \text{Glass Thickness}$</td> </tr> </tbody> </table>	Length and Width	Acc. Qty	$c < 3.0, b < 3.0$	Ignore	$a < \text{Glass Thickness}$					
Length and Width	Acc. Qty											
$c < 3.0, b < 3.0$	Ignore											
$a < \text{Glass Thickness}$												
09	<p>Glass Burr: (Minor defect)</p> 	<table border="1"> <thead> <tr> <th>Length</th> <th>Acc. Qty</th> </tr> </thead> <tbody> <tr> <td>$F < 1.0$</td> <td>Ignore</td> </tr> </tbody> </table> <p>Glass burr don't affect assemble and module dimension.</p>	Length	Acc. Qty	$F < 1.0$	Ignore						
Length	Acc. Qty											
$F < 1.0$	Ignore											

10	<p>FPC Defect: (Minor defect)</p> 	<p>10.1 Dent, pinhole width $a < w/3$. (w: circuitry width.) 10.2 Open circuit is unacceptable. 10.3 No oxidation, contamination and distortion.</p>								
11	Bubble on Polarizer (Minor defect)	<table border="1" data-bbox="737 595 1206 766"> <thead> <tr> <th>Diameter</th> <th>Acc. Qty</th> </tr> </thead> <tbody> <tr> <td>$\varphi \leq 0.30$</td> <td>Ignore</td> </tr> <tr> <td>$0.30 < \varphi \leq 0.50$</td> <td>$N \leq 2$</td> </tr> <tr> <td>$0.50 < \varphi$</td> <td>$N = 0$</td> </tr> </tbody> </table>	Diameter	Acc. Qty	$\varphi \leq 0.30$	Ignore	$0.30 < \varphi \leq 0.50$	$N \leq 2$	$0.50 < \varphi$	$N = 0$
Diameter	Acc. Qty									
$\varphi \leq 0.30$	Ignore									
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$0.50 < \varphi$	$N = 0$									
12	Dent on Polarizer (Minor defect)	<table border="1" data-bbox="737 837 1206 1008"> <thead> <tr> <th>Diameter</th> <th>Acc. Qty</th> </tr> </thead> <tbody> <tr> <td>$\varphi \leq 0.25$</td> <td>Ignore</td> </tr> <tr> <td>$0.25 < \varphi \leq 0.50$</td> <td>$N \leq 4$</td> </tr> <tr> <td>$0.50 < \varphi$</td> <td>None</td> </tr> </tbody> </table>	Diameter	Acc. Qty	$\varphi \leq 0.25$	Ignore	$0.25 < \varphi \leq 0.50$	$N \leq 4$	$0.50 < \varphi$	None
Diameter	Acc. Qty									
$\varphi \leq 0.25$	Ignore									
$0.25 < \varphi \leq 0.50$	$N \leq 4$									
$0.50 < \varphi$	None									
13	Bezel	<p>13.1 No rust, distortion on the Bezel. 13.2 No visible fingerprints, stains or other contamination.</p>								
14	Touch Panel	<p>D: Diameter W: width L: length 14.1 Spot: $D < 0.25$ is acceptable $0.25 \leq D \leq 0.4$ 2dots are acceptable and the distance between defects should more than 10 mm. $D > 0.4$ is unacceptable 14.2 Dent: $D > 0.40$ is unacceptable 14.3 Scratch: $W \leq 0.03$, $L \leq 10$ is acceptable, $0.03 < W \leq 0.10$, $L \leq 10$ is acceptable Distance between 2 defects should more than 10 mm. $W > 0.10$ is unacceptable.</p>								
15	PCB	<p>15.1 No distortion or contamination on PCB terminals. 15.2 All components on PCB must same as documented on the BOM/component layout. 15.3 Follow IPC-A-600F.</p>								
16	Soldering	Follow IPC-A-610C standard								

17	Electrical Defect (Major defect)	<p>The below defects must be rejected.</p> <p>17.1 Missing vertical / horizontal segment, 17.2 Abnormal Display. 17.3 No function or no display. 17.4 Current exceeds product specifications. 17.5 LCD viewing angle defect. 17.6 No Backlight. 17.7 Dark Backlight. 17.8 Touch Panel no function.</p>
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Remark: LCD Panel Broken shall be rejected. Defect out of LCD viewing area is acceptable.

12.7. Classification of Defects

12.7.1. Visual defects (Except no / wrong label) are treated as minor defect and electrical defect is major.

12.7.2. Two minor defects are equal to one major in lot sampling inspection.

12.8. Identification/marketing criteria

Any unit with illegible / wrong /double or no marking/ label shall be rejected.

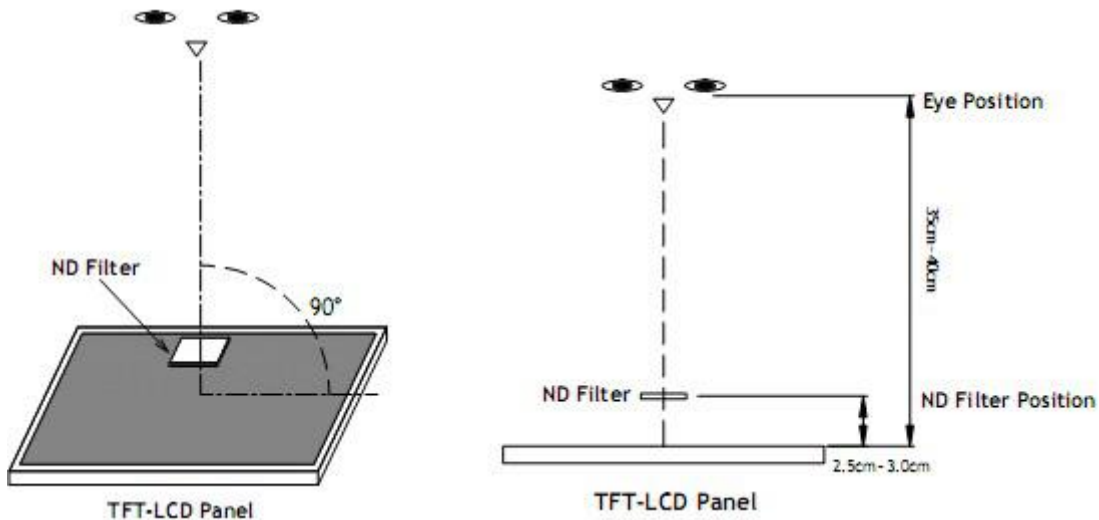
12.9. Packing

12.9.1. There should be no damage of the outside carton box, each packaging box should have one identical label.

12.9.2. Modules inside package box should have compliant mark.

12.9.3. All direct package materials shall offer ESD protection.

Note1: Bright dot is defined as the defective area of the dot is larger than 50% of one sub-pixel area.



Bright dot: The bright dot size defect at black display pattern. It can be recognized by 2% transparency of filter when the distance between eyes and panel is $350\text{mm} \pm 50\text{mm}$.

Dark dot: Cyan, Magenta or Yellow dot size defect at white display pattern. It can be recognized by 5% transparency of filter when the distance between eyes and panel is $350\text{mm} \pm 50\text{mm}$.

Note2: Mura on display which appears darker / brighter against background brightness on parts of display area.

13. Reliability Specification

No	Item	Condition	Quantity	Criteria
1	High Temperature Operating	70°C, 96Hrs	2	GB/T2423.2-2008
2	Low Temperature Operating	-20°C, 96Hrs	2	GB/T2423.1-2008
3	High Humidity	60°C, 90%RH, 96Hrs	2	GB/T2423.3-2016
4	High Temperature Storage	80°C, 96Hrs	2	GB/T2423.2-2008
5	Low Temperature Storage	-30°C, 96Hrs	2	GB/T2423.1-2008
6	Thermal Cycling Test	-20°C, 60min ~ 70°C, 60min, 20 cycles.	2	GB/T2423.22-2012
7	Packing vibration	Frequency range:10Hz~50Hz Acceleration of gravity:5G X, Y, Z 30 min for each direction.	2	GB/T5170.14-2009
8	Electrical Static Discharge	Air: $\pm 4KV$ 150pF/330 Ω Contact: $\pm 2KV$ 150pF/330 Ω	2	GB/T17626.2-2018
9	Drop Test (Packaged)	Height:72cm(weight $\leq 10kg$),60cm (weight $> 10kg$) 1 corner, 3 edges, 6 surfaces.	2	GB/T2423.8-1995

Note1. No deflection cosmetic and operational function allowable.

Note2. Total current Consumption should be below double of initial value

14. Precautions and Warranty

14.1. Safety

- 14.1.1. The liquid crystal in the LCD is poisonous. Do not put it in your mouth. If the liquid crystal touches your skin or clothes, wash it off immediately using soap and water.
- 14.1.2. Since the liquid crystal cells are made of glass, do not apply strong impact on them. Handle with care.

14.2. Handling

- 14.2.1. Reverse and use within ratings in order to keep performance and prevent damage.
- 14.2.2. Do not wipe the polarizer with dry cloth, as it might cause scratch. If the surface of the LCD needs to be cleaned, wipe it swiftly with cotton or other soft cloth soaked with petroleum IPA, do not use other chemicals.

14.3. Storage

- 14.3.1. Do not store the LCD module beyond the specified temperature ranges.
- 14.3.2. Strong light exposure causes degradation of polarizer and color filter

14.4. Metal Pin (Apply to Products with Metal Pins)

14.4.1. Pins of LCD and Backlight

14.4.1.1. Solder tip can touch and press on the tip of Pin LEAD during the soldering

14.4.1.2. Recommended Soldering Conditions

Solder Type: Sn96.3~94-Ag3.3~4.3-Cu0.4~1.1

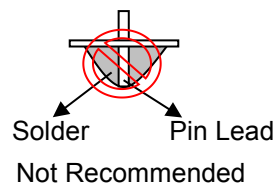
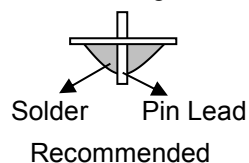
Maximum Solder Temperature: 370 °C

Maximum Solder Time: 3s at the maximum temperature

Recommended Soldering Temp: 350±20 °C

Typical Soldering Time: ≤3s

14.4.1.3. Solder Wetting



14.4.2. Pins of EL

14.4.2.1. Solder tip can touch and press on the tip of EL leads during soldering.

14.4.2.2. No Solder Paste on the soldering pad on the motherboard is recommended.

14.4.2.3. Recommended Soldering Conditions

Solder type: Nippon Alimit Leadfree SR-34, size 0.5mm

Recommended Solder Temperature: 270~290 °C

Typical Soldering Time: ≤2s

Minimum solder distance from EL lamp (body): 2.0mm

14.4.2.4. No horizontal press on the EL leads during soldering.

14.4.2.5. 180° bend EL leads three times is not allowed.

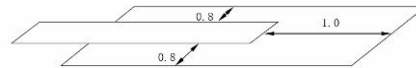
14.4.2.6. Solder Wetting



14.4.2.7. The type of the solder iron:



14.4.2.8. Solder Pad



14.5. Operation

- 14.5.1. Do not drive LCD with DC voltage
- 14.5.2. Response time will increase below lower temperature
- 14.5.3. Display may change color with different temperature
- 14.5.4. Mechanical disturbance during operation, such as pressing on the display area, may cause the segments to appear "fractured".
- 14.5.5. Do not connect or disconnect the LCM to or from the system when power is on.
- 14.5.6. Never use the LCM under abnormal condition of high temperature and high humidity.
- 14.5.7. Module has high frequency circuits. Sufficient suppression to the electromagnetic interface shall be done by system manufacturers. Grounding and shielding methods may be important to minimize the interference.
- 14.5.8. Do not display the fixed pattern for long time (we suggest the time not longer than one hour) because it may develop image sticking due to the TFT structure.

14.6. Static Electricity

- 14.6.1. CMOS LSIs are equipped in this unit, so care must be taken to avoid the electro-static charge, by ground human body, etc.
- 14.6.2. The normal static prevention measures should be observed for work clothes and benches.
- 14.6.3. The module should be kept into anti-static bags or other containers resistant to static for storage.

14.7. Limited Warranty

- 14.7.1. Our warranty liability is limited to repair and/or replacement. We will not be responsible for any consequential loss.
- 14.7.2. If possible, we suggest customer to use up all modules in six months. If the module storage time over twelve months, we suggest that recheck it before the module be used
- 14.7.3. After the product shipped, any product quality issues must be feedback within three months, otherwise, we will not be responsible for the subsequent or consequential events.

15. Packaging

TBD

16. Outline Drawing

