

	PECIFICA	FIONS			
JSTOMER	: HC	TW185A			
AMPLE CODE	: SH	SH480272T009-IAF01			
ASS PRODUCTION CODE	: PH	PH480272T009-IAF01			
AMPLE VERSION	: 02				
PECIFICATIONS EDITION	: 00	6			
RAWING NO. (Ver.)	: LN	ID-PH480272T0	09-IAF01 (Ver.004)		
CKAGING NO. (Ver.)	: РК	G-PH480272T0	09-IAF01 (Ver.002)		
Approved	Checke	Date:			
Approved 黃秋源 Oliver Huang	Checke 石建莊 Stone Sh	d	Designer 黃俊清 Ackey Huang		



History of Version

Date	Ver.	Edi.	Description	Page	Design by
12/25/2015	01	001	New Drawing.	-	Ryan
02/05/2016	01	001	New Sample.	-	Ryan
01/11/2018	01	003	Update Average Brightness.	6	Ryan
09/16/2019	01	004	Update Optical Characteristics.	6	Ackey
09/16/2019	01	005	Modify CTP.	Appendix	Ackey
12/08/2020	02	006	Second Sample.	-	Ackey
					1



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Note: For detailed information please refer to IC data sheet:

TFT LCD : ILITEK---- ILI6480B CTP : Sitronix—ST1633



1.1 Features

Item	Standard Value
Display Type	480 * 3 (RGB) * 272 Dots
LCD Type	Normally white TN, Transmissive Type
Screen size(inch)	4.3"(Diagonal)
Viewing Direction	6 O'clock
Color configuration	R,G, B vertical stripe
Backlight	White LED B/L
Display Interface	Digital 24-bits RGB
Driver IC	ILI6480B
	THIS PRODUCT CONFORMS THE ROHS OF PTC
ROHS	Detail information please refer website :
	http://www.powertip.com.tw/news_detail.php?Key=1&cID=1

1.2 Mechanical Specifications

Item	Standard Value	Unit
Outline Dimension	105.5(W) x 67.2 (L) x 2.6(H) +0,-0.2	mm

LCD panel

Item	Standard Value			
Viewing Area	96.7 (W) * 55.3 (L)	mm		
Active Area	95.04 (W) x 53.856 (L)			
Pixel Size	0.198 (W) * 0.198 (H)	mm		

Touch panel

Item	Standard Value	Unit
Viewing Area	97.1 (W) x 55.9 (L)	mm

Note : For detailed information please refer to LCM drawing.



1.3 Absolute Maximum Ratings

Module

Item	Symbol	Condition	Min.	Max.	Unit
System Power Supply Voltage	VDD	GND=0	-0.5	+5.0	V
Operating Temperature	Тор	-	-20	+70	°C
Storage Temperature	Tst	-	-30	+80	°C
Storage Humidity	HD	Ta ≦ 60 °C	10	90	%RH

1.4 DC Electrical Characteristics

GND = 0V Ta = 25°C

Module	ModuleGND = 0V, Ta = 25°C								
Item	Symbol	Condition	Min.	Тур.	Max.	Unit			
	VDD	-	3.0	3.3	3.6	V			
Power supply	VGH	-	-	15	-	V			
	VGL		-	-10	-	V			
"H" Input Voltage	VIH	-	0.7*VDD	-	VDD	V			
"L" Input Voltage	VIL	·	GND	-	0.3* GND	V			
"H" Output Voltage	Vон	-	VDD-0.4	-	VDD	V			
"L" Output Voltage	Vol	-	GND	-	GND +0.4	V			
Supply Current	IDD	VDD=3.3V Pattern= Full display *1	-	15	25	mA			

Note1: Maximum current display.



1.5 Optical Characteristics

TFT LCD Panel

VDD =3.3V, Ta=25°C

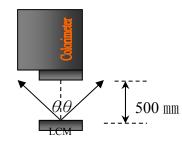
ltem		Symbol	Condition	Min.	Тур.	Max.	unit	
Response time		Tr + Tf	Ta = 25°C θX, θY = 0°	-	35	53	ms	Note2
	Тор	θY+		-	80	-		
Viewing angle	Bottom	θY-	- CR≥10	-	80	ľ	Dog	Note4
	Left	θХ-		-	80	ľ	Deg.	NOLE4
	Right	θX+	1	-	80	1		
Contrast rati	0	CR		480	600	-	-	-
	\A/bite	Х		0.23	0.28	0.33		
	White	Y	Ta = 25°C	0.27	0.32	0.37		
	Ded	Х		0.52	0.57	0.62		
Color of CIE Coordinate	Red	Y		0.28	0.33	0.38		Note1
Coordinate	Groop	Х	θX , $\theta Y = 0^{\circ}$	0.28	0.33	0.38	_	Note I
	Green	Y		0.56	0.61	0.66		
	Dhuo	Х		0.09	0.14	0.19		
	Blue	Y		0.02	0.07	0.12		
Average Brightr Pattern=white di		IV	IF= 20 mA	270	360	-	cd/m ²	Note1
Uniformity		∆B	IF= 20 mA	70	-	-	%	Note1

Note1:

 $1 : \triangle B=B(min) / B(max) \times 100\%$

- 2 : Measurement Condition for Optical Characteristics:
 - a : Environment: 25°C±5°C / 60±20%R.H , no wind , dark room below 10 Lux at typical lamp current and typical operating frequency.
 - b : Measurement Distance: 500 ± 50 mm \rightarrow (θ = 0°)
 - c : Equipment: TOPCON BM-7 fast , (field 1°) , after 10 minutes operation.
 - d : The uncertainty of the C.I.E coordinate measurement ± 0.01 , Average Brightness $\pm 4\%$





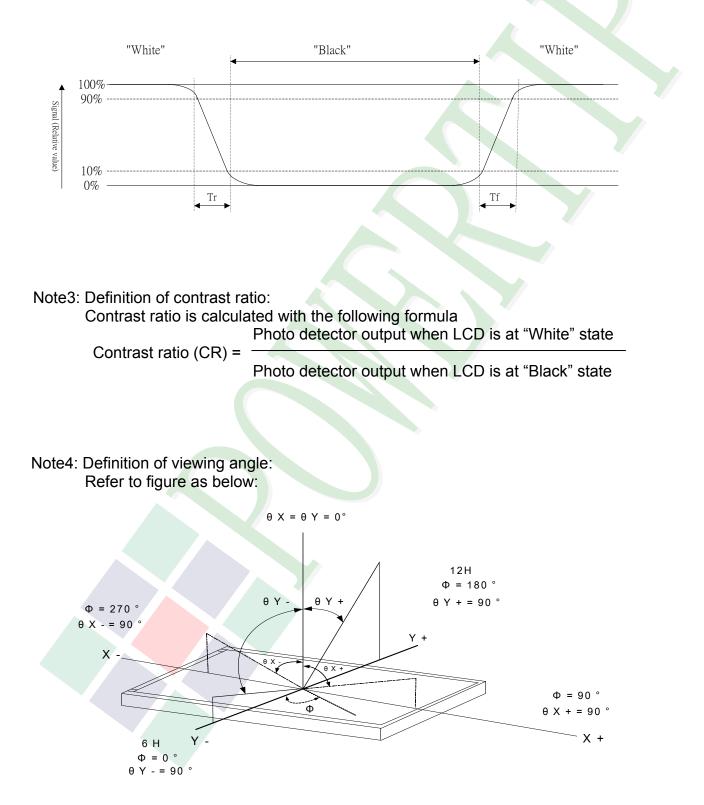
Colorimeter=BM-7 fast



Note2: Definition of response time:

The output signals of photo detector are measured when the input signals are changed from "black" to "white" (falling time) and from "white" to "black" (rising time), respectively. The response time is defined as the time interval between the 10% and 90% of Amplitudes.

Refer to figure as below:





1.6 Backlight Characteristics

Item	Symbol	Conditions	Min.	Max.	Unit
LED Forward Current (Each LED)	IF	Ta =25℃	-	30*1	mA
LED Reverse Voltage (Each LED)	VR	Ta =25℃	-	5.0	V
Power Dissipation	PD	Ta =25 ℃	-	90*8	mW

Electrical / Optical Characteristics

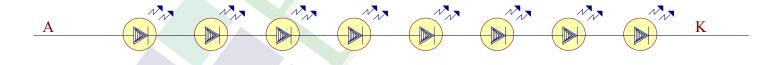
Item	Symbol	Conditions	Min.	Тур.	Max.	Unit
Forward Voltage	VF	IF=20mA	23.5	25.6	Y -	V
LED life time*1	-	-	30,000			Hr
Color			White			

*1 : The "LED life time" is defined as the module brightness decrease to 50% original

brightness at Ta=25°C and I∟=20mA. The LED lifetime could be decreased if

operating I∟ is lager than 20 mA.

Internal Circuit Diagram





1.7 Touch Panel Characteristics

Features

Item	Standard Value
Touch Panel Size	4.3 Inch
Touch type	Projective Capacitive Touch Panel
Input Method	True Multi-touch with up to 5 Points of Absolution X and Y coordinates
Output Interface	l ² C
IC	ST1633

Absolute Maximum Ratings

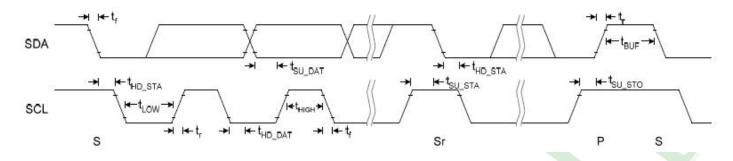
Item	Symbol	Condition	Min.	Max.	Unit
Supply voltage	TPVDD	-	-0.3	3.6	V
Operating Temperature	Тор	-	-20	+70	°C
Storage Temperature	Тѕт		-30	+80	°C

DC Electrical Characteristics

Item	Symbol	Condition	Min.	Тур.	Max.	Unit
Power Supply Voltage	TPVDD	-	3.0	3.3	3.6	V
Input High Voltage	VIH	-	0.7 TPVDD	-	TPVDD	V
Input Low Voltage VI		-	-0.3	-	0.3 TPVDD	V
Output High Voltage	VOH	IOH=-0.1mA	0.7 TPVDD	-	-	V
Output Low Voltage	VOL	IOL=+0.1mA	-	-	0.3 TPVDD	V



1.7.1 I²C Timing Characteristics



Symbol	Parameter		Rating		- Unit
Symbol	Falameter	Min.	Тур	Max. 400 - - 300 300 - - - 09 -	Unit
fscl	SCL clock frequency	0	-	400	kHz
tLOW	Low period of the SCL clock	1.3	-	-	us
tHigh	High period of SCL clock	0.6	_	-	us
tf	Signal falling time	-	-	300	ns
tr	Signal rising time	-	-	300	ns
tsu_sta	Set up time for a repeated START condition	0.6	-	-	us
thd_sta	Hold time (repeated) START condition. After this period the first clock pulse is generated	0.6	-	-	us
tsu_dat	Data set up time	100	-	-	ns
t _{HD_DAT}	Data hold time	0	-	09	us
tsu_sто	Set up time for STOP condition	0.6	-	-	us
tbuf	Bus free time between a STOP and START condition	1.3	-	-	us
Cb	Capacitive load for each bus line	-	-	400	pF



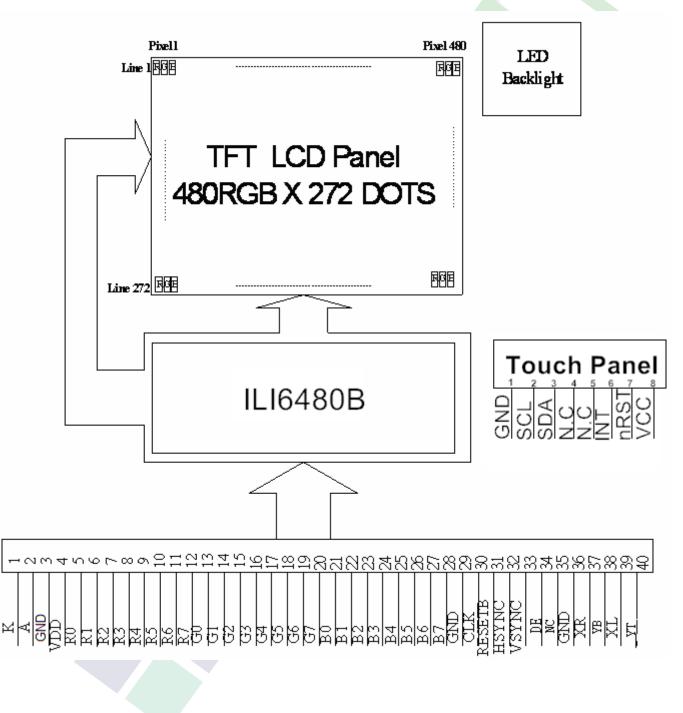
2. MODULE STRUCTURE

2.1 Counter Drawing

2.1.1 LCM Mechanical Diagram

* See Appendix

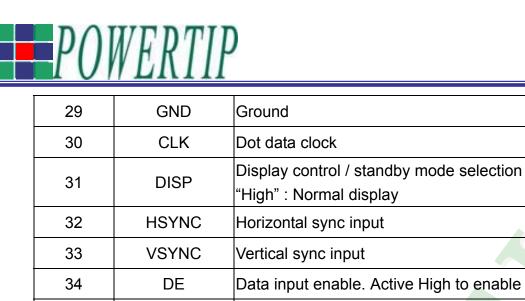
2.1.2 Block Diagram





2.2 Interface Pin Description

Pin No.	Symbol	Function
1	K	Power supply for LED Backlight cathode input
2	А	Power supply for LED Backlight anode input
3	GND	Ground
4	VDD	Digital power
5	R0	Red data bit 0
6	R1	Red data bit 1
7	R2	Red data bit 2
8	R3	Red data bit 3
9	R4	Red data bit 4
10	R5	Red data bit 5
11	R6	Red data bit 6
12	R7	Red data bit 7
13	G0	Green data bit 0
14	G1	Green data bit 1
15	G2	Green data bit 2
16	G3	Green data bit 3
17	G4	Green data bit 4
18	G5	Green data bit 5
19	G6	Green data bit 6
20	G7	Green data bit 7
21	В0	Blue data bit 0
22	B1	Blue data bit 1
23	B2	Blue data bit 2
24	B3	Blue data bit 3
25	B4	Blue data bit 4
26	B5	Blue data bit 5
27	B6	Blue data bit 6
28	B7	Blue data bit 7



51	DISI	"High" : Normal display
32	HSYNC	Horizontal sync input
33	VSYNC	Vertical sync input
34	DE	Data input enable. Active High to enable the data input
35	N.C	Not Connect.
36	GND	Ground
37	XR	Not Connect.
38	YB	Not Connect.
39	XL	Not Connect.
40	YT	Not Connect.

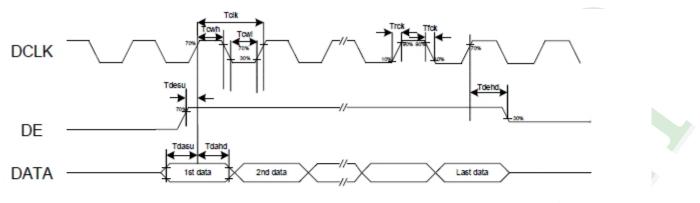
CTP Interface

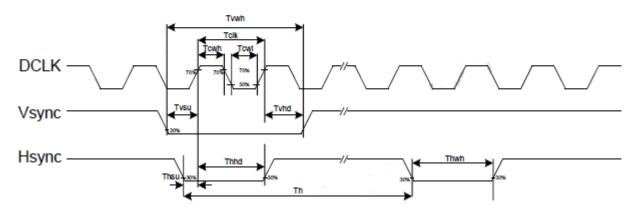
Pin No.	Symbol	Function
1	GND	Ground
2	SCL	I ² C Clock.
3	SDA	I ² C Data
4	N.C	Not Connect.
5	N.C	Not Connect.
6	INT	The interrupt from the CTP to the Host
7	XRES	RESET
8	VDD	Power Supply



2.3 Timing Characteristics

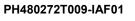
2.3.1 Clock and Data Input Waveforms





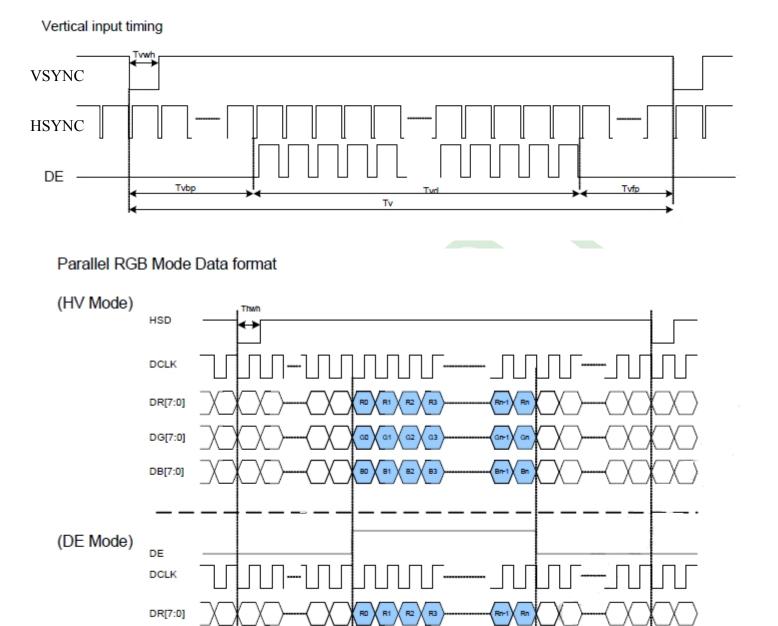


Parameters	Symbol	Min.	Тур.	Max.	Unit	Conditions
System operation timing						
VDD power source slew time	TPOR	-	-	20	ms	From 0V to 99% VDD
GRB pulse width	tRSTW	10	50	-	us	R=10Kohm, C=1uF
Input Output timing						
DCLK clock time	Tclk	33.3	-	-	ns	DCLK=30MHz
DCLK clock low period	Tcwl	40	-	60	%	
DCLK clock high period	Tcwh	40	-	60	%	
Clock rising time	Trck	9	-	-	ns	
Clock falling time	Tfck	9	-	-	ns	
HSD width	Thwh	1	-	-	DCLK	
HSD period time	Th	55	60	65	us	
HSD setup time	Thsu	12	-	- 1	ns	
HSD hold time	Thhd	12	-	-	ns	
VSD width	Tvwh	1	-	(Th	
VSD setup time	Tvsu	12	-	-	ns	
VSD hold time	Tvhd	12		-	ns	
Data setup time	Tdasu	12	-	-	ns	
Data hold time	Tdahd	12	-	-	ns	
DE setup time	Tdesu	12	-	-	ns	
DE hold time	Tdehd	12	-	-	ns	
Source output setting time	Tsst	-	-	TBD	us	10% to 90% CL=60pF, RL=2Kohm
Gate output setting time	Tgst	-	-	1200	ns	10% to 90%, CL=60pF
VCOM output setting time	Tcst	-	-	TBD	us	10% to 90%, CL=40nF, RL=50ohm
Time from VSD to 1st line data input	Tvs	3	8	31	Th	HV mode By HDL[4:0] setting





2.3.2 Data Input Format



ac

BO X B1 X B2

H back porch(Thbp)

63

83

Active Area(Thd) Total Area(Th) a

Bn

H front porch(Thfb)

DG[7:0]

DB[7:0]



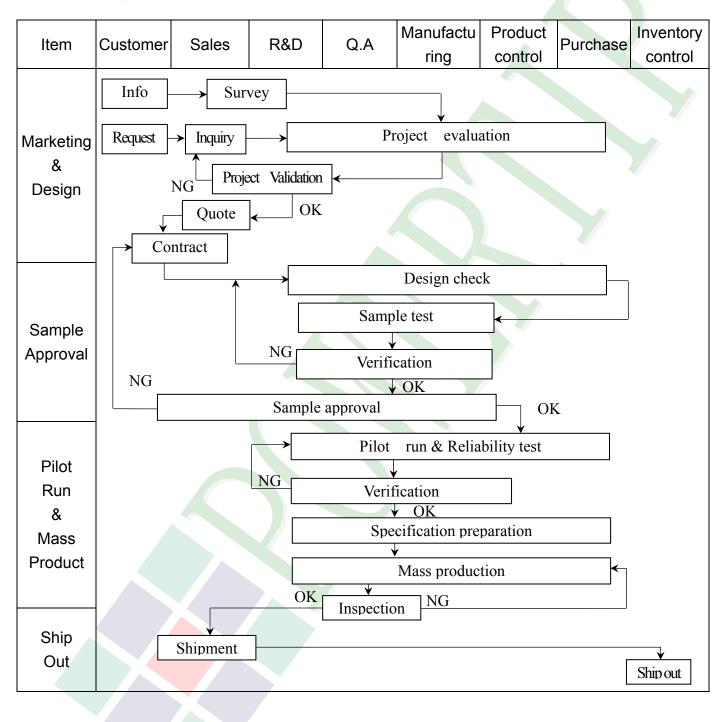
Parallel RGB input timign table

Parameters	Symbol	Value			Unit
		Min.	Тур.	Max.	
DCLK frequency	Fclk	5	9	12	MHz
VSYNC period time	Τv	277	288	400	Н
VSYNC display area	Tvd		272		Н
VSYNC back porch	Tvb	3	8	31	Н
VSYNC front porch	Tvfp	2	8	97	Н
HSYNC period time	Th	520	525	800	DCLK
HSYNC display area	Thd		480		DCLK
HSYNC back porch	Thbp	36	40	255	DCLK
HSYNC front porch	Thfp	4	5	65	DCLK



3. QUALITY ASSURANCE SYSTEM

3.1 Quality Assurance Flow Chart



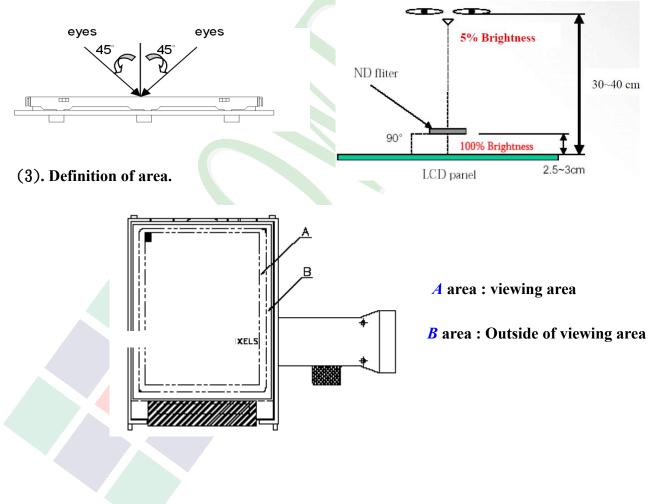


Item	Customer	Sales	R&D	Q.A	Manufact uring	Product control	Purchase	Inventory control
Sales Service	Info Analys	→ Claim -	[Trackin	Failure an Corrective	alysis		
	1. ISO 900 3. Equipme 5. Standarc	ent calibrati	ion	4	Process in Education		nt proposal ing Activitie	es

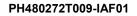
POWERTIP

3.2. Inspection Specification

- ◆Scope: The document shall be applied to TFT-LCD Module for 3. 5″~15″ (Ver.B01).
- ◆Inspection Standard : MIL-STD-105E Table Normal Inspection Single Sampling Level Ⅱ.
- ◆Equipment : Gauge、MIL-STD、Powertip Tester、Sample
- ◆Defect Level:Major Defect AQL: 0.4 ; Minor Defect AQL: 1.5
- **OUT Going Defect Level : Sampling.**
- **♦**Standard of the product appearance test :
 - a. Manner of appearance test :
 - (1). The test best be under 20W×2 fluorescent light(about 300lux ~500lux)
 - and distance of view must be at 30~40 cm.
 - (2). The test direction is base on about around 45° of vertical line.



(4). Standard of inspection : (Unit : mm)



	production.				Major		
Product condition	1.2 Mixed produ	uct types.			Major		
	1.3 Assembled i	in inverse direc ⁻	tion.		Major		
Quantity	2.1The quantity	is inconsistent	with work order of prod	luction.	Major		
Outline dimension	3.1 Product dim diagram.	ension and struct	cure must conform to st	ructure	Major		
	4.1 Missing lin	ne character and	icon.		Major		
	4.2 No function of	or no display.			Major		
	4. 3 Display malfunction.				Major		
Electrical Testing	4. 4 LCD viewing angle defect.						
	4. 5 Current consumption exceeds product specifications.						
	4. 6 Mura can not be seen through 5% ND filter at 50% Gray screen , should be judged by the viewing angle of 90 degree.						
				1			
		Item	Acceptance (Q'ty)				
		Bright Dot	≦ 4				
Dot defect	Dot	Dark Dot	≤ 5				
	Defect	Joint Dot	≦ 3				
(Bright dot >	Total ≤ 7						
Dark dot) On -display 5. 1 Inspection pattern : full white , full black , Red , Green and blue screens. 5. 2 It is defined as dot defect if defect area >1/2 dot.					Minor		

Criterion

1.1The part number is inconsistent with work order of

Item

NO

01

02

03

04

05

◆Specification For TFT-LCD Module 3. 5″~15″:

(Ver.B01)

Level

Major

5. 3 The distance between two dot defect ≥ 5 mm.

5.4 Bright dot that can not be seen through 5% ND filter.



♦ Speci	fication For TFT-L	CD Module 3. 5″~15″:	(Ver.B01)
NO	Item	Criterion	Level
dot < scratch < contamination Round type		6. 1 Round type (Non-display or display) :Dimension (diameter : Φ)Acceptance (Q'ty) $\Phi \leq 0.25$ Ignore $0.25 < \Phi \leq 0.50$ 5 $0.25 < \Phi \leq 0.50$ 0IgnoreIgnoreIgnore $0.25 < \Phi \leq 0.50$ 0 0 Total5	
06	06 $\Phi = (x+y) / 2$ Line type $\downarrow W$ $\downarrow L$	module sizeLength (L)Width (W)Acceptance (Q'ty) A areaW ≤ 0.03 IgnoreL $\leq 0.03 < W \leq 0.03$ IgnoreL $\leq 0.03 < W \leq 0.03$ 410.00.05410.00.052IL ≤ 5.0 0.05 < W ≤ 0.10 W > 0.10As round typeTotal59" to 15"W ≤ 0.05 9" to 15"IgnoreW > 0.10As round typeIgnoreIgnoreL $\leq 0.05 < W \leq 5$ IgnoreIgnoreIgnoreL $\leq 0.05 < W \leq 5$ IgnoreIgnoreIgnoreU ≤ 0.10 IgnoreIgnor	- Minor
		Total5	
07	Polarizer Bubble	Dimension (diameter : Φ)Acceptance (Q'ty) $\Phi \leq 0.25$ Ignore $0.25 < \Phi \leq 0.50$ 4 $0.50 < \Phi \leq 0.80$ 1IgnoreIgnore $\Phi > 0.80$ 0Total5	Minor



◆Specification For TFT-LCD Module 3. 5″~15″: (Ver.B01					
NO	Item	Criterion		Level	
		Z : The thickness of crack	Y : The width of crack. W : terminal length a : LCD side length		
		8.1 General glass chip: 8.1.1 Chip on panel surface and cra	ack between panels:		
08	The crack of glass		Y Y Y Y Y Y Y Y Y Y	Minor	
		Seal width	Y		
		XY	Z		
		\leq a Crack can't enter viewing area	$\leq 1/2 t$		
		≤ a rack can't exceed the half of SP width.	$1/2 t < Z \leq 2 t$		



◆Specification For TFT-LCD Module 3. 5″~15″: (Ver.B01)					
NO	Item	Criterion			
Z : The thickness of crack W : terminal		X : The length of crackY : The width of crack.Z : The thickness of crackW : terminal lengtht : The thickness of glassa : LCD side length			
		X Y Z			
		$ \leq 1/5 \text{ a} \begin{array}{c} \text{Crack can't enter} \\ \text{viewing area} \end{array} \mathbf{Z} \leq 1/2 \text{ t} \end{array} $			
		$\leq 1/5 \text{ a} \begin{array}{c} \text{Crack can't exceed the} \\ \text{half of SP width.} \end{array} 1/2 \text{ t} < \text{Z} \leq 2 \text{ t} \end{array}$			
08	08 The crack of glass 8.2 Protrusion over terminal:				
	8. 2. 1 Chip on electrode pad : X Y Z X Y Z Z X X Y Z Z X X Y Z X				
		X			
		XYZFront $\leq a$ $\leq 1/2$ W $\leq t$			
		From $\leq a$ $\leq 1/2$ W $\leq t$ Back $\leq a$ $\leq W$ $\leq 1/2$ t			



Specification For TFT-LCD Module 3. 5" ~15": (Ver.B01) NO Criterion Level Item Symbols : **X** : The length of crack Y: The width of crack. **Z** : The thickness of crack W: terminal length t : The thickness of glass a : LCD side length 8.2.2 Non-conductive portion: Х Y Z ≦ 1/3 a $\leq W$ ≦t The crack of 80 Minor glass \odot If the chipped area touches the ITO terminal, over 2/3 of the ITO must remain and be inspected according to electrode terminal specifications. 8.2.3 Glass remain : Pitch Х Y Ζ \leq 1/3 W $\leq a$ ≤t 8.2.4 Cracking Not Allowed



◆Specification For TFT-LCD Module 3 5″~15″:

NO	cation For TFT-L Item	Criterion	Level
09	Backlight elements	9. 1 Backlight can't work normally.	Majoi
		9. 2 Backlight doesn't light or color is wrong.	Majo
		9. 3 Illumination source flickers when lit.	Majo
	General appearance	10. 1 Pin type \ quantity \ dimension must match type in structure diagram.	Majo
		10. 2 No short circuits in components on PCB or FPC .	Majo
		10.3 Parts on PCB or FPC must be the same as on the production characteristic chart .There should be no wrong parts , missing parts or excess parts.	Majo
10		10. 4 Product packaging must the same as specified on packaging specification sheet.	Mino
		10. 5 The folding and peeled off in polarizer are not acceptable.	Mino
		10. 6 The PCB or FPC between B/L assembled distance(PCB or FPC) is ≤ 1.5 mm.	Mino



4. RELIABILITY TEST

4.1 Reliability Test Condition

(Ver.B01)

4.	(ver.but)			
NO.	TEST ITEM	TEST CONDITION		
1	High Temperature Storage Test	Keep in +80 ±2°C 240 hrs Surrounding temperature, then storage at normal condition 4hrs.		
2	Low Temperature Storage Test	Keep in −30 ±2°C 240 hrs Surrounding temperature, then storage at normal condition 4hrs.		
3	High Temperature / High Humidity Storage Test	Keep in +60°C / 90% R.H duration for 240 hrs Surrounding temperature, then storage at normal condition 4hrs. (Excluding the polarizer)		
4	Temperature Cycling Storage Test	$30^{\circ}C \rightarrow +25^{\circ}C \rightarrow +80^{\circ}C \rightarrow +25^{\circ}C$ $(30^{\circ}mins) (5^{\circ}mins) (30^{\circ}mins) (5^{\circ}mins)$ $20^{\circ}Cycle$ Surrounding temperature, then storage at normal condition 4hrs.		
5	ESD Test	Air Discharge:Contact Discharge:Apply 2 KV with 5 timesApply 250 V with 5 timesDischarge for each polarity +/-discharge for each polarity +/-1.Temperature ambiance : 15° C ~ 35° C2.Humidity relative : $30\% \sim 60\%$ 3.Energy Storage Capacitance(Cs+Cd) : $150 \text{pF}\pm10\%$ 4.Discharge Resistance(Rd) : $330\Omega\pm10\%$ 5.Discharge, mode of operation :Single Discharge (time between successive discharges at least 1 sec)(Tolerance if the output voltage indication : $\pm5\%$)		
6	Vibration Test (Packaged)	 Sine wave 10~55 Hz frequency (1 min) The amplitude of vibration :1.5 mm Each direction (X × Y × Z) duration for 2 Hrs 		
7	Drop Test (Packaged)	Packing Weight (Kg) 0 ~ 45. 4 45. 4 ~ 90. 8 90. 8 ~ 454 0ver 454 Drop direction :%1 corner / 3 edge	Drop Height (cm) 122 76 61 46 es / 6 sides each 1 times	



5. PRECAUTION RELATING PRODUCT HANDLING

5.1 SAFETY

- 5.1.1 If the LCD panel breaks, be careful not to get the liquid crystal to touch your skin.
- 5.1.2 If the liquid crystal touches your skin or clothes, please wash it off immediately by using soap and water.

5.2 HANDLING

- 5.2.1 Avoid any strong mechanical shock which can break the glass.
- 5.2.2 Avoid static electricity which can damage the CMOS LSI—When working with the module, be sure to ground your body and any electrical equipment you may be using.
- 5.2.3 Do not remove the panel or frame from the module.
- 5.2.4 The polarizing plate of the display is very fragile. So, please handle it very carefully, do not touch, push or rub the exposed polarizing with anything harder than an HB pencil lead (glass, tweezers, etc.)
- 5.2.5 Do not wipe the polarizing plate with a dry cloth, as it may easily scratch the surface of plate.
- 5.2.6 Do not touch the display area with bare hands, this will stain the display area.
- 5.2.7 Do not use ketonics solvent & aromatic solvent. Use with a soft cloth soaked with a cleaning naphtha solvent.
- 5.2.8 To control temperature and time of soldering is $320 \pm 10^{\circ}$ C and 3-5 sec.
- 5.2.9 To avoid liquid (include organic solvent) stained on LCM
- 5.2.10 Caution!(LCM products with Capacitive Touch Panel)

Strong EMI-sources such as switch-mode power supplies (SMPS) can lead to touch malfunction (e.g. ghost-touches).

Therefore, the touch needs to be thoroughly tested inside the target application.

5.2.11 CAUTION: Continuously displaying same static image will result in high possibility of image sticking/image burn-in effect due to TFT panel characteristic.

5.3 STORAGE

- 5.3.1 Store the panel or module in a dark place where the temperature is $25^{\circ}C \pm 5^{\circ}C$ and the humidity is below 65% RH.
- 5.3.2 Do not place the module near organics solvents or corrosive gases.
- 5.3.3 Do not crush, shake, or jolt the module.

5.4 TERMS OF WARRANTY

- 5.4.1 Applicable warrant period The period is within thirteen months since the date of shipping out under normal using and storage conditions.
- 5.4.2 Unaccepted responsibility

This product has been manufactured to your company's specification as a part for use in your company's general electronic products. It is guaranteed to perform according to delivery specifications. For any other use apart from general electronic equipment, we cannot take responsibility if the product is used in nuclear power control equipment, aerospace equipment, fire and security systems or any other applications in which there is a direct risk to human life and where extremely high levels of reliability are required.

