	SPECIFIC	AHONS		
CUSTOMER		РТС		
SAMPLE CODE		SH480272T005-IA	AA07	
ASS PRODUCTION CODE	: I	PH480272T005-IA	A07	
AMPLE VERSION	· ·	01		
PECIFICATIONS EDITION	. (002		
DRAWING NO. (Ver.)		JLMD-PH4802721	Γ005-IAA07_001	
PACKAGING NO. (Ver.)		JPKG-PH4802721	JPKG-PH480272T005-IAA07_001	
C	ustomer A	pproved		
C	ustomer A	approved Date) :	
Approved	ustomer A	Date	Designer	

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History of Version

Date (mm / dd / yyyy)	Ver.	Edi.	Description	Page	Design by
01/30/2019	01	001	New Drawing	-	任健
05/20/2019	01	002	New Sample	-/	任健
					2/
					47

Total: 30 Pages



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Appendix: 1. LCM Drawing

2. Packaging

Note: For detailed information please refer to IC data sheet: ST7257



1. SPECIFICATIONS

1.1 Features

Item	Standard Value
Display Type	480 * 3 (RGB) * 272 Dots
LCD Type	a-Si TFT, Positive/Normally white, Transmissive type
Screen size(inch)	4.3 inch
Viewing Direction	6 O'clock
Color configuration	RGB-Strip
Interface	Digital 24-bits RGB
Other(controller/driver IC)	ST7257
	THIS PRODUCT CONFORMS THE ROHS OF PTC
ROHS	Detail information please refer website :
	http://www.powertip.com.tw/news.php?area_id_view=1085560481/

1.2 Mechanical Specifications

Item	Standard Value	Unit
Outline Dimension	105.5(W) x 67.2 (L) x 2.6(H)	mm

LCD panel

Item	Standard Value	Unit
Viewing Area	96.04 (W) * 54.856 (L)	mm
Active Area	95.04 (W) x 53.856 (L)	mm
Pixel Size	0.198 (W) * 0.198 (H)	mm

Note: For detailed information please refer to LCM drawing



1.3 Absolute Maximum Ratings

Module

Item	Symbol	Condition	Min.	Max.	Unit
System Power Supply Voltage	VDD	GND=0	-0.3	4.6	V
Operating Temperature	TOP	-	-20	70	°C
Storage Temperature	TST	-	-30	80	°C
Storage Humidity	HD	Ta < 60 °C	10	90	%RH

1.4 DC Electrical Characteristics

Module GND = 0V, Ta = 25°C

Item	Symbol	Condition	Min.	Тур.	Max.	Unit
	VDD	-	3.0	3.3	3.6	V
Power Supply Voltage	VGH	-	12	15	16	V
	VGL		-12	-10	-7	V
Input II/I Lovel Voltage	VIH	4	0.7VDD	-	VDD	V
Input H/L Level Voltage	VIL		0	-	0.3VDD	V
Output H/L Level	VOH	-	VDD-0.4	-	VDD	V
Voltage	VOL	-	0	-	GND+0.4	V
Supply Current	IDD	VDD = 3.3 V	-	25	40	mA





1.5 Optical Characteristics

TFT LCD Module

VDD= 3.3 V, Ta=25°C

TI I LOD MOddic						VDD- (,	
Item		Symbol	Condition	Min.	Тур.	Max.	unit	-
Response time	Tr+Tf	25°C	-	-	26	39	ms	-
	Тор	θΥ+		-	60	(-		
Viowing angle	Bottom	θΥ-	CR ≥ 10	-	60	-	Dog	Note 4
Viewing angle	Left	θХ-	CR 2 10	-	60	-	Deg.	Note 4
	Right	θХ+		- (60	-		
Contrast rati	0	CR	-	500	600	-	-	Note 3
	\\/bito	Х		0.24	0.29	0.34		
	White	Υ		0.25	0.30	0.35		
	Red	Х	IF= 20 mA	0.55	0.60	0.65		
Color of CIE		Υ		0.31	0.36	0.41		Natad
Coordinate (B/L + LCD)	Croon	Х	IF= 20 mA	0.29	0.34	0.39	-	Note1
	Green	Υ		0.53	0.58	0.63		
	Dive	X		0.10	0.15	0.20		
	Blue	Υ		0.02	0.07	0.12		
Average Brightness								
Pattern=white display		IV	IF= 20 mA	350	450	-	cd/m2	Note1
(B/L + LCD)*1								
Uniformity (B/L + LCD)*	2	ΔΒ	IF= 20 mA	70	-	-	%	Note1



Note 1:

*1 : △B=B(min) / B(max) * 100%

*2 : Measurement Condition for Optical Characteristics:

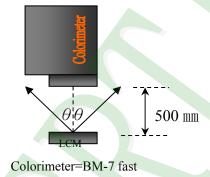
a: Environment: 25°C±5°C / 60±20%R.H, no wind, dark room below 10 Lux at typical lamp current and typical operating frequency.

b : Measurement Distance: $500 \pm 50 \text{ mm}$, $(\theta = 0^\circ)$

c: Equipment: TOPCON BM-7 fast, (field 1°), after 10 minutes operation.

d: The uncertainty of the C.I.E coordinate measurement ±0.01, Average Brightness ± 4%





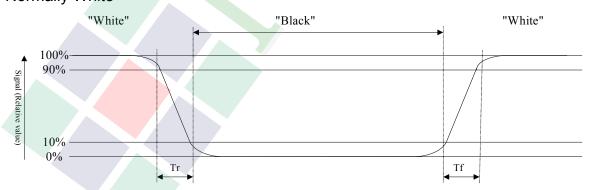
To be measured at the center area of panel with a viewing cone of 1° by Topcon luminance meter BM-7, after 10 minutes operation (module)

Note2: Definition of response time:

The output signals of photo detector are measured when the input signals are changed from "black" to "white" (falling time) and from "white" to "black" (rising time), respectively. The response time is defined as the time interval between the 10% and 90% of Amplitudes.

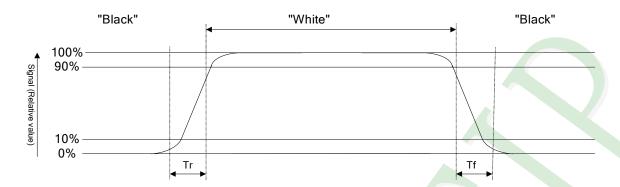
Refer to figure as below:

Normally White





Normally Black



Note3: Definition of contrast ratio:

Contrast ratio is calculated with the following formula

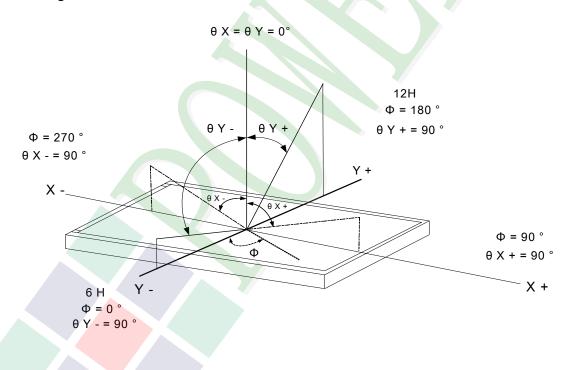
Photo detector output when LCD is at "White" state

Contrast ratio (CR) =

Photo detector output when LCD is at "Black" state

Note4: Definition of viewing angle:

Refer to figure as below:





1.6 Backlight Characteristics

Maximum Ratings

, ,					
Item	Symbol	Conditions	Min.	Max.	Unit
LED Forward Current	IF	Ta =25°ℂ	-	30	mA
LED Reverse Voltage (Each LED)	VR	Ta =25℃	-	5.0	V
Power Dissipation (Each LED)	PD	Ta =25°C	-	816	mW

Electrical / Optical Characteristics

Item	Symbol	Conditions	Min.	Тур.	Max.	Unit
Forward Voltage	VF		23.2	25.6	27.2	V
Average Brightness (Without LCD)	IV	IF=20mA	6500	7800	-	cd/m ²
CIE Color Coordinate	X		0.26	0.28	0.31	
(Without LCD)	Y		0.25	0.27	0.30	-
Color			White			

Circuit diagram:

PIN(A) O D D D D D O PIN(K)

Other Description

Item	Conditions	Description
Life Time	Ta =25°ℂ IF= 20 mA	50000 hrs



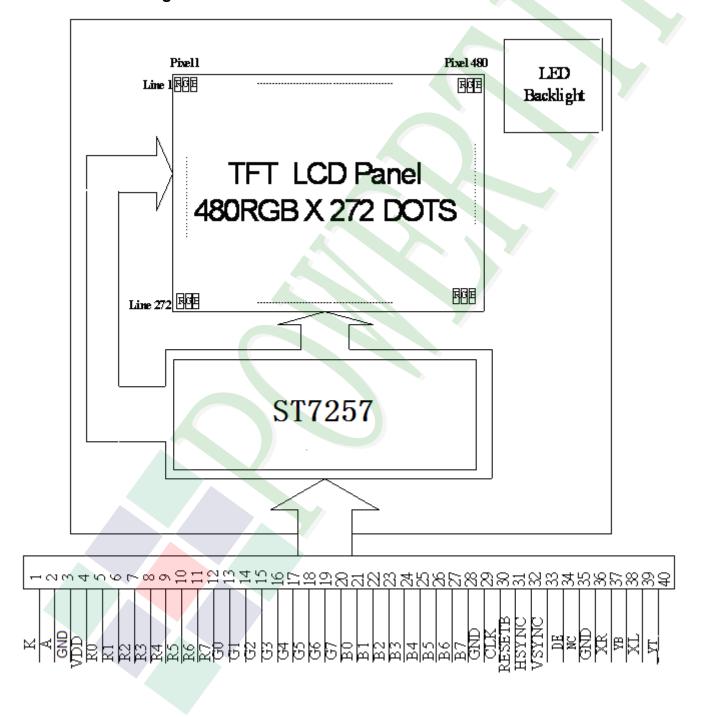
2. MODULE STRUCTURE

2.1 Counter Drawing

2.1.1 LCM Mechanical Diagram

* See Appendix

2.1.2 Block Diagram





2.2 Interface Pin Description

Pin No.	Symbol	Function
1	K	Power supply for LED Backlight cathode input
2	Α	Power supply for LED Backlight anode input
3	GND	Ground
4	VDD	Digital power
5	R0	Red data bit 0
6	R1	Red data bit 1
7	R2	Red data bit 2
8	R3	Red data bit 3
9	R4	Red data bit 4
10	R5	Red data bit 5
11	R6	Red data bit 6
12	R7	Red data bit 7
13	G0	Green data bit 0
14	G1	Green data bit 1
15	G2	Green data bit 2
16	G3	Green data bit 3
17	G4	Green data bit 4
18	G5	Green data bit 5
19	G6	Green data bit 6
20	G7	Green data bit 7



Pin No.	Symbol	Function
21	В0	Blue data bit 0
22	B1	Blue data bit 1
23	B2	Blue data bit 2
24	В3	Blue data bit 3
25	B4	Blue data bit 4
26	B5	Blue data bit 5
27	В6	Blue data bit 6
28	В7	Blue data bit 7
29	GND	Ground
30	CLK	Dot data clock
31	DISP	Display control / standby mode selection "High" : Normal display
32	HSYNC	Horizontal sync input
33	VSYNC	Vertical sync input
34	DE	Data input enable. Active High to enable the data input
35	NC	Not Connect.
36	GND	Ground
37	XR	Not Connect.
38	YB	Not Connect.
39	XL	Not Connect.
40	YT	Not Connect.



2.3 Timing Characteristics

2.3.1 AC Characteristics

AC Electrical Characteristics (VDD=VDDI= 3.3V, AGND= 0V, TA=25°C)

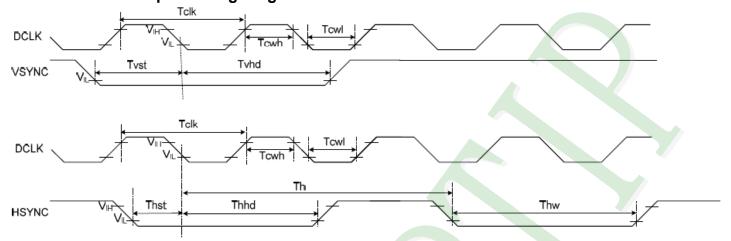
Item	Symbol	Min.	Тур.	Max.	Unit	Conditions
System operation timing						
VDD power source slew time	TPOR	-	-	20	ms	From 0V to 99% VDD
GRB pulse width	tRSTW	10	50	-	us	R=10Kohm, C=1uF
	Inpu	it/ Output	timing			
CLK pulse duty	Tcw	40	50	60	%	
HSYNC period	Th	55	60	65	us	7//
VSYNC setup time	Tvst	12	-		ns	
VSYNC hold time	Tvhd	12	_	-	ns	
HSYNC setup time	Thst	12	-	7	ns	
HSYNC hold time	Thhd	12	-	-	ns	
Data setup time	Tdsu	12	-	-	ns	
Data hold time	Tdhd	12		-	ns	
DE setup time	Tdest	12			ns	
DE hold time	Tdehd	12			ns	
SD output stable time	Tst		-	12	us	Output settled within +20mV
						Loading = 6.8k+28.2pF.
GD output rise and fall time	Tgst	-	-	6	us	Output settled (5%~95%),
						Loading = 4.7k+29.8pF

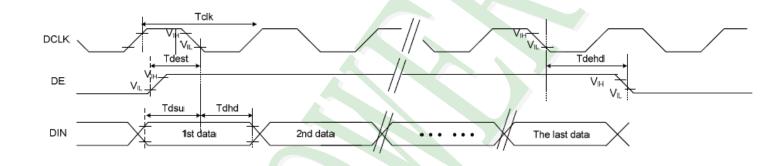




2.3.2 AC Timing Diagram

Clock and Data Input Timing Diagram



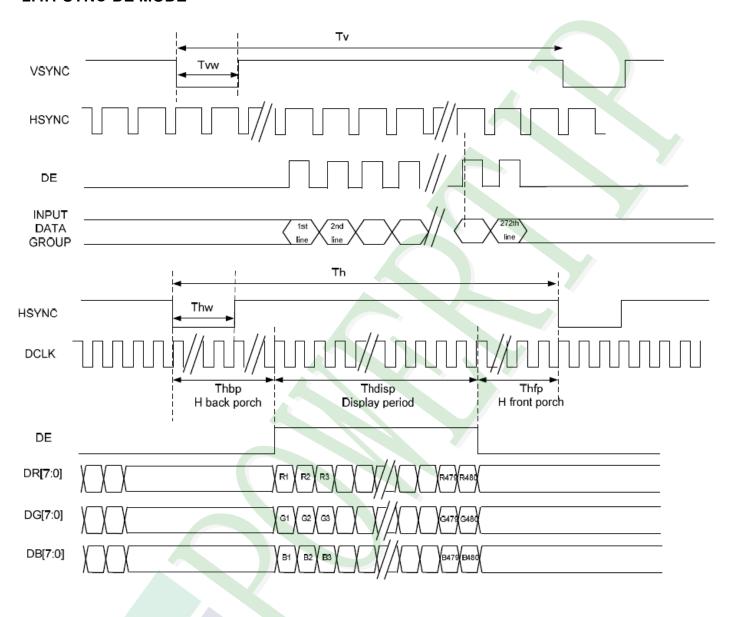






2.4 Data Format

2.4.1 SYNC-DE MODE





2.4.2 Parallel RGB Input Timing Table

	480RGB X 272 Resolution Timing Table						
	Item	Symbol	Min.	Тур.	Max.	Unit	Remark
DCLK Free	quency	Fclk	8	9	12	MHz	
DCLK Peri	od	Tclk	83	111	125	ns	
HSYNC	Period Time	Th	485	531	598	DCLK	
	Display Period	Thdisp		480		DCLK	
	Back Porch	Thbp	3	43	43	DCLK	By H_Blanking setting
	Front Porch	Thfp	2	œ	75	DCLK	
	Pulse Width	Thw	2	4	75	DCLK	
VSYNC	Period Time	Tv	276	292	321	Н	
	Display Period	Tvdisp		272		Н	
	Back Porch	Tvbp	2	12	12	H	By V_Blanking setting
	Front Porch	Tvfp	2	8	37	H	
	Pulse Width	Tvw	2	4	37	H	

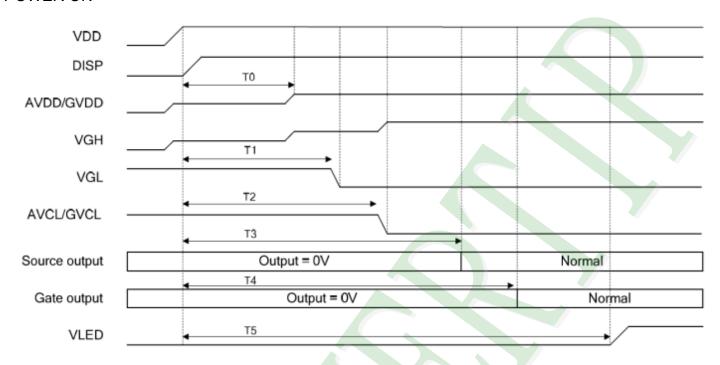
Note: It is necessary to keep Tvbp =12 and Thbp =43 in sync mode. DE mode is unnecessary to keep it.





2.4.3 Power Sequence

POWER ON

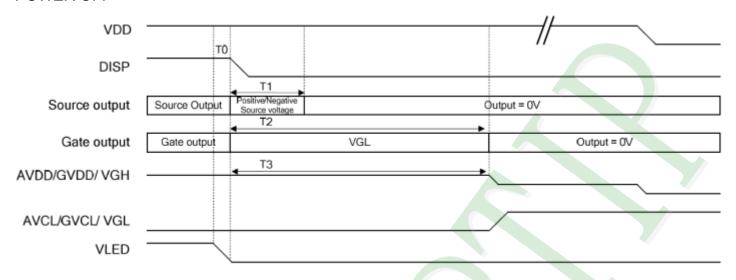


Symbol	Description	Min. Time	Unit
T0	DISP="High" to AVDD/GVDD voltage stability	40	ms
T1	DISP="High" to VGL voltage stability	50	ms
T2	DISP="High" to AVCL/GVCL stability	70	ms
Т3	DISP="High" to Source output	100	ms
T4	DISP="High" to Gate output	110	ms
T5	Black Turn on	130	ms





POWER OFF



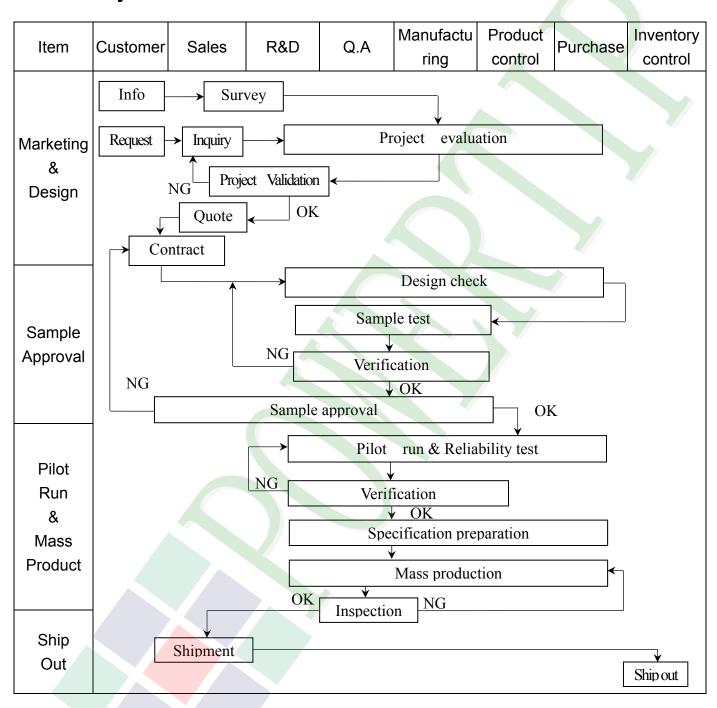
Symbol	Description	Min. Time	Unit
T0	Backlight turn off to DISP="Low"	5	ms
T1	DISP="Low" to Source output disable	20	ms
T2	DISP="Low" to Gate output disable	50	ms
T3	DISP="Low" to Gate output disable	50	ms



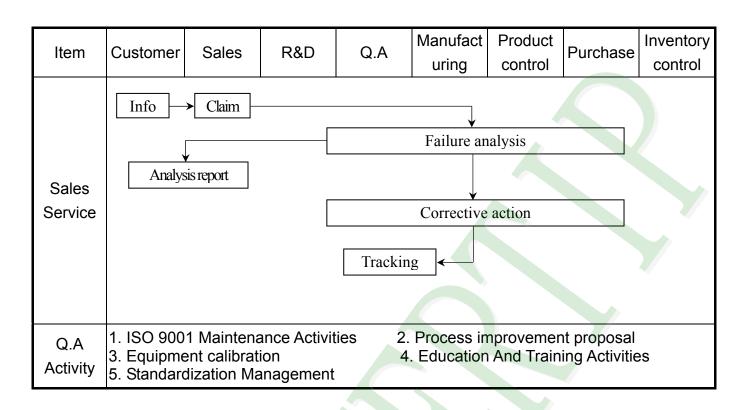


3. QUALITY ASSURANCE SYSTEM

3.1 Quality Assurance Flow Chart



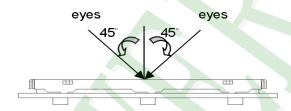




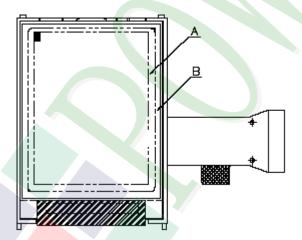


3.2 Inspection Specification

- ◆ Scope: The document shall be applied to TFT-LCD Module for 3. 5" ~15" (Ver.B01).
- ◆ Inspection Standard: MIL-STD-105E Table Normal Inspection Single Sampling Level Ⅱ.
- **♦** Equipment : Gauge · MIL-STD · Powertip Tester · Sample
- ◆ Defect Level: Major Defect AQL: 0.4; Minor Defect AQL: 1.5
- **♦** OUT Going Defect Level: Sampling.
- ◆ Standard of the product appearance test:
 - a. Manner of appearance test:
 - (1). The test best be under 20W×2 fluorescent light, and distance of view must be at 30 cm.
 - (2). The test direction is base on about around 45° of vertical line.



(3). Definition of area.



A area: viewing area

B area: Outside of viewing area

(4). Standard of inspection: (Unit: mm)



◆Specification For TFT-LCD Module 3. 5″ ~15″:

NO	Item	Criterion	Level			
		1. 1The part number is inconsistent with work order of production.	Major			
01	Product condition	1. 2 Mixed product types.	Major			
		1. 3 Assembled in inverse direction.	Major			
02	Quantity	2. 1The quantity is inconsistent with work order of production.	Major			
03	Outline dimension	3. 1 Product dimension and structure must conform to structure diagram.	Major			
		4. 1 Missing line character and icon.	Major			
		4. 2 No function or no display.	Major			
		4. 3 Display malfunction.	Major			
04	Electrical Testing	4. 4 LCD viewing angle defect.				
		4. 5 Current consumption exceeds product specifications.	Major			
		4. 6 Mura can not be seen through 5% ND filter. (Mura: Under the normal examination angle of view,the picture has the non-uniform phenomenon.)				
		Item Acceptance (Q'ty)				
		Bright Dot ≤ 4				
	Dot defect	$\mathbf{Dot} \qquad \mathbf{Dark} \ \mathbf{Dot} \qquad \qquad \leq 5$				
		Defect Joint Dot ≤ 3				
05	(Bright dot \ Dark dot)	Total ≤ 7	Minor			
	On -display	 5. 1 Inspection pattern: full white, full black, Red, Green and blue screens. 5. 2 It is defined as dot defect if defect area >1/2 dot. 5. 3 The distance between two dot defect ≥5 mm. 5. 4 Bright dot that can not be seen through 5% ND filter. 	Willion			



◆Specification For TFT-LCD Module 3. 5" ~15":

NO NO	Item	LOD IV	Criterion				Level		
		6. 1 Ro	ound type (Non-displa				7	
			Dimensio	on (diamete	r : Ф)	Accepta A area	nce (Q'ty) B area		
	Black or white dot > scratch >		0.25	$\Phi \le 0.$ $< \Phi \le 0.$		Ignore 5			
	contamination			$\Phi > 0$ Total	.50	5	- Ignore		
	Round type $\begin{array}{c c} & X & & \downarrow \\ & & Y \\ \hline & & \uparrow \end{array}$	6. 2 Liı	ne type(No	on-display o	or displ	ay):			
06	Y	mo	dule size	Length (L)	W	idth (W)	Acceptanc A area	e (Q'ty) B area	Minor
00	$\Phi = (x+y)/2$			 L ≤10.0		$W \le 0.03$ $< W \le 0.05$	Ignore 4		
	Line type	3.5"	5" to less 9"	L ≦5.0	0.05		As round type	Ignore	
	$ \bigvee \stackrel{\stackrel{\downarrow}{\blacktriangleright}}{\blacktriangleright} W $				Total		5		
	→ı _L			L ≤10.0	0.05	$W \le 0.05$ $< W \le 0.10$	Ignore 5		
		9"	' to 15"			W >0.10	As round type	Ignore	
					Total		5		
		I	Dimension	(diameter :	Ф) —	Accepta A area	nce (Q'ty) B are	ea	
				$\Phi \leq 0.25$		Ignore			
07	Polarizer Bubble			$\Phi \le 0.50$		4			Minor
			0.50 <	$\Phi \le 0.80$ $\Phi > 0.80$		0	Ignor	re	
			7	Total		5			



◆Specification For TFT-LCD Module 3. 5″~15″:

NO	Item		Criterion		Level
			kness of crack	Y : The width of crack. W : terminal length a : LCD side length	
			glass chip: on panel surface and cra	ack between panels:	
		Y	Z	Z X	
08	The crack of glass	SP—	Y [OK]	SP [NG]	Minor
		S	Seal width Z	Y	
		X	Y	Z	
		≦ a	Crack can't enter viewing area	≦1/2 t	
		≦ a	Crack can't exceed the half of SP width.	$1/2 t < Z \leq 2 t$	



◆Specification For TFT-LCD Module 3, 5″ ~15″:

NO	Item		Cri	iterion		Level
	Symbols: X: The length of crack Z: The thickness of crack t: The thickness of glass X: The width of crack W: terminal length a: LCD side length					
		8.1.2 Cor	ner crack:	X		
		X	Y		Z	
		≤1/5 a	Crack can't e viewing are		Z ≤ 1/2 t	
		≤1/5 a	Crack can't exce		$t < Z \leq 2 t$	
08	The second of sleen				_	M:
00	The crack of glass		sion over termin			Minor
		8. 2. 1 Chi	p on electrode p		Z	
				X	W	
			X	Y	Z	
		Front	≦ a	≤ 1/2 W	≦ t	
		Back	≤ a	≦ W	≤ 1/2 t	



♦Specification For TFT-LCD Module 3. 5″ ~15″:

NO	Item	Criterion	Level
	Item The crack of glass		Level



♦Specification For TFT-LCD Module 3. 5″ ~15″:

NO	Item	Criterion	Level
110		9. 1 Backlight can't work normally.	Major
09	Backlight elements	9. 2 Backlight doesn't light or color is wrong.	Major
		9. 3 Illumination source flickers when lit.	Major
	10. 2 No sho 10. 3 Parts produ parts General appearance 10. 4 Produ specif 10. 5 The fo	10. 1 Pin type \quantity \dimension must match type in structure diagram.	Major
		10. 2 No short circuits in components on PCB or FPC.	Major
		10. 3 Parts on PCB or FPC must be the same as on the production characteristic chart .There should be no wrong parts , missing parts or excess parts.	Major
10		10. 4 Product packaging must the same as specified on packaging specification sheet.	Minor
		10. 5 The folding and peeled off in polarizer are not acceptable.	Minor
		10. 6 The PCB or FPC between B/L assembled distance(PCB or FPC) is ≤1.5 mm.	Minor



4. RELIABILITY TEST

4.1 Reliability Test Condition

NO.	TEST ITEM	TEST CONDITION		
140.	ILOTTILIVI			
1	High Temperature Storage Test	Keep in +80 ±2℃ 240 hrs Surrounding temperature, then storage at normal condition 4hrs.		
2	Low Temperature Storage Test	Keep in -30 ±2℃ 240 hrs Surrounding temperature, then storage at normal condition 4hrs.		
3	High Temperature / High Humidity Storage Test	Keep in +60 °C / 90% R.H duration for 240 hrs Surrounding temperature, then storage at normal condition 4hrs. (Excluding the polarizer)		
		-30°C → +25°C → +80°C → +25°C		
		(30mins) (5mins) (30mins) (5mins)		
4	Temperature Cycling	20 Cycle		
	Storage Test	Surrounding temperature, then storage at normal condition		
		4hrs.		
5	ESD Test	Air Discharge: Apply 2 KV with 5 times Discharge for each polarity +/- 1. Temperature ambiance : 15°C~35°C 2. Humidity relative : 30%~60% 3. Energy Storage Capacitance(Cs+Cd) : 150pF±10% 4. Discharge Resistance(Rd) : 330Ω±10% 5. Discharge, mode of operation : Single Discharge (time between successive discharges at least 1 sec) (Tolerance if the output voltage indication : ±5%)		
		1. Sine wave 10~55 Hz frequency (1 min/sweep)		
6	Vibration Test (Packaged)	2. The amplitude of vibration :1.5 mm		
		3. Each direction (X · Y · Z) duration for 2 Hrs		
		Packing Weight (Kg) Drop Height (cm)		
		0 ~ 45.4 122		
7	Drop Test	45.4 ~ 90.8 76		
-	(Packaged)	90.8 ~ 454 61		
		Over 454 46		
		Drop Direction : 1 corner / 3 edges / 6 sides each 1 time		



5. PRECAUTION RELATING PRODUCT HANDLING

5.1 SAFETY

- 5.1.1 If the LCD panel breaks, be careful not to get the liquid crystal to touch your skin.
- 5.1.2 If the liquid crystal touches your skin or clothes, please wash it off immediately by using soap and water.

5.2 HANDLING

- 5.2.1 Avoid any strong mechanical shock which can break the glass.
- 5.2.2 Avoid static electricity which can damage the CMOS LSI—When working with the module, be sure to ground your body and any electrical equipment you may be using.
- 5.2.3 Do not remove the panel or frame from the module.
- 5.2.4 The polarizing plate of the display is very fragile. So , please handle it very carefully ,do not touch , push or rub the exposed polarizing with anything harder than an HB pencil lead (glass , tweezers , etc.)
- 5.2.5 Do not wipe the polarizing plate with a dry cloth, as it may easily scratch the surface of plate.
- 5.2.6 Do not touch the display area with bare hands, this will stain the display area.
- 5.2.7 Do not use ketonics solvent & aromatic solvent. Use with a soft cloth soaked with a cleaning naphtha solvent.
- 5.2.8 To control temperature and time of soldering is 320±10°C and 3-5 sec.
- 5.2.9 To avoid liquid (include organic solvent) stained on LCM.
- 5.2.10 Caution!(LCM products with Capacitive Touch Panel)
 Strong EMI-sources such as switch-mode power supplies (SMPS) can lead to touch malfunction (e.g. ghost-touches).

Therefore, the touch needs to be thoroughly tested inside the target application.

5.3 STORAGE

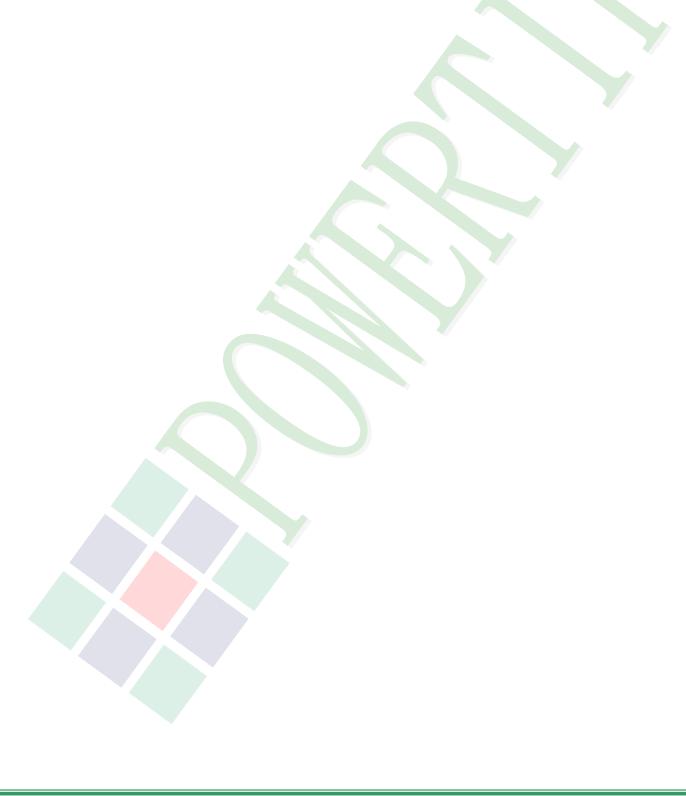
- 5.3.1 Store the panel or module in a dark place where the temperature is 25°C ±5°C and the humidity is below 65% RH.
- 5.3.2 Do not place the module near organics solvents or corrosive gases.
- 5.3.3 Do not crush, shake, or jolt the module.

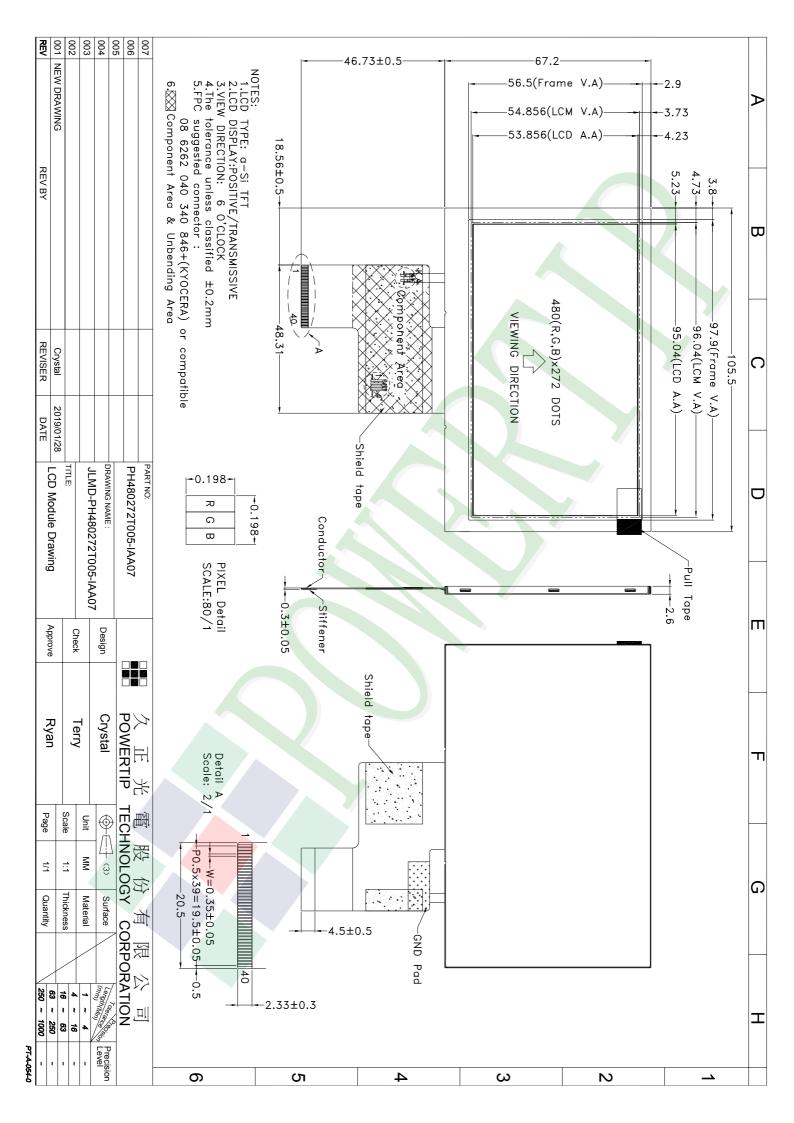
5.4 TERMS OF WARRANTY

- 5.4.1 Applicable warrant period
 - The period is within thirteen months since the date of shipping out under normal using and storage conditions.
- 5.4.2 Unaccepted responsibility
 - This product has been manufactured to your company's specification as a part for

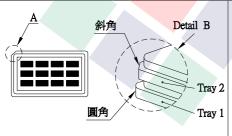


use in your company's general electronic products. It is guaranteed to perform according to delivery specifications. For any other use apart from general electronic equipment, we cannot take responsibility if the product is used in nuclear power control equipment, aerospace equipment, fire and security systems or any other applications in which there is a direct risk to human life and where extremely high levels of reliability are required.





Approve Check Contact Ver.001 LCM包裝規格書 LCM Packaging Specifications Ryan Documents NO. JPKG-PH480272T005-IAA07 Terry Crystal (For Tray) 1.包裝材料規格表 (Packaging Material): (per carton) Item 1Pcs Weight Total Weight No. Dimensions (mm) Quantity 成品 (LCM) PH480272T005-IAA07 105.5 X 67.2 X 2.6 216 1 0.0465 10.044 2 6 多層薄膜(1)POF OTFILM0BA03ABA 19"X350X0.015 3 TRAY 盤 (2)Tray TYSG000000227 352 X 260 X 2.6 60 0.1 6.0 4 内盒(3)Product Box BX36627063ABBA 383 X 270 X 66 0.182 6 1.092 OTPLB00PL08ABA 2 5 550 X 393 X 20 0.0284 0.0568 保利龍板(4)Polylon board 6 外紙箱(5)Carton BX57041027CCBA 570 X 410 X 265 1.0 1 1.0 7 8 9 2.一 整箱總重量 (Total LCD Weight in carton): 3.單箱數量規格表 (Packaging Specifications and Quantity): (1)LCM quantity per box: no per tray x no of tray 9 36 (2) Total LCM quantity in carton: quantity per box x no of boxes 36 6 216 (4)保利龍板 Polylon board Use empty tray 空盤 (1)多層薄膜 POF Put products into the tray (2)TRAY 盤 (4)保利龍板 Tray Polylon board 仆 (3)内盒 Tray stacking Product Box (5)外紙箱 Carton 特 記 事 項 (REMARK)



4.TRAY盤相疊時,需旋轉180度,請詳見B視圖 Rotate tray 180 degrees and place on top of stack. Check the tray stack using Fig. B.