

POWERTIP TECH. CORP.

SPECIFICATIONS

CUSTOMER	PTC
SAMPLE CODE	SH320240T023-IHA03
MASS PRODUCTION CODE	PH320240T023-IHA03
SAMPLE VERSION	01
SPECIFICATIONS EDITION	006
DRAWING NO. (Ver.)	JLMD- PH320240T023-IHA03_004
PACKAGING NO. (Ver.)	JPKG- PH320240T023-IHA03_001

Customer Approved

Date:

Approved	Checked	Designer
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- \Box Preliminary specification for design input
- Specification for sample approval

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History of Version

Date	Ver.	Edi.	Description	Page	Desig n by
03/20/2017	01	001	New Drawing	-	夏子豪
05/25/2017	01	002	New Sample	-	夏子豪
08/7/2017	01	003	Modify LCD	6	夏子豪
12/25/2017	01	004	Modify LCM Drawing	Appendix	夏子豪
3/21/2018	01	005	Modify Reliability Test Condition	27	夏子豪
03/02/2020	01	006	Modify LCM Drawing	Appendix	陳璐
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Note : For detailed information please refer to IC data sheet :

Primacy(TFT LCD): Himax: HX8238-D



1. SPECIFICATIONS

1.1 Features

Main LCD Panel

Item	Standard Value
Display Type	320* (R 、 G 、 B) * 240 Dots
LCD Type	Normally white, Transmissive type
Screen size(inch)	3.5(Diagonal)
Viewing Direction	6 O'clock
Color configuration	R.G.B. vertical stripe
Interface	Digital 24-bits RGB/3 wire SPI
Other	
(controller / driver IC)	Himax: HX8238-D
	THIS PRODUCT CONFORMS THE ROHS OF PTC
ROHS	Detail information please refer web site :
	http://www.powertip.com.tw/news.php?area_id_view=1085560481/

1.2 Mechanical Specifications

Item	Standard Value	Unit
Outline Dimension	76.9(W) * 63.9 (L) * 3.2 (H)	mm

LCD panel

Item	Standard Value	Unit
Active Area	70.08 (W) * 52.56 (L)	

Note : For detailed information please refer to LCM drawing



1.3 Absolute Maximum Ratings

Module

Item	Symbol	Condition	Min.	Max.	Unit
Power Supply Voltage		GND=0	-0.3	4.0	V
Operating Temperature	Top	-	-30	85	°C
Storage Temperature	Tst	-	-40	85	°C
Storage Humidity	HD	Ta < 60 °C	20	90	%RH

1.4 DC Electrical Characteristics

Module

GND = 0V, Ta = 25°C

Item	Symbol	bol Condition		Тур.	Max.	Unit
Power Supply Voltage	DVDD	-	3.0	3.3	3.6	V
V _{COM} High Voltage	Vсомн	-	-	-	5.54	V
V _{COM} Low Voltage	Vcoml	4	-2.8	-	-	V
	Vін		0.8*DV _{DD}	-	DVDD	V
Input H/L Level Voltage	VIL	-	0	-	0.2*DV _{DD}	V
	Vон	-	0.9*DV _{DD}	-	DV _{DD}	V
Output H/L Level Voltage	Vol		-	-	0.1*DV _{DD}	V
Supply Current	IDD	DV _{DD} =3.3V	-	11	17	mA



1.5 Optical Characteristics

TFT LCD Panel

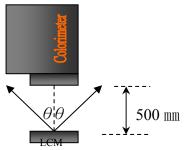
DV_{DD}=3.3V, Ta=25°C

							, 14 20 (
Item		Symbol	Condition	Min.	Тур.	Max.	unit	-
Response time	Tr+Tf	25°C	-	-	40	60	ms	Note 2
	Тор	Θ+		-	60	-		
Viewing angle	Bottom	Θ-	CR ≥ 10	-	60	-	Deg.	Note 1
viewing angle	Left	ΘL		-	60	I	Deg.	NOLE I
	Right	ΘR		-	60	-		
Contrast rati	0	CR	-	500	600	-	-	Note3
	White	Х		0.27	0.32	0.37		
	VVIILE	Y		0.31	0.36	0.41		
	Red	Х	IF= 20 mA	0.60	0.65	0.70		
Color of CIE Coordinate	Reu	Y		0.28	0.33	0.38		
(With LCD)	Green	Х	II – 20 IIIA	0.29	0.34	0.39		
(Green	Y		0.56	0.61	0.66		
	Blue	Х		0.09	0.14	0.19]	Note4
	Diue	Y		0.03	0.08	0.13		
Average Brightness								
Pattern=white display		IV	IF= 20 mA	660	880	-	cd/m ²	
(With LCD)*1								
Uniformity (With LCD)*	2	∆B	IF= 20 mA	80	-	-	%	

Note 4 :

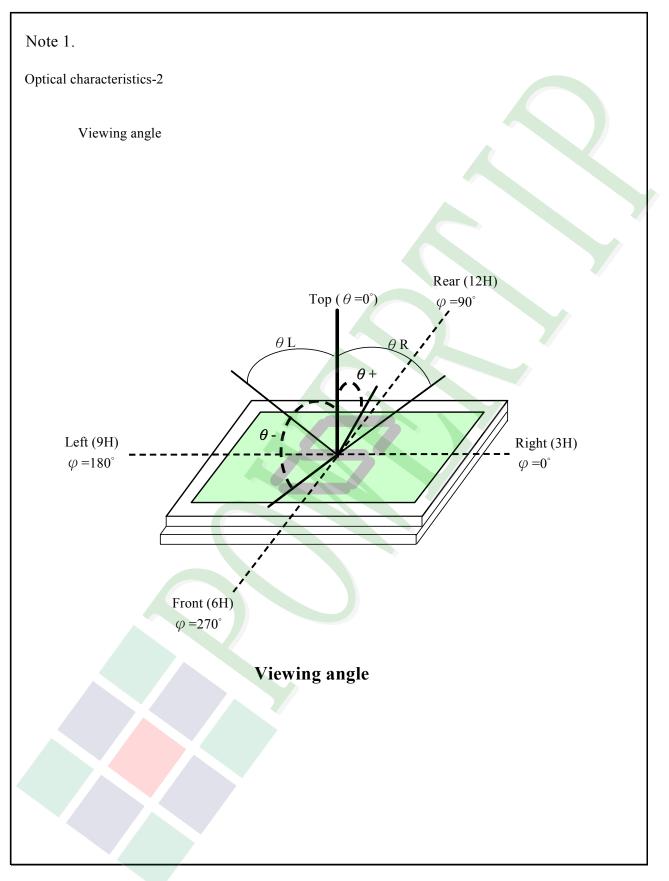
- $1 : \Delta B = B(\min) / B(\max) * 100\%$
- 2 : Measurement Condition for Optical Characteristics:
 - a : Environment: 25℃ ±5℃ / 60±20%R.H, no wind, dark room below 10 Lux at typical lamp current and typical operating frequency.
 - b : Measurement Distance: $500 \pm 50 \text{ mm} \rightarrow (\theta = 0^\circ)$
 - c: Equipment: TOPCON BM-7 fast , (field 1°), after 10 minutes operation.
 - d : The uncertainty of the C.I.E coordinate measurement ± 0.01 , Average Brightness $\pm 4\%$





Colorimeter=BM-7 fast

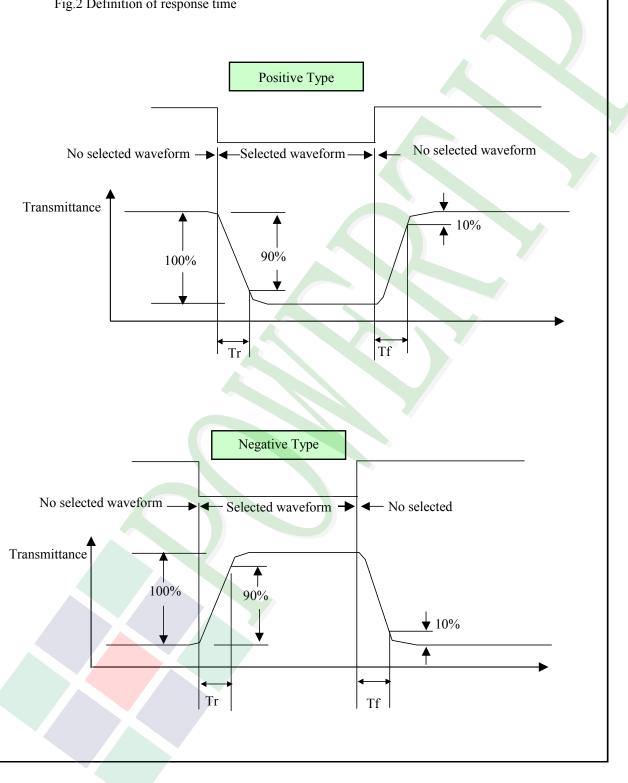




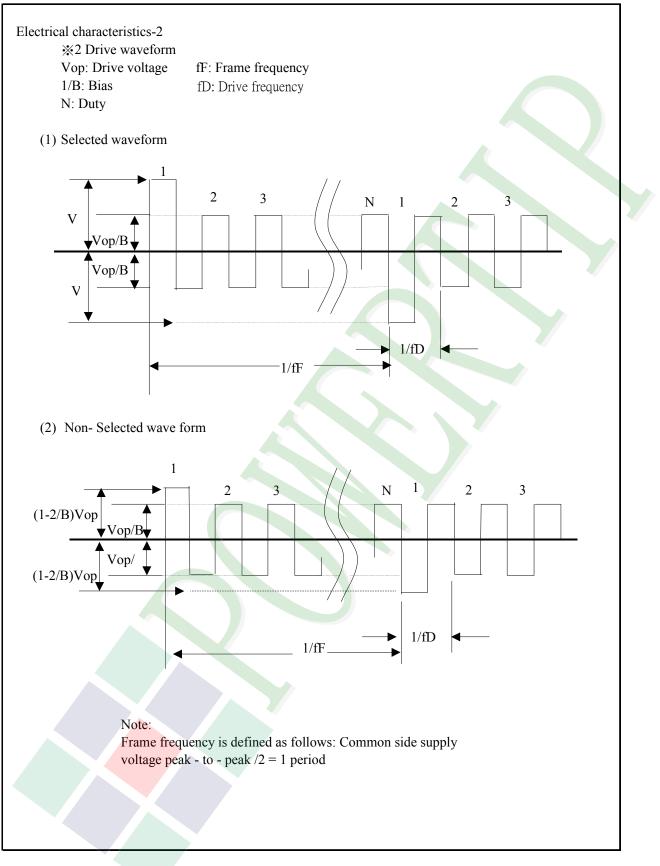


Note 2.

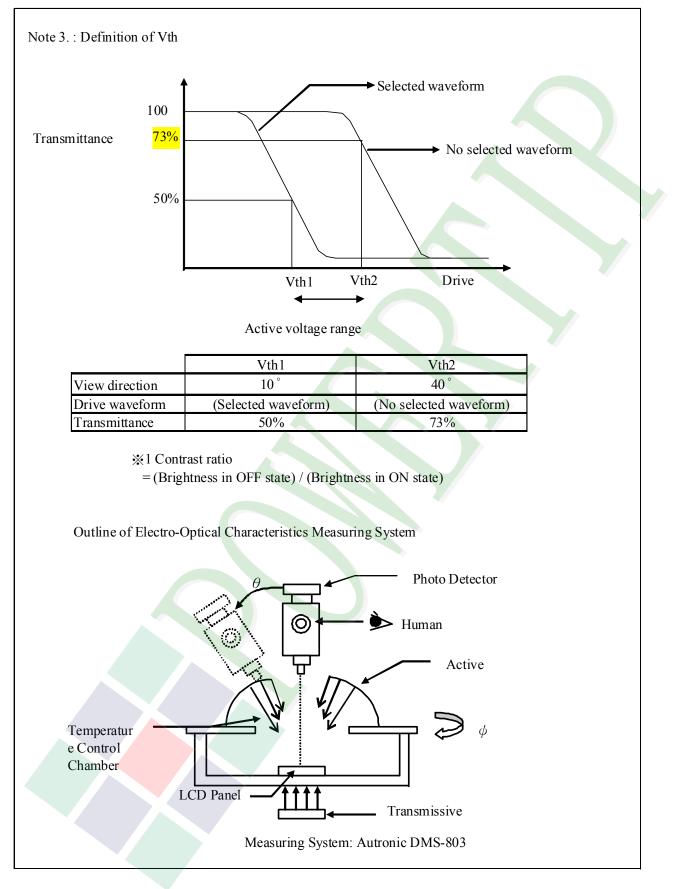
Optical characteristics-3 Fig.2 Definition of response time













1.6 Backlight Characteristics

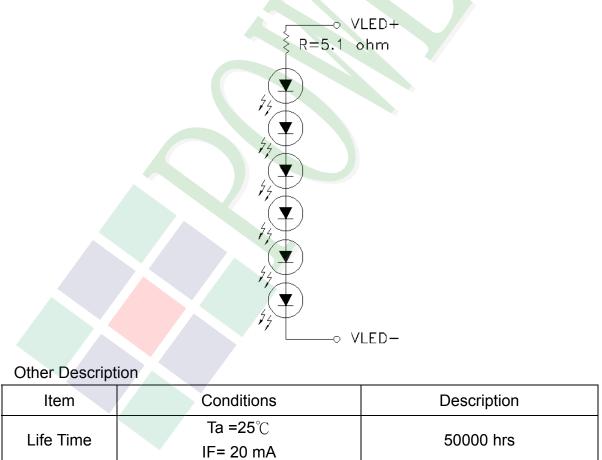
Maximum Ratings

Item	Symbol	Conditions	Min.	Max.	Unit	Remark
Reverse Voltage	IF	Ta=25 ℃	-	20	mA	Each LED
Forward Current	VR	Ta=25 ℃	-	19	V	Each LED
Power dissipation	PD	Ta=25 ℃	-	396	mW	-

Electrical / Optical Characteristics

Item	Symbol	Conditions	Min.	Тур.	Max.	Unit
Forward Voltage	VF	-	18	19	19.8	V
Average Brightness (Without LCD)	IV	IF= 20 mA	7000	8000	-	cd/m ²
CIE Color Coordinate	Х		0.28	0.31	0.33	
(Without LCD)	Y		0.30	0.325	0.35	-
Color			White			

Circuit diagram:



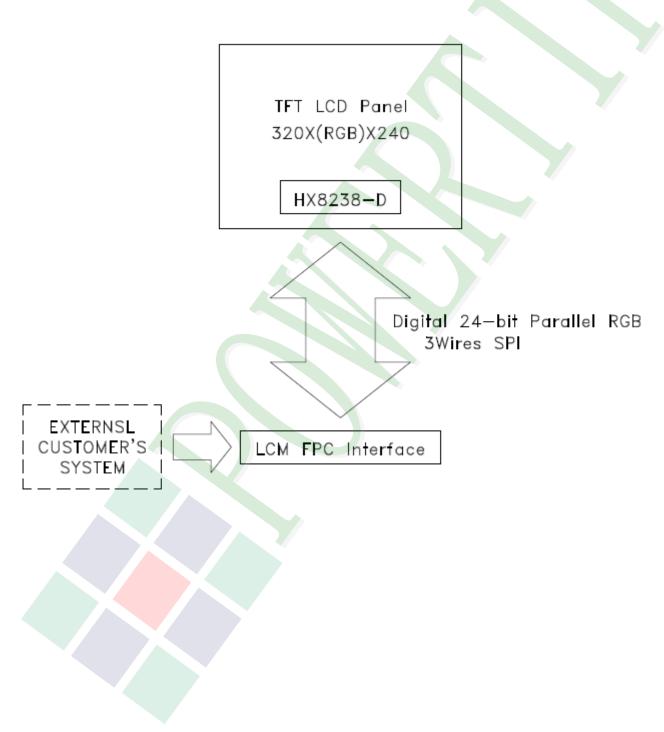


2. MODULE STRUCTURE

2.1 Counter Drawing

2.1.1 LCM Mechanical Diagram

- * See Appendix
- 2.1.2 Block Diagram





2.2 Interface Pin Description

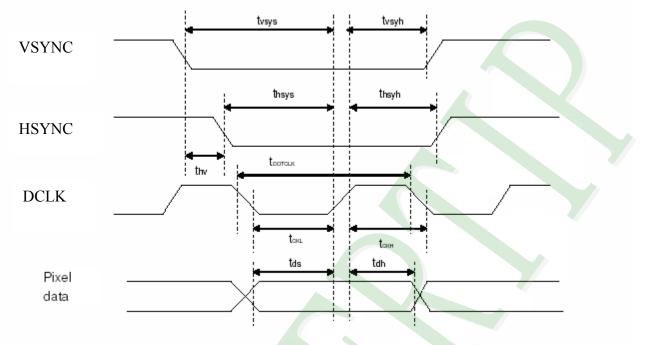
Pin No.	Symbol	Function
1	VLED+	Power For LED backlight (+).
2	VLED+	Power For LED backlight (+).
3	VLED-	Power For LED backlight (-).
4	VLED-	Power For LED backlight (-).
5	GND	Power ground.
6	NC	No connection.
7	DV_DD	Power for Digital Circuit.
8	NC	No connection.
9	DEN	Data input Enable. Active High to enable the data input Bus under "DE Mode".
10	VS	Vertical Sync input. Negative polarity.
11	HS	Horizontal Sync input. Negative polarity.
12	B7	Blue Data(MSB).
13	B6	Blue Data.
14	B5	Blue Data.
15	B4	Blue Data.
16	B3	Blue Data.
17	B2	Blue Data.
18	B1	Blue Data.
19	B0	Blue Data(LSB).
20	G7	Green Data(MSB).
21	G6	Green Data.
22	G5	Green Data.
23	G4	Green Data.
24	G3	Green Data.
25	G2	Green Data.
26	G1	Green Data.
27	G0	Green Data(LSB).
28	R7	Red Data(MSB).



Pin No.	Symbol	Function
29	R6	Red Data.
30	R5	Red Data.
31	R4	Red Data.
32	R3	Red Data.
33	R2	Red Data.
34	R1	Red Data.
35	R0	Red Data(LSB).
36	GND	Power Ground
37	DCLK	Clock signal. Latching data at the rising edge
38	GND	Power Ground.
39	NC	No connection.
40	NC	No connection.
41	NC	No connection.
42	NC	No connection.
43	NC	No connection.
44	RESETB	Active low global reset signal input.
45	CSB	Chip select pin of serial interface. Internal pull high.
46	NC	No connection.
47	NC	No connection.
48	GND	Power Ground.
49	SCK	Clock pin of serial interface. Internal pull high.
50	SDI	Data input pin in serial mode. Internal pull high.



2.3 Timing Characteristics



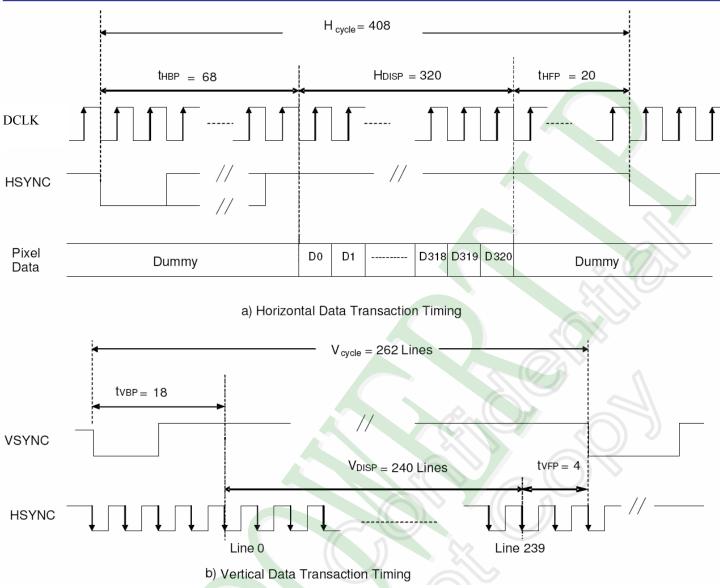
Pixel timing

Characteristics	Symbol	Mi	n.	Ту	/p.	Ма	ax.	Unit
Characteristics	Symbol	24-bit	8-bit	24- bit	8-bit	24-bit	8-bit	Unit
DCLK Frequency	DCLK			6.5	19.5	10	30	MHz
DCLK Period	DCLK	100	33.3	154	51.3	-	-	ns
Vertical Sync Setup Time	tvsys	20	10	-	-	-	-	ns
Vertical Sync Hold Time	tvsyh	20	10		-	-	-	ns
Horizontal Sync Setup Time	thsys	20	10	-	-	-	-	ns
Horizontal Sync Hold Time	thsyh	20	10	-	-	-	-	ns
Phase difference of Sync	thy	1				240		tDOTCLK
Signal Falling Edge	uiv				-	24	+0	IDOTOLK
DCLK Low Period	tCKL	50	15	-	-	-	-	ns
DCLK High Period	tCKH	50	15	-	-	-	-	ns
Data Setup Time	tds	12	10	-	-	-	-	ns
Data hold Time	tdh	12	10	-	-	-	-	ns
Reset pulse width	tRES	1	0		-		-	μ s

Note: External clock source must be provided to DCLK pin of HX8238-D. The driver will not operate if absent of the clocking signal.

Note : The interface of this module can drive by digital 24-bit data.



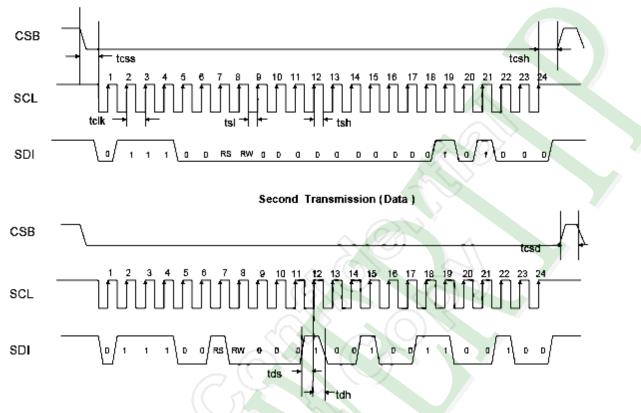


Data transaction timing in parallel RGB(24 bit)interface (SYNC mode)



Write SPI

First Transmission (Register)



Note: The example writes "0x1264h" to register R28h. SPID connected to VSS.

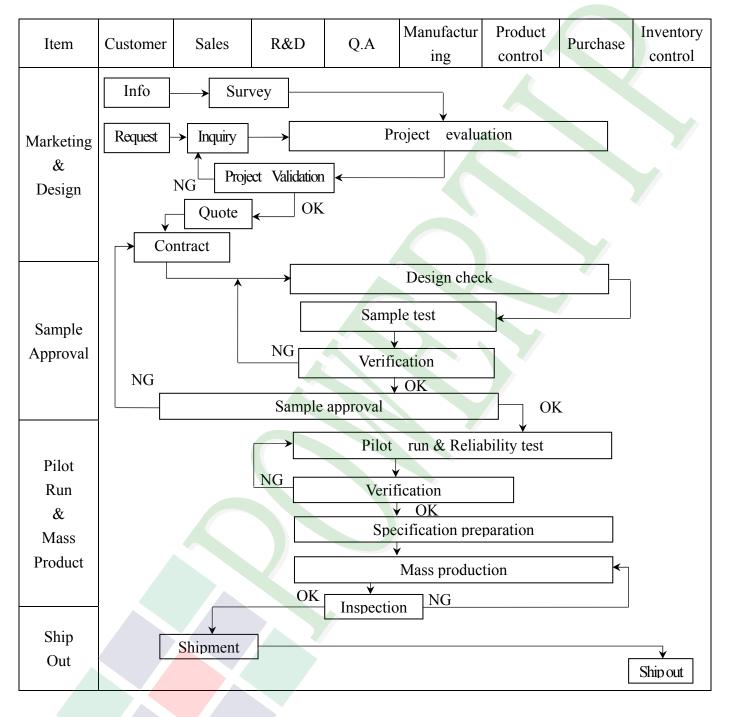
Figure 12. 14: (a) SPI interface timing diagram & write SPI example

Characteristics	Symbol	Spec.			Unit
	Oymbol	Min.	Тур.	Max.	Onic
Serial Clock Frequency	fclk	-	-	20	MHz
Serial Clock Cycle Time	tclk	50	-	-	ns
Clock Low Width	tsl	25	-	-	ns
Clock High Width	tsh	25	-	-	ns
Clock Rising Time	trs	-	-	30	ns
Clock Falling Time	tfl	-	-	30	ns
Chip Select Hold Time	tcsh	10	-	-	ns
Chip Select High Delay Time	tcsd	20	-	-	ns
Data Setup Time	tds	5	-	-	ns
Data Hold Time	tdh	10	-	-	ns



3. QUALITY ASSURANCE SYSTEM

3.1 Quality Assurance Flow Chart



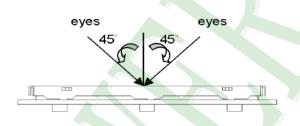


Item	Customer	Sales	R&D	Q.A	Manufactu	Product	Purchase	Inventory
ntenn	Customer	Buies	nad	Q.21	ring	control	1 urendse	control
Sales Service	Info Analys	→ Claim –	[Trackin	Failure an Corrective			
Q.A		Maintenan			ocess improv	1 1		
Activity	3. Equipment			4. E	ducation An	d Training	Activities	
	5. Standardi	zation Mana	agement					

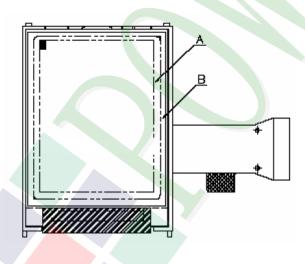


3.2. Inspection Specification

- Scope : The document shall be applied to TFT-LCD Module for 3. 5" ~10" (Ver.B01).
- ◆Inspection Standard : MIL-STD-105E Table Normal Inspection Single Sampling Level Ⅱ.
- ◆Equipment : Gauge、MIL-STD、Powertip Tester、Sample
- ◆Defect Level : Major Defect AQL : 0.4 ; Minor Defect AQL : 1.5
- ♦OUT Going Defect Level : Sampling.
- ◆Standard of the product appearance test :
 - a. Manner of appearance test :
 - (1). The test best be under 20W×2 fluorescent light, and distance of view must be at 30 cm.
 - (2). The test direction is base on about around 45° of vertical line.



(3). Definition of area.



A area : viewing area

B area : Outside of viewing area

(4). Standard of inspection : (Unit : mm)



◆Specification For TFT-LCD Module 3. 5″ ~10″:

◆Specification For TFT-LCD Module 3. 5″~10″: (Ver.						
NO	Item	Criterion	Level			
		1. 1The part number is inconsistent with work order of production.				
01	Product condition	1. 2 Mixed product types.				
		1. 3 Assembled in inverse direction.	Major			
02	Quantity	2. 1The quantity is inconsistent with work order of production.	Major			
03	Outline dimension	3.1 Product dimension and structure must conform to structure diagram.	Major			
	Electrical Testing	4. 1 Missing line character and icon.	Major			
		4. 2 No function or no display.				
04		4. 3 Display malfunction.				
		4.4 LCD viewing angle defect.				
		4. 5 Current consumption exceeds product specifications.	Major			
		Item Acceptance (Q'ty)				
	Dot defect (Bright dot 、	$\begin{array}{c c} \textbf{Bright Dot} & \leq 4 \end{array}$				
		Dot Dark Dot ≤ 5				
		Defect Joint Dot ≤ 3				
05	Dark dot)	Total ≤ 7	Minor			
	On -display	5. 1 Inspection pattern : full white , full black , Red , Green and blue screens.				
		5. 2 It is defined as dot defect if defect area $>1/2$ dot.				
		5. 3 The distance between two dot defect ≥ 5 mm.				



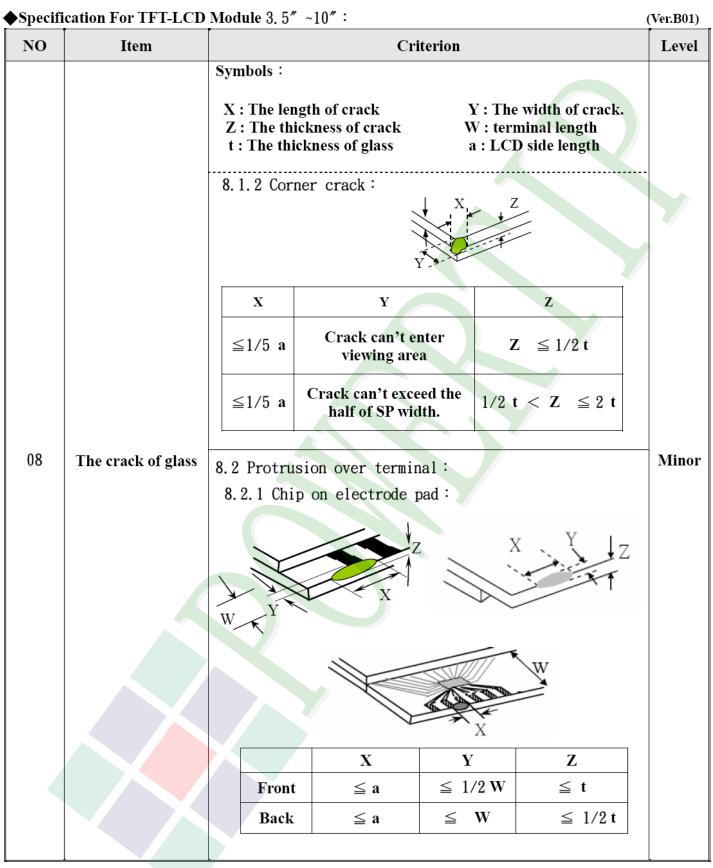
♦ Speci	Specification For TFT-LCD Module 3. 5" ~10" : (Ver.B01)						
NO	Item	Criterion					
		6. 1 Round type (Non-display or display) :					
		Dimension (diameter : Φ)Acceptance (Q'ty)A areaB area					
	Black or white dot、scratch、	$\Phi \leq 0.25$ Ignore					
	contamination	$0.25 < \Phi \leq 0.50$ 5					
	Round type	$\Phi > 0.50 \qquad 0 \qquad \text{Ignore}$					
	\rightarrow X \leftarrow Y	Total 5					
06	$\Phi = (x+y)/2$	6. 2 Line type(Non-display or display) :	Minor				
	2 (2 + 5), 2	Length (L) Width (W) Acceptance (Q'ty)					
	Line type	A area B area					
		W ≤ 0.03 Ignore L ≤ 10.0 0.03 < W ≤ 0.05 4					
		L ≤ 5.0 0.05 < W ≤ 0.10 2 Ignore					
		W >0.10 As round type					
		Total 5					
		Dimension (diameter : Φ)Acceptance (Q'ty)A areaB area					
		$\Phi \leq 0.25$ Ignore					
07	Polarizer	$0.25 < \Phi \leq 0.50$ 4	Minor				
	Bubble	$0.50 < \Phi \leq 0.80$ 1 Ignore					
		$\Phi > 0.80$ 0					
		Total 5					



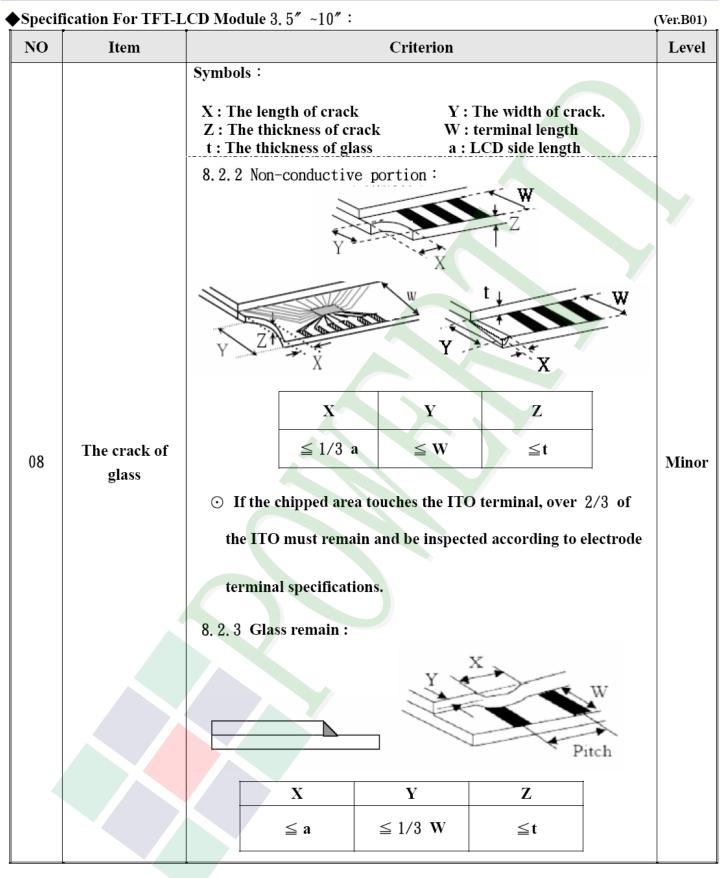
Specification For TFT-LCD Module 3.5" ~10":

NO	Item	Criterion		Level
		Z : The thickness of crack	Y : The width of crack. W : terminal length a : LCD side length	
		8.1 General glass chip: 8.1.1 Chip on panel surface and cr	ack between panels:	
		Y Z Z	Z Y X	
08	The crack of glass	SP	SP DICL	Minor
			[NG]	
		Seal width Z		
		XY	Z	
		≤ a Crack can't enter viewing area	$\leq 1/2 t$	
		$\leq a \qquad \begin{array}{c} Crack can't exceed the \\ half of SP width. \end{array}$	$1/2 t < Z \leq t$	











Specification For TFT-LCD Module 3. 5″ ~10″:

Specification For TFT-LCD Module 3. 5" ~10": (Ver					
NO	Item	Criterion	Level		
	Backlight elements	9. 1 Backlight can't work normally.	Major		
09		9. 2 Backlight doesn't light or color is wrong.	Major		
		9. 3 Illumination source flickers when lit.	Major		
	General appearance	10. 1 Pin type < quantity < dimension must match type in structure diagram.	Major		
		10. 2 No short circuits in components on PCB or FPC .	Major		
		10.3 Parts on PCB or FPC must be the same as on the production characteristic chart .There should be no wrong parts , missing parts or excess parts.	Major		
10		10. 4 Product packaging must the same as specified on packaging specification sheet.	Minor		
		10. 5 The folding and peeled off in polarizer are not acceptable.	Minor		
		10. 6 The PCB or FPC between B/L assembled distance(PCB or FPC) is ≤1.5 mm.	Minor		



4. RELIABILITY TEST

4.1 Reliability Test Condition

(Ver.B01)

<u> </u>	Reliability test condition (ver.bot)					
NO.	TEST ITEM	TEST CO	NDITION			
1	High Temperature Storage Test	Keep in +85°C±2°C 240hrs Surrounding temperature, then storage at normal condition 4hrs.				
2	Low Temperature Storage Test	Keep in −40°C±2°C 240hrs Surrounding temperature, then sto	prage at normal condition 4hrs.			
3	High Temperature / High Humidity Storage Test	Keep in +60 °C / 90% R.H duration Surrounding temperature, then stor (Excluding the polarizer)				
4	Temperature Cycling Storage Test	(30mins) (5mins) ◀ 10 C	$-40^{\circ}C \rightarrow +25^{\circ}C \rightarrow +85^{\circ}C \rightarrow +25^{\circ}C$			
5	ESD Test	Air Discharge:Contact Discharge:Apply 2 KV with 5 timesApply 250 V with 5 timesDischarge for each polarity +/-discharge for each polarity +/-1. Temperature ambiance : 15°C ~35°C2. Humidity relative : 30% ~60%3. Energy Storage Capacitance(Cs+Cd) : 150pF±10%4. Discharge Resistance(Rd) : 330 Ω±10%5. Discharge, mode of operation :Single Discharge (time between successive discharges at least 1 sec)(Tolerance if the output voltage indication : ±5%)				
6	Vibration Test (Packaged)	 Sine wave 10~55 Hz frequency The amplitude of vibration :1.5 Each direction (X ≤ Y ≤ Z) dur 	5 mm			
7	Drop Test (Packaged)	Packing Weight (Kg) 0 ~ 45. 4 45. 4 ~ 90. 8 90. 8 ~ 454 Over 454 Drop Direction : %1 corner / 3 edge	122 76 61 46			
<u> </u>						



5. PRECAUTION RELATING PRODUCT HANDLING

5.1 SAFETY

- 5.1.1 If the LCD panel breaks , be careful not to get the liquid crystal to touch your skin.
- 5.1.2 If the liquid crystal touches your skin or clothes, please wash it off immediately by using soap and water.

5.2 HANDLING

- 5.2.1 Avoid any strong mechanical shock which can break the glass.
- 5.2.2 Avoid static electricity which can damage the CMOS LSI—When working with the module , be sure to ground your body and any electrical equipment you may be using.
- 5.2.3 Do not remove the panel or frame from the module.
- 5.2.4 The polarizing plate of the display is very fragile. So , please handle it very carefully ,do not touch , push or rub the exposed polarizing with anything harder than an HB pencil lead (glass , tweezers , etc.)
- 5.2.5 Do not wipe the polarizing plate with a dry cloth , as it may easily scratch the surface of plate.
- 5.2.6 Do not touch the display area with bare hands , this will stain the display area.
- 5.2.7 Do not use ketonics solvent & aromatic solvent. Use with a soft cloth soaked with a cleaning naphtha solvent.
- 5.2.8 To control temperature and time of soldering is $320\pm10^{\circ}$ C and 3-5 sec.
- 5.2.9 To avoid liquid (include organic solvent) stained on LCM .

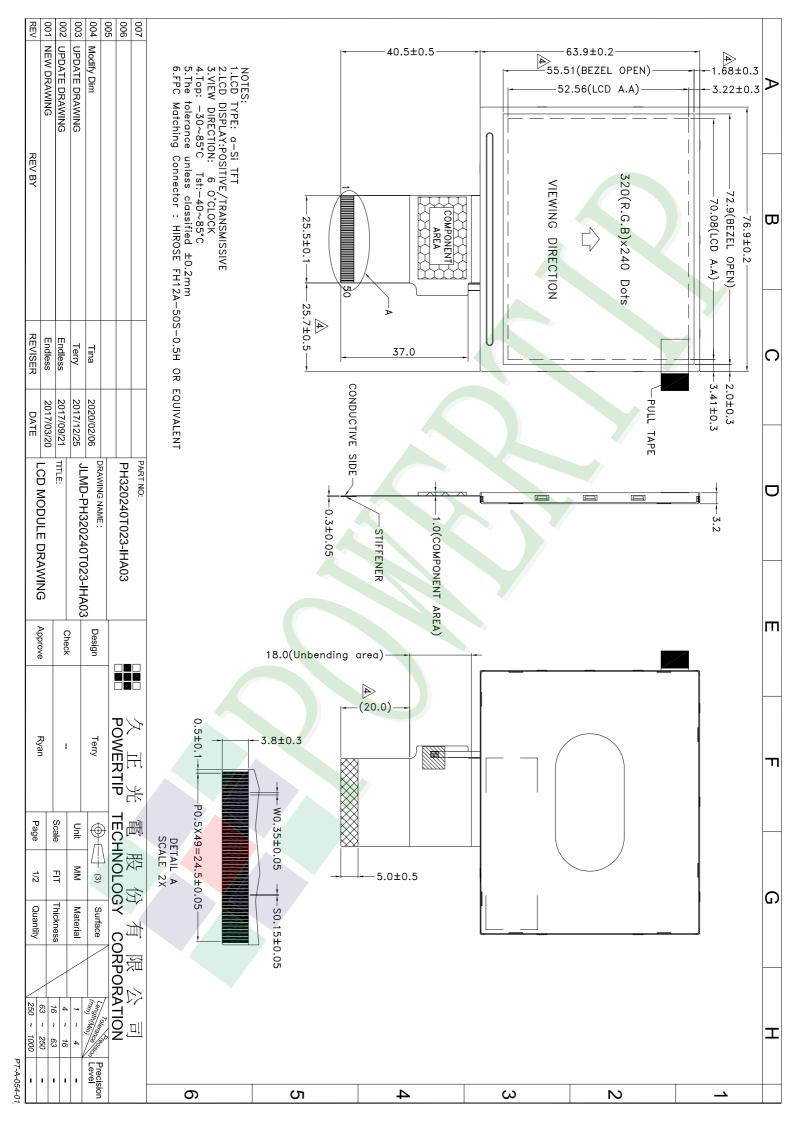
5.3 STORAGE

- 5.3.1 Store the panel or module in a dark place where the temperature is $25^{\circ}C \pm 5^{\circ}C$ and the humidity is below 65% RH.
- 5.3.2 Do not place the module near organics solvents or corrosive gases.
- 5.3.3 Do not crush , shake , or jolt the module.

5.4 TERMS OF WARRANTY

- 5.4.1 Applicable warrant period The period is within thirteen months since the date of shipping out under normal using and storage conditions.
- 5.4.2 Unaccepted responsibility

This product has been manufactured to your company's specification as a part for use in your company's general electronic products. It is guaranteed to perform according to delivery specifications. For any other use apart from general electronic equipment, we cannot take responsibility if the product is used in nuclear power control equipment, aerospace equipment, fire and security systems or any other applications in which there is a direct risk to human life and where extremely high levels of reliability are required.





POWERTIP TECH. CORP.