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CUSTOMER

SAMPLE CODE SH240320T074-ZAA01

MASS PRODUCTION CODE PH240320T074-ZAA01

SAMPLE VERSION 01

SPECIFICATIONS EDITION 006

DRAWING NO. (Ver.) LMD- PH240320T074-ZAA 01(Ver.002)

PACKAGING NO. (Ver.) PKG- PH240320T074-ZAA 01(Ver.001)

Customer Approved

Date:

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Preliminary specification for design input

Specification for sample approval

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History of Version

Date	Ver.	Edi.	Description	Page	Design by
08/30/2018	01	001	New Drawing.	-	Terry
11/09/2018	01	002	New Sample	-	Terry
12/19/2018	01	003	Modify LCD Type	-	Terry
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03/210/2020	01	005	Update Drawing.	-	Terry
12/17/2020	01	006	Update LED Life Time	9	Terry
				-	





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LCM Packaging Specifications

Note: For detailed information please refer to IC data sheet: ST7789VI



1. SPECIFICATIONS

1.1 Features

Item	Standard Value				
Display Type	240 (RGB) * 320 Dots				
LCD Type	Full Viewing Angle , Normally Black, Transmissive type				
Screen size(inch)	2.0 inch				
Color configuration	RGB-Strip				
Display Colors	Full Color: 262K (RGB= 666, Idle Mode Off)				
Backlight Type	LED B/L				
Interface	16-bit / 18-bit 80-system				
	THIS PRODUCT CONFORMS THE ROHS OF PTC				
ROHS	Detail information please refer website :				
	http://www.powertip.com.tw/news_detail.php?Key=1&cID=1				

1.2 Mechanical Specifications

Item	Standard Value	Unit
Outline Dimension	36.1(W) * 51.0 (L) * 2.05 (H)	mm

LCD Panel

Item	Standard Value	Unit
Viewing Area	31.6 (W) * 41.8(L)	mm
Active Area	30.6 (W) * 40.8 (L)	mm
Pixel Pitch	0.1275(H) x 0.1275(V)	mm

Note: For detailed information please refer to LCM drawing



1.3 Absolute Maximum Ratings

Module

Item	Symbol	Condition	Min.	Max.	Unit
	VCC / VCI	-	-0.3	+4.6	V
System Power Supply Voltage	IOVCC	-	-0.3	+4.6	V
Voltage	VGH ~ VGL	-	-0.3	+30	V
Logic Input Voltage Range	VIN	-	-0.3	IOVCC+0.5	V
Operating Temperature	Тор	-	-20	+70	°C
Storage Temperature	T _{ST}	-	-30	+80	°C
Storage Humidity	Но	Ta ≦ 60 °C	-	90	%RH

1.4 DC Electrical Characteristics

Module GND = 0V, Ta = $25^{\circ}C$

Item	Symbol	Condition	Min.	Тур.	Max.	Unit
Power Supply for Analog, Digital System	VCC / VCI	-	2.4	2.8	3.6	V
Power Supply for I/O System	IOVCC	-	1.65	1.8	3.6	V
Input Signal Voltage	VIH	-	0.7IOVCC	-	IOVCC	V
Input Signal Voltage	VIL	-	GND	-	0.3 IOVCC	V
Output Signal Valtage	VOH	-	0.8 IOVCC	-	IOVCC	V
Output Signal Voltage	VOL	-	GND	-	0.2 IOVCC	V
		VCC / VCI=2.8V				
Supply Current	ICC / ICI	IOVCC=2.8V	-	9	14	mA
		Pattern= Black *1				

Note1: Maximum current display.



1.5 Optical Characteristics TFT LCD Panel

Ta=25°C

Item		Symbol	Condition	Min.	Тур.	Max.	unit	
Response time	Tr	+ Tf	Ta = 25°C θX, θY = 0°	-	30	40	ms	Note 2
	Тор	θΥ+		-	80	-		
Viouing angle	Bottom	θΥ-	CR ≥ 10	-	80	-	Dog	Note 4
Viewing angle	Left	θX-	CR 2 10	-	80	-	Deg.	Note 4
	Right	θX+		-	80	-		
Contrast ratio	C	CR		650	800	-	-	Note 3
	\\/bito	Х		0.25	0.30	0.35		
	White	у	Ta = 25°C θX , θY = 0°	0.27	0.32	0.37	-	
	Dod	Х		0.59	0.64	0.69		
Color of CIE Coordinate	Red	у		0.28	0.33	0.38		Note1
(With B/L)	Green	Х		0.28	0.33	0.38		Note
,	Green	у		0.56	0.61	0.66		
	Blue	Х		0.09	0.14	0.19		
	Diue	y		0.03	0.08	0.13		
Average Brightr	ness							
Pattern=white di	splay	IV	IF= 60 mA	200	250		-	Note1
(With LCD)*	1							
Uniformity (With LCD)*	2	△B	IF=60 mA	80	-	-	%	Note1
Color gamut of (NTSC%)	CF	S	-	54	60	-	%	-



Note 1:

*1 : △B=B(min) / B(max) * 100%

*2 : Measurement Condition for Optical Characteristics:

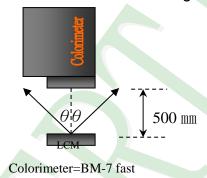
a: Environment: 25°C±5°C / 60±20%R.H, no wind, dark room below 10 Lux at typical lamp current and typical operating frequency.

b : Measurement Distance: $500 \pm 50 \text{ mm}$, $(\theta = 0^{\circ})$

c: Equipment: TOPCON BM-7 fast, (field 1°), after 10 minutes operation.

d: The uncertainty of the C.I.E coordinate measurement ±0.01, Average Brightness ± 4%





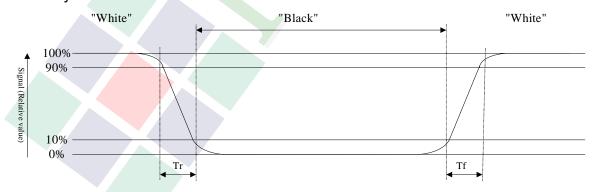
To be measured at the center area of panel with a viewing cone of 1° by Topcon luminance meter BM-7, after 10 minutes operation (module)

Note2: Definition of response time:

The output signals of photo detector are measured when the input signals are changed from "black" to "white" (falling time) and from "white" to "black" (rising time), respectively. The response time is defined as the time interval between the 10% and 90% of Amplitudes.

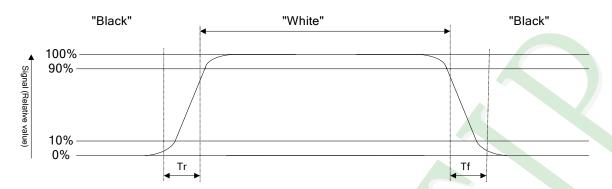
Refer to figure as below:

Normally White





Normally Black



Note3: Definition of contrast ratio:

Contrast ratio is calculated with the following formula

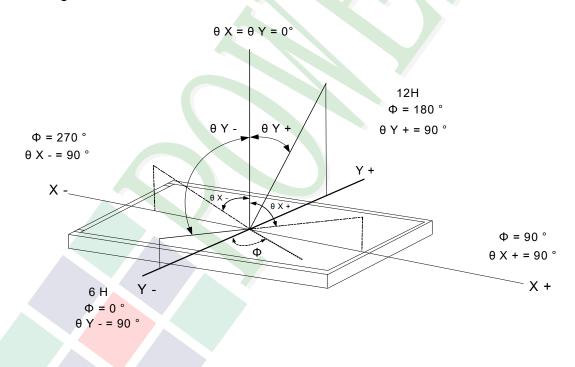
Photo detector output when LCD is at "White" state

Contrast ratio (CR) =

Photo detector output when LCD is at "Black" state

Note4: Definition of viewing angle:

Refer to figure as below:





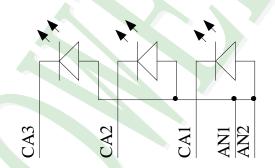
Backlight Characteristics 1.6

Maximum Ratings

Item	Symbol	Conditions	Min.	Max.	Unit	Remark
LED Forward Current	IF	Ta =25°C	-	25	mA	Each LED
LED Reverse Voltage	VR	Ta =25°C	-	5	V	Each LED
Power Dissipation	PD	Ta =25°C	-	85	mW	-

Backlight Characteristics

Item	Symbol	Conditions	Min.	Тур.	Max.	Unit
Forward Voltage	VF	IF=60mA (Without LCD)	2.7	3.1	3.5	V
Color	White					
LED life time			30000	nr		





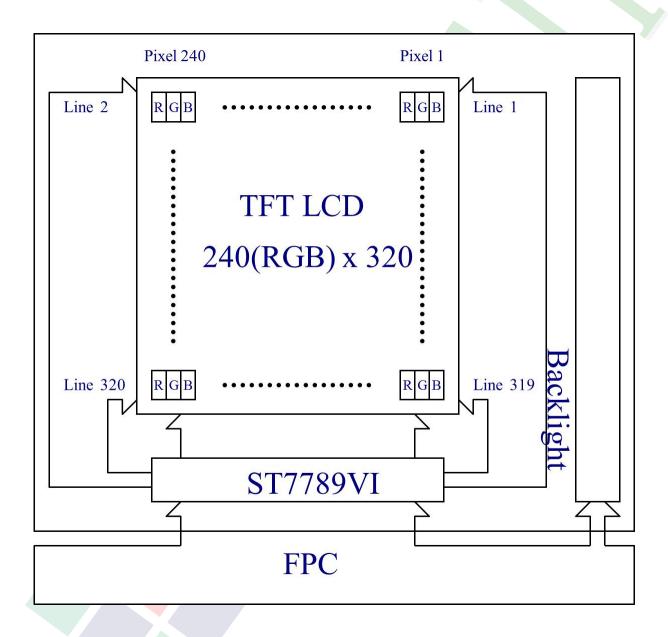
2. MODULE STRUCTURE

2.1 Counter Drawing

2.1.1 LCM Mechanical Diagram

* See Appendix

2.1.2 Block Diagram





2.2 Interface Pin Description

Pin No.	Symbol	Function
1	GND	System Ground
2	CA3	LED Cathode
3	CA2	LED Cathode
4	CA1	LED Cathode
5	AN1	LED Anode
6	AN2	LED Anode
7	VCC / VCI	Dower Cumply for Angles Digital Cystem and Deceter Circuit
8	VCC / VCI	Power Supply for Analog, Digital System and Booster Circuit
9	IOVCC	Devices County for U.O. Counters
10	IOVCC	Power Supply for I/O System
11	TE	Tearing effect signal is used to synchronize MCU to frame memory
12	CSX	Chip selection pin Low enable. High disable.
13	DCX	Display data/command selection pin in parallel interface. This pin is used to be serial interface clock. DCX='1': display data or parameter. DCX='0': command data.
14	WRX	Write enable in MCU parallel interface.
15	RDX	Read enable in 8080 MCU parallel interface.
16	RESX	This signal will reset the device and it must be applied to properly initialize the chip. Signal is active low.
17	IM1	The MCU interface mode select IM1='1': 80-18bit parallel I/F IM1='0': 80-16bit parallel I/F

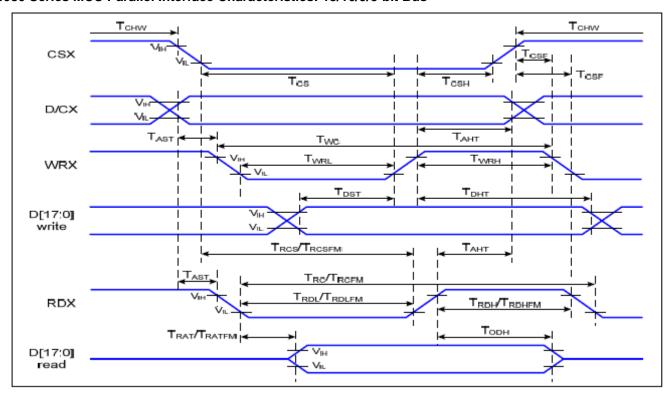


Pin No.	Symbol	Function
18	DB0	
19	DB1	
20	DB2	
21	DB3	
22	DB4	
23	DB5	
24	DB6	
25	DB7	DB[17:0] are used as MCU parallel interface data bus.
26	DB8	16-bit I/F: DB[15:0] are used;
27	DB9	18-bit I/F: DB[17:0] are used.
28	DB10	If not used, please fix this pin at IOVCC or GND.
29	DB11	
30	DB12	
31	DB13	
32	DB14	
33	DB15	
34	DB16	
35	DB17	
36	GND	System Ground
37	NC	NC
38	NC	NC
39	NC	NC
40	NC	NC



2.3 Timing Characteristics

8080 Series MCU Parallel Interface Characteristics: 18/16/9/8-bit Bus



Parallel Interface Timing Characteristics (8080-Series MCU Interface)

Signal	Symbol	Parameter	Min	Max	Unit	Description	
D/CX	T _{AST}	Address setup time	0		ns		
DICA	T _{AHT}	Address hold time (Write/Read)	10		ns		
	T _{CHW}	Chip select "H" pulse width	0		ns		
CCV	T _{cs}	Chip select setup time (Write)	15		ns		
	T _{RCS}	Chip select setup time (Read ID)	45		ns		
CSX	T _{RCSFM}	Chip select setup time (Read FM)	355		ns	-	
	T _{CSF}	Chip select wait time (Write/Read)	10		ns		
	T _{CSH}	Chip select hold time	10		ns		
	T _{wc}	Write cycle	66		ns		
WRX	T _{WRH}	Control pulse "H" duration	15		ns		
	T _{WRL}	Control pulse "L" duration	15		ns		
	T _{RC}	Read cycle (ID)	160		ns		
RDX (ID)	T _{RDH}	Control pulse "H" duration (ID)	90		ns	When read ID data	
	T _{RDL}	Control pulse "L" duration (ID)	45		ns		
DDV	T _{RCFM}	Read cycle (FM)	450		ns	When and from	
RDX (EM)	T _{RDHFM}	Control pulse "H" duration (FM)	90		ns	When read from	
(FM)	T _{RDLFM}	Control pulse "L" duration (FM)	355		ns	frame memory	
D[17:0]			10		ns	For CL=30pF	



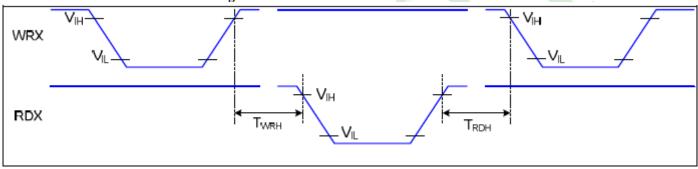
8080 Parallel Interface Characteristics

T _{DHT}	Data hold time	10		ns	
T _{RAT}	Read access time (ID)		40	ns	
T _{RATFM}	Read access time (FM)		340	ns	
T _{ODH}	Output disable time	20	80	ns	

Rising and Falling Timing for I/O Signal



Write-to-Read and Read-to-Write Timing





2.4 Refer Initial Code

HW_Reset();

Delay(120); //ms

Write(Command, 0x11);

Delay(120); //ms

Write(Command, 0x36); Write(Parameter, 0x00);

Write(Command, 0x3A);

Write(Parameter, 0x05); 05 16bit, 06 18bit

Write(Command, 0xB2); Write(Parameter, 0x0C); Write(Parameter, 0x0C); Write(Parameter, 0x00); Write(Parameter, 0x33); Write(Parameter, 0x33);

Write(Command, 0xB7); Write(Parameter, 0x35);

Write(Command, 0xBB); Write(Parameter, 0x21);

Write(Command, 0xC0); Write(Parameter, 0x2C);

Write(Command, 0xC2); Write(Parameter, 0x01);

Write(Command, 0xC3); Write(Parameter, 0x0B);

Write(Command, 0xC4); Write(Parameter, 0x20);

Write(Command , 0xC6);

Write(Parameter , 0x0F);

Write(Command, 0xD0); Write(Parameter, 0xA4); Write(Parameter, 0xA1);

Write(Command, 0x21);

Write(Command, 0xE0); Write(Parameter, 0x70);

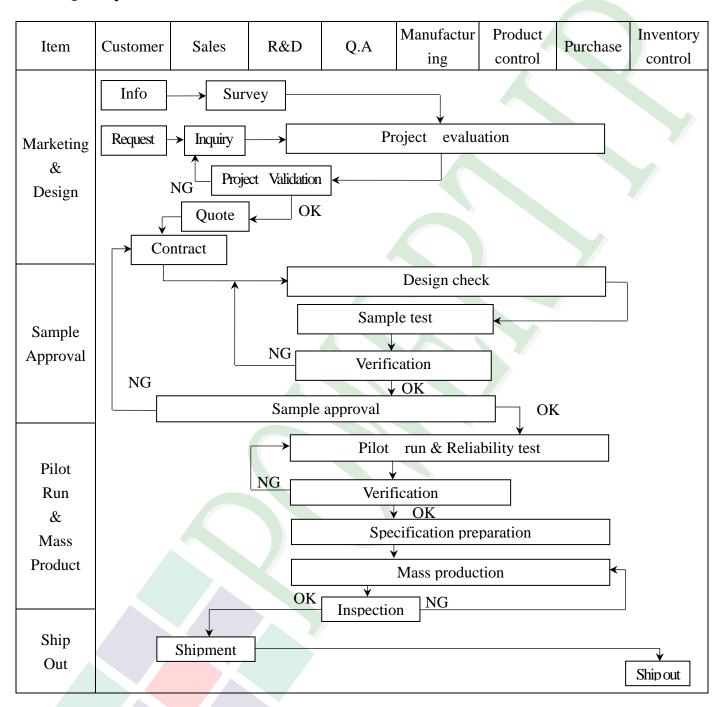


Write(Parameter, 0x04); Write(Parameter, 0x08); Write(Parameter, 0x07); Write(Parameter, 0x06); Write(Parameter, 0x23); Write(Parameter, 0x29); Write(Parameter, 0x32); Write(Parameter, 0x41); Write(Parameter, 0x38); Write(Parameter, 0x15); Write(Parameter, 0x16); Write(Parameter, 0x27); Write(Parameter, 0x2C); Write(Command, 0xE1); Write(Parameter, 0x70); Write(Parameter, 0x01); Write(Parameter, 0x05); Write(Parameter, 0x08); Write(Parameter, 0x07); Write(Parameter, 0x04); Write(Parameter, 0x27); Write(Parameter, 0x44); Write(Parameter, 0x42); Write(Parameter, 0x0A); Write(Parameter, 0x16); Write(Parameter, 0x14); Write(Parameter, 0x2B); Write(Parameter, 0x2F); Write(Command, 0x29);



3. QUALITY ASSURANCE SYSTEM

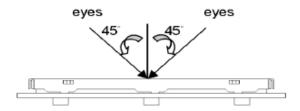
3.1 Quality Assurance Flow Chart



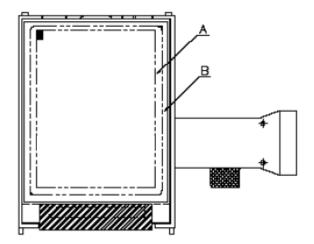


3.2. Inspection Specification

- ◆Scope : The document shall be applied to TFT-LCD Module for less than 3, 5" (Ver.B01).
- ♦Inspection Standard: MIL-STD-105E Table Normal Inspection Single Sampling Level II.
- ◆Equipment: Gauge \ MIL-STD \ Powertip Tester \ Sample
- ◆Defect Level: Major Defect AQL: 0.4; Minor Defect AQL: 1.5
- **♦**OUT Going Defect Level: Sampling.
- ◆Standard of the product appearance test:
 - a. Manner of appearance test:
 - (1). The test best be under 20W×2 fluorescent light, and distance of view must be at 30 cm.
 - (2). The test direction is base on about around 45° of vertical line.



(3). Definition of area.



A area: viewing area

B area: Outside of viewing area

(4). Standard of inspection: (Unit: mm)



◆Specification For TFT-LCD Module Less Than 3.5":

NO	Item			Criteri	on		Level
		1. 1The part number is inconsistent with work order of production.					Major
01	Product condition	1. 2 Mi	xed prod	luct types.			Major
		1. 3 Ass	sembled	in inverse direction.			Major
02	Quantity	2. 1The	e quantit	y is inconsistent witl	n work order of produc	tion.	Major
03	Outline dimension		oduct di agram.	mension and structu	ire must conform to str	ructure	Major
		4. 1 Mi	ssing lin	e character and icon			Major
	Electrical Testing	4. 2 No	function	ı or no display.			Major
04		4. 3 Display malfunction.					
04		4. 4 LCD viewing angle defect.					Major
		4. 5 Current consumption exceeds product specifications.					Major
		1	4. 6 Mura can not be seen through 5% ND filter, should be judged by the viewing angle of 90 degree.				
				Item	Acceptance (Q'ty)		
				Bright Dot	≦ 2		
	Dot defect		Dot	Dark Dot	≦ 3		
			Defect	Joint Dot	≦ 2		
05	(Bright dot \			Total	≦ 3		Minor
	Dark dot)	5. 1 Inspection pattern: full white, full black, Red, Green and					
	On -display	blue screens.					
		1		l as dot defect if defe e between two dot d			
					seen through 5% ND f	ilter	
		5, 1 DI	-6 40		out ough on its I		



Specification For TFT-LCD Module Less Than 3, 5":

NO	Item		Criterion					
		6. 1 Round type (Non-display or display):						
		Dimension (diameter : Φ)			Acceptance (Q'ty)			
					A area	B area		
	Black or white dot \ scratch \		$\Phi \le 0.15$		Ignore			
	contamination	0.15	$<\Phi \le 0.20$		2			
	Round type	0.20	$<\Phi \le 0.30$		2	Ignore		
	→ <u>x</u> <u>←</u>		$\Phi > 0.30$		0			
06	Y T		Total		3		Minor	
	$\Phi = (x+y)/2$	6. 2 Line type(Non-display or display):						
	Line type	Dimension			Acceptance (Q'ty)			
		Length (L) Width (W)		V)	A area	B area		
		$ \begin{array}{c ccccccccccccccccccccccccccccccccccc$		0.03	Ignore			
				0.05				
					w >	0.05	As roun- type	d Ignore
			Total		3			
						(01)		
		Dimension (diameter : Φ)			Acceptance A area	(Q'ty) B area		
			$\Phi \leq 0.20$		gnore	Darea		
07	Polarizer Bubble	$0.20 < \Phi \leq 0.50$			3		Minor	
	Dubble	0,20				Ignore		
			$\Phi > 0.50$		0			
		Total			3			



◆Specification For TFT-LCD Module Less Than 3.5":

NO	Item		Criterion		Level
08	The crack of glass	Z: The thing t: The thing t: The thing to the second secon	ngth of crack ickness of crack	Y: The width of crack. W: terminal length a: LCD side length nck between panels: Y X ING ING	Minor
		X	Y	z	
		≦ a	Crack can't enter viewing area	≦1/2 t	
		≦ a	Crack can't exceed the half of SP width.	1/2 t < Z ≤2 t	



◆Specification For TFT-LCD Module Less Than 3.5":

NO	Item	Criterion (Level		
		Symbols: X: The length of crack Z: The thickness of crack t: The thickness of glass 8. 1. 2 Corner crack: X: The width of crack. W: terminal length a: LCD side length			
		X Y Z ≤1/5 a Crack can't enter viewing area Z ≤ 1/2 t			
		$\leq 1/5$ a Crack can't exceed the half of SP width. $1/2$ t $<$ Z ≤ 2			
08	The crack of glass	8. 2 Protrusion over terminal: 8. 2. 1 Chip on electrode pad: X X X Y Z X X Y Z	Minor		
		Front $\leq a$ $\leq 1/2 W$ $\leq t$ Back $\leq a$ $\leq W$ $\leq 1/2 t$			



◆Specification For TFT-LCD Module Less Than 3, 5":

NO	Item	Criterion	Level
NO	Item	Symbols: X: The length of crack Z: The thickness of crack t: The thickness of glass 8. 2. 2 Non-conductive portion:	Level
08	The crack of glass	 ≤ 1/3 a ≤W ≤t ⊙ If the chipped area touches the ITO terminal, over 2/3 of the ITO must remain and be inspected according to electrode terminal specifications. 8. 2. 3 Glass remain : X Y Z ≤ a ≤ 1/3 W ≤t 	Minor



◆Specification For TFT-LCD Module Less Than 3.5″:

NO	Item	Criterion	Level
09		9. 1 Backlight can't work normally.	Major
	Backlight elements	9, 2 Backlight doesn't light or color is wrong.	
		9. 3 Illumination source flickers when lit.	Major
	diagram. 10. 2 No short circuits in com 10. 3 Parts on PCB or FPC characteristic chart . missing parts or excess 10. 4 Product packaging mus specification sheet. 10. 5 The folding and peeled	10. 1 Pin type · quantity · dimension must match type in structure diagram.	Major
		10. 2 No short circuits in components on PCB or FPC .	Major
10		10. 3 Parts on PCB or FPC must be the same as on the production characteristic chart .There should be no wrong parts , missing parts or excess parts.	Major
10		10. 4 Product packaging must the same as specified on packaging specification sheet.	Minor
		10. 5 The folding and peeled off in polarizer are not acceptable.	Minor
		10. 6 The PCB or FPC between B/L assembled distance(PCB or FPC) is ≤1.5 mm.	Minor



4. RELIABILITY TEST

4.1 Reliability Test Condition

(Ver.B01)

4.	Reliability rest condition (ver.bor)						
NO.	TEST ITEM		TEST CO	ONDITION			
1	High Temperature Storage Test	Keep in 80	Keep in 80 ±5°C 96 hrs				
2	Low Temperature Storage Test	Keep in −30	Keep in -30 ±5°C 96 hrs				
3	High Temperature / High Humidity Storage Test	_	Keep in 60 °C / 90% R.H duration for 96 hrs (Excluding the polarizer)				
			-30°C → +25°C	→ 80°C → +25°C			
4	Temperature Cycling		(30mins) (5mins)	(30mins) (5mins)			
	Storage Test		20 0	ycle			
		Air Dischar	ge:	Contact Discharge:			
	ESD Test	Apply 2 KV	with 5 times	Apply 250 V with 5 tim	ies		
		Discharge for each polarity +/- discharge for ea			ch polarity +/-		
		1. Temperature ambiance : 15°C ~35°C					
5		2. Humidity relative : 30%~60%					
		3. Energy Storage Capacitance(Cs+Cd): 150pF±10%					
		4. Discharge Resistance(Rd): 330 Ω±10%					
		5. Discharge, mode of operation : Single Discharge (time between successive discharges at least 1 sec)					
		(Tolerance if the output voltage indication: ±5%)					
		-	ve 10~55 Hz frequenc				
6	Vibration Test		olitude of vibration :1.	• •			
	(Packaged)	3. Each direction (X \ Y \ Z) duration for 2 Hrs					
			Packing Weight (Kg)	Drop Height (cm)			
			0 ~ 45.4	122			
	D T. (45.4 ~ 90.8	76			
7	Drop Test (Packaged)		90.8 ~ 454	61			
	(= ====================================		Over 454	46			
		Drop Direction: **1 corner / 3 edges / 6 sides each 1 time					

OResult Evaluation Criteria:

Under the display quality test conditions with normal operations with normal operation state. Do not change these conditions as such changes may affect practical display function. (Normal operation state)

Temperature: +20~30°C Humidity: 50~70%

Atmospheric pressure: 86~106Kpa



5. PRECAUTION RELATING PRODUCT HANDLING

5.1 SAFETY

- 5.1.1 If the LCD panel breaks, be careful not to get the liquid crystal to touch your skin.
- 5.1.2 If the liquid crystal touches your skin or clothes, please wash it off immediately by using soap and water.

5.2 HANDLING

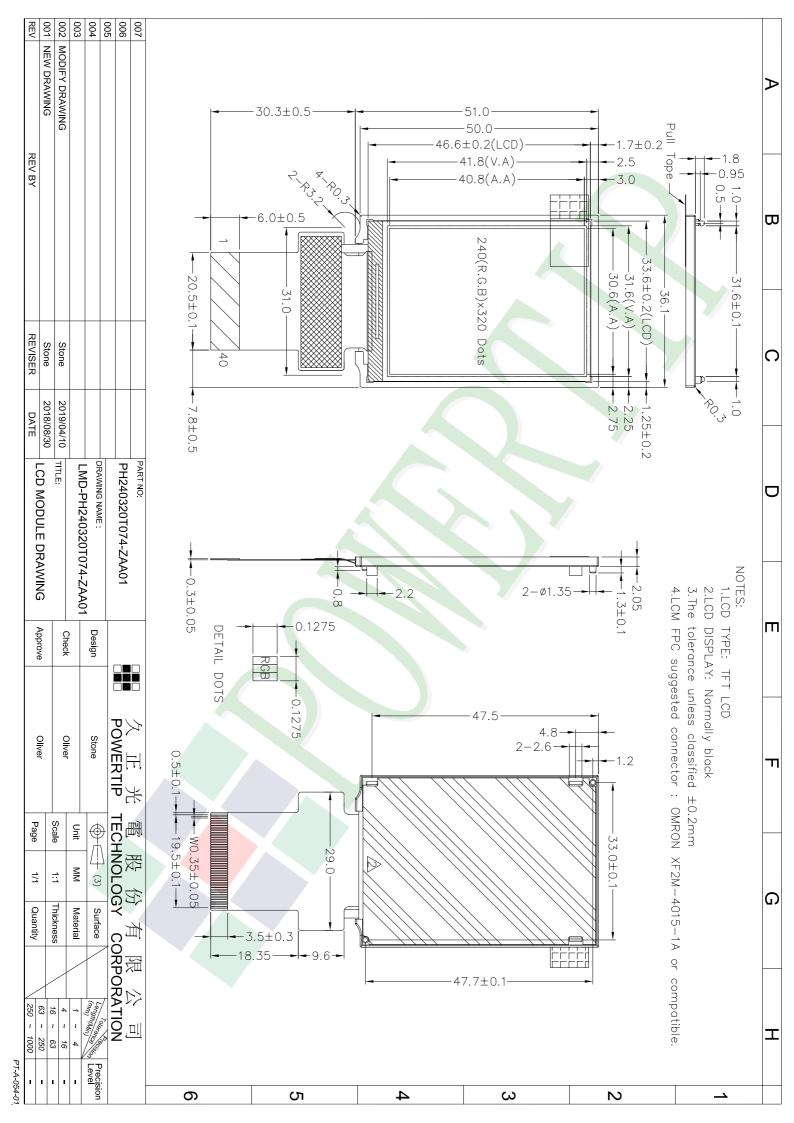
- 5.2.1 Avoid any strong mechanical shock which can break the glass.
- 5.2.2 Avoid static electricity which can damage the CMOS LSI—When working with the module, be sure to ground your body and any electrical equipment you may be using.
- 5.2.3 Do not remove the panel or frame from the module.
- 5.2.4 The polarizing plate of the display is very fragile. So, please handle it very carefully, do not touch, push or rub the exposed polarizing with anything harder than an HB pencil lead (glass, tweezers, etc.)
- 5.2.5 Do not wipe the polarizing plate with a dry cloth, as it may easily scratch the surface of plate.
- 5.2.6 Do not touch the display area with bare hands, this will stain the display area.
- 5.2.7 Do not use ketonics solvent & aromatic solvent. Use with a soft cloth soaked with a cleaning naphtha solvent.
- 5.2.8 To control temperature and time of soldering is $320 \pm 10^{\circ}$ C and 3-5 sec.
- 5.2.9 To avoid liquid (include organic solvent) stained on LCM
- 5.2.10 Caution!(LCM products with Capacitive Touch Panel)
 Strong EMI-sources such as switch-mode power supplies (SMPS) can lead to touch malfunction (e.g. ghost-touches).
 - Therefore, the touch needs to be thoroughly tested inside the target application.
- 5.2.11 CAUTION: Continuously displaying same static image will result in high possibility of image sticking/image burn-in effect due to TFT panel characteristic.
- 5.2.12 Double-sided tape designed to be attach with the customer's mechanical device, please follow up the rules and regulations published by the original manufacturer of double-sided tape for the attachment operation.

5.3 STORAGE

- 5.3.1 Store the panel or module in a dark place where the temperature is $25^{\circ}\text{C} \pm 5^{\circ}\text{C}$ and the humidity is below 65% RH.
- 5.3.2 Do not place the module near organics solvents or corrosive gases.
- 5.3.3 Do not crush, shake, or jolt the module.

5.4 TERMS OF WARRANTY

- 5.4.1 Applicable warrant period The period is within thirteen months since the date of shipping out under normal using and storage conditions.
- 5.4.2 Unaccepted responsibility
 - This product has been manufactured to your company's specification as a part for use in your company's general electronic products. It is guaranteed to perform according to delivery specifications. For any other use apart from general electronic equipment, we cannot take responsibility if the product is used in nuclear power control equipment, aerospace equipment, fire and security systems or any other applications in which there is a direct risk to human life and where extremely high levels of reliability are required.



Approve Check Contact Ver.001 LCM包裝規格書 Oliver Oliver Stone Documents NO. PKG-PH240320T074-ZAA01 LCM Packaging Specifications 1.包裝材料規格表 (Packaging Material): (per carton) Item 1Pcs Weight Total Weight No. Model Dimensions (mm) Quantity PH240320T074-ZAA01 1 成品 (LCM) 51.0 X 36.1 0.0056 468 2.6208 2 靜電袋(1)Antistatic Bag BAG100100ARABA 100 X 100 0.0011 468 0.5148 3 0.3744 氣泡袋(2)Bubble Bag 100 X 65 0.0008 468 BAG100065BRABA 4 A1-1隔板(3)A1-1 Partition 295 X 47 X 3 0.0078 1.3104 BX29500047BZBA 168 5 B1-1隔板(4)B1-1 Partition 245 X 47 X 3 0.0065 48 0.312 BX24500047BZBA 6 氣泡紙(5)Bubble Sheet BAG280240BWABA 280 X 240 0.006 24 0.144 7 C1內盒(6)Product Box BX31025555AABA 310 X 255 X 55 0.13 12 1.56 8 外紙箱(7)Carton 0.83 BX52732536CCBA 527 X 325 X 360 0.83 1 9 2.一 整箱總重量 (Total LCD Weight in carton): 7.67 Kg±10% 3.單箱數量規格表 (Packaging Specifications and Quantity): (1)Quantity Of Spacer: A1-1隔板 X 14, B1-1隔板 X (2) Total LCM quantity in carton: quantity per box x no of boxes 12 468 (5) 氣泡紙 **Bubble Sheet** (1)靜電袋+(2)氣泡袋+LCM Antistatic Bag+Bubble Bag+LCM (3)(4)隔板 Partition ᆥ (註 Remark 1) (5) 氣泡紙 **Bubble Sheet** (7)外紙箱 Carton (6) C1内盒 Product Box 特 記 事 項 (REMARK) 1. LCM排放示意圖(前後間隔不放置): 1. LCM placed as figure showing: (First and last slot should be empty)

Ø 模組(LCM) X 1pcs.