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SPECIFICATIONS					
CUSTOMER	:				
SAMPLE CODE	SH128800T004-ZFA				
MASS PRODUCTION CODE	. PH128800T004-ZFA				
SAMPLE VERSION	. 01				
SPECIFICATIONS EDITION	. 008				
DRAWING NO. (Ver.)	LMD-PH128800T004-ZFA (Ver.002)				
PACKAGING NO. (Ver.)	PKG-PH128800T004-ZFA (Ver.005)				

# **Customer Approved**

Date:

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☐ Preliminary specification for design input

■ Specification for sample approval

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# **History of Version**

Date	Ver.	Edi.	Description	Page	Design by
01/25/2017	01	001	New Drawing	-	Stephen
03/21/2017	01	002	Update Spec	12~14	Stephen
05/10/2017	01	003	New Sample	-	Stephen
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12/20/2018	01	007	Modify Spec 1.1 Features - Display Mode 1.3 Maximum Ratings - Operating Temperature	4、5	Stephen
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		X			
	X				



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### 1. SPECIFICATIONS

### 1.1 Features

Item	Standard Value
Screen Size(inch)	10.1(Diagonal)
Resolution	1280* (R · G · B) * 800 Dots
Display Mode	Full Viewing Angle   Transmissive   Normally Black
Color	16.7M
Weight	222 g
Interface	LVDS
	THIS PRODUCT CONFORMS THE ROHS OF PTC
ROHS	Detail information please refer website :
	http://www.powertip.com.tw/news_detail.php?Key=1&cID=1

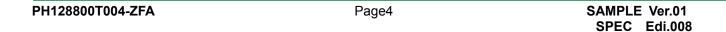
# 1.2 Mechanical Specifications

Item	Standard Value	Unit
Outline Dimension	229.8(W) * 149.0 (L) * 5.9 (H)	mm

# LCD panel

Item	Standard Value	Unit
Active Area	216.96 (W) * 135.60 (L)	mm

Note: For detailed information please refer to LCM drawing.





# 1.3 Absolute Maximum Ratings

Item	Symbol	Condition	Min.	Max.	Unit
Power Supply Voltage	VDD		-0.3	+4.0	٧
Operating Temperature	Тор	-	-20	+70	$^{\circ}\mathbb{C}$
Storage Temperature	Tst	-	-30	+80	$^{\circ}\!\mathbb{C}$
Storage Humidity	H <sub>D</sub>	Ta<25 °C	55	60	%RH

# 1.4 DC Electrical Characteristics

Item		Symbol	Condition	Min.	Тур.	Max.	Unit	Note
Power Supply Volt LCD Driver	•	VDD-VSS	-	3.0	3.3	3.6	V	-
Power Supply Volt LED Driver	•	VLED-VLSS	-	6	12	21	V	-
PWM Signal	High	V <sub>PWM</sub>		3.0		3.6	V	-
Voltage	Low	V PWM		0	-	0.4	V	-
LED Enable	High	V. 50 5N		3.0	1	3.6	V	-
Voltage	Low	VLED_EN		0	-	0.4	V	-
VDD Curren	ıt	IDD	VDD=3.3V, Pattern= Picture*1	_	0.222	-	Α	-
VLED Currer	nt	ILED	VLED=12V PWM=100%	-	0.294	-	Α	-
VDD Power Consu	ımption	PDD	VDD=3.3V	-	-	1	W	-
VLED Powe Consumption		PLED	VLED=12V	-	-	5	W	
Rush Currer	nt	Irush	-	-	-	1.5	Α	-
Driver Ripple Vo	Itage	VVDD-RP	-	-	-	300	mV	-
				100	-	200		<b>D</b> ым≥0.1%
				200	1	500		<b>D</b> ым≥0.25%
				500	ı	1000		<b>D</b> ым≥0.5%
Input PWM Frequency		FPWM	_	1000	•	2000	Hz	Dым≥1%
		I I VVIVI	-	2000		5000	1 12	<b>D</b> ым≥2.5%
				5000	-	10000		Оом≥5%
				10000	-	20000		<b>D</b> ым≥10%
				20000	ı	30000		<b>D</b> ым≥15%

Note1: Maximum current display.



### 1.5 Optical Characteristics

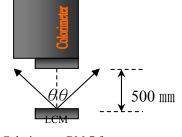
TFT LCD Panel Ta=25°C

Item		Symbol	Condition	Min.	Тур.	Max.	Unit	-
Response Tin	ne	Tr + Tf	-	-	25	50	ms	Note2
	Тор	ΘΥ+		75	85	-		
Viowing Anglo	Bottom	ΘΥ-	CR ≥ 10	75	85	-	Dog	Note4
Viewing Angle	Left	ΘХ-	CR 2 10	75	85	1	Deg.	Note4
	Right	ΘХ+		75	85	-		
Contrast Rat	io	CR		600	800	-	1	Note3
	White	X		0.268	0.318	0.368		
	vviile	Υ		0.302	0.352	0.402		
0.1(015	Red	Х		0.541	0.591	0.641		
Color of CIE	Reu	Υ	-	0.300	0.350	0.400		Natad
Coordinate (With B/L)	Green	X		0.293	0.343	0.393	_	Note1
(VVIdi B/L)	Olccii	Y		0.534	0.584	0.634		
	Dluc	Х		0.104	0.154	0.204		
	Blue	Y		0.099	0.149	0.199		
Average Brightr Pattern=White Di		IV	IF=80 mA	400	500	-	cd/m2	Note1
Luminance Unifo	ormity	YU	IF=80 mA	70	75	-	%	Note1

#### Note1:

- $1 : \triangle B=B(min) / B(max) \times 100\%$
- 2 : Measurement Condition for Optical Characteristics:
  - a : Environment: 25°C±5°C / 60±20%R.H → no wind → dark room below 10 Lux at typical lamp current and typical operating frequency.
  - b : Measurement Distance: 500  $\pm$  50 mm  $\rightarrow$  ( $\theta$ = 0°)
  - c: Equipment: TOPCON BM-7 fast, (field 1°), after 10 minutes operation.
  - d: The uncertainty of the C.I.E coordinate measurement ±0.01, Average Brightness ± 4%





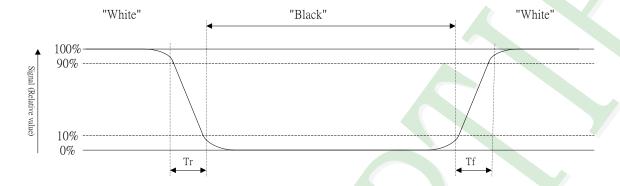
Colorimeter=BM-7 fast



#### Note2: Definition of response time:

The output signals of photo detector are measured when the input signals are changed from "black" to "white" (falling time) and from "white" to "black" (rising time), respectively. The response time is defined as the time interval between the 10% and 90% of Amplitudes.

Refer to figure as below:



Note3: Definition of contrast ratio:

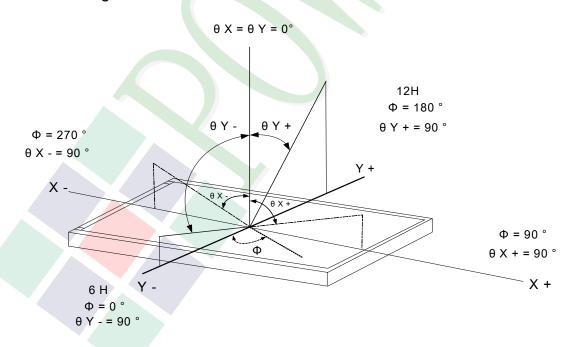
Contrast ratio is calculated with the following formula

Photo detector output when LCD is at "White" state

Contrast ratio (CR) =

Photo detector output when LCD is at "Black" state

### Note4: Definition of viewing angle: Refer to figure as below:





# 1.6 Backlight Characteristics

Maximum Ratings

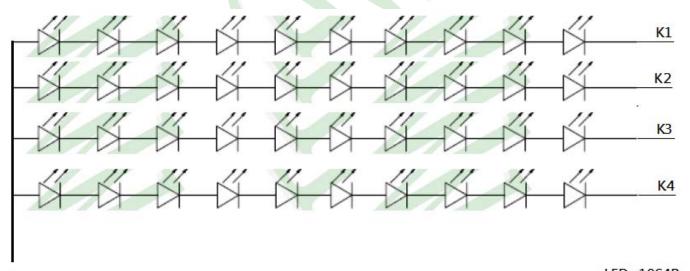
Item	Symbol	Conditions	Min.	Тур.	Max.	Unit
Power Dissipation	Pd	-	-	100	-	mW
LED Forward Current	IF	1 LED	-	-	30	mA
LED Reverse Voltage	VR	1 LED	-	-	1.2	V

### **Electrical / Optical Characteristics**

Item	Symbol	Conditions	Min.	Тур.	Max.	Unit
Voltage for LED Backlight	VF	If-90m 1	27.5	31.0	34.0	V
Current for LED Backlight	IF	IF If=80mA		80	<del>-</del>	mA
Color	White					

Other Description

Item	Conditions	Description
Life Time	Ta =25°C If= 80 mA	50000 hrs



LED=10S4P

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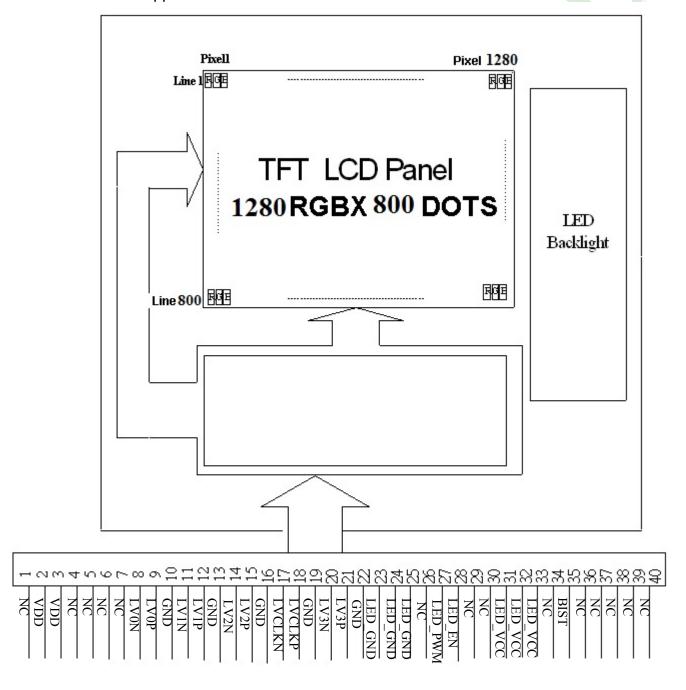


## 2. MODULE STRUCTURE

## 2.1 Counter Drawing

### 2.1.1 LCM Mechanical Diagram

\* See Appendix





# 2.2 Interface Pin Description

Pin No.	Symbol	Description
1	NC	No Connection.
2	VDD	Power Supply.
3	VDD	Power Supply.
4	VDD_EDID	VDD_EDID ( Do not connect if not used.)
5	SCL_EDID	SCL_EDID (Do not connect if not used.)
6	SDA_EDID	SDA_EDID (Do not connect if not used.)
7	NC	No Connection.
8	LV0N	-LVDS Differential Data Input.
9	LV0P	+LVDS Differential Data Input.
10	GND	Ground.
11	LV1N	-LVDS Differential Data Input.
12	LV1P	+LVDS Differential Data Input.
13	GND	Ground.
14	LV2N	-LVDS Differential Data Input.
15	LV2P	+LVDS Differential Data Input.
16	GND	Ground.
17	LVCLKN	-LVDS Differential Clock Input.
18	LVCLKP	+LVDS Differential Clock Input.
19	GND	Ground.
20	LV3N	-LVDS Differential Data Input.
21	LV3P	+LVDS Differential Data Input.
22	GND	Ground.
23	LED_GND	Ground for LED Driving.
24	LED_GND	Ground for LED Driving.
25	LED_GND	Ground for LED Driving.
26	NC	No Connection.
27	LED_PWM	PWM Input Signal for LED Driver.



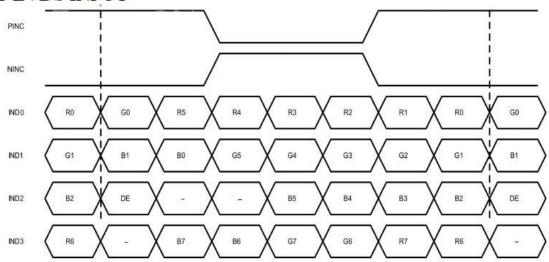
Pin No.	Symbol	Description
28	LED_EN	LED Enable Pin.
29	NC	Reserved For CABC.
30	NC	No Connection.
31	LED_VCC	Power Supply for LED Driver.
32	LED_VCC	Power Supply for LED Driver.
33	LED_VCC	Power Supply for LED Driver.
34	NC	No Connection.
35	BIST	BIST pin.
36	NC	No Connection.
37	NC	No Connection.
38	NC	No Connection.
39	NC	No Connection.
40	NC	No Connection.



# 2.3 Timing Characteristics

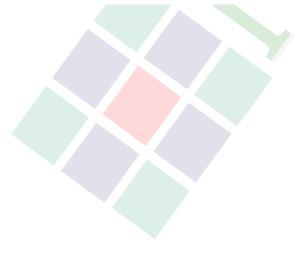
# 2.3.1 LVDS Data Input Format

# 8-BIT LVDS INPUT



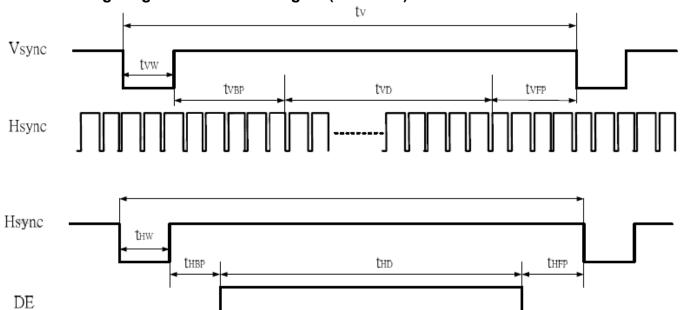
## 2.3.2 Interface Timings

Parameter	Symbol	Unit	Min.	Тур.	Max.
Frame Rate		Hz	-	60	-
Frame Period	Tv	line	815	823	1023
Vertical Display Time	TVD	line		800	
Vertical Blanking Time	Tvw+Tvbp+Tvfp	line	15	23	33
1 Line Scanning Time	Тн	clock	1410	1440	1470
Horizontal Display Time	THD	clock		1280	
Horizontal Blanking Time	THW+THBP+THFP	clock	60	160	190
Clock Rate	1/Tc	MHz	68.9	71.1	73.4





# 2.3.3 Timing Diagram of Interface Signal (DE mode)





### 2.3.4 Power Sequence

#### Power ON/OFF Sequence

Interface signals are also shown in the chart. Signals from any system shall be Hi- resistance state or low level when VDD voltage is off.

Figure 1 Power Sequence

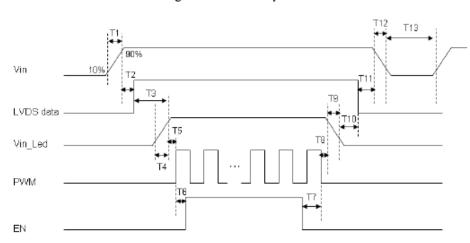
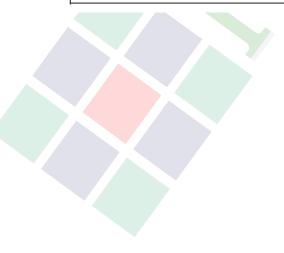


Table 1 Power Sequencing Requirements

Parameter	Symbol	Unit	Min	Тур.	Max
VIN Rise Time	T1	ms	0.5		10
VIN Good to Signal Valid	T2	ms	30		90
Signal Valid to Backlight On	T3	ms	200		
Backlight Power On Time	T4	ms	0.5		
Backlight VDD Good to System PWM On	T5	ms	10	-	
System PWM ON to Backlight Enable ON	T6	ms	10		
Backlight Enable Off to System PWM Off	T7	ms	0	-	
System PWM Off to B/L Power Disable	T8	ms	200		
Backlight Power Off Time	T9	ms	0.5	10	30
Backlight Off to Signal Disable	T10	ms	200		
Signal Disable to Power Down	T11	ms	0	-	50
VIN Fall Time	T12	ms	0.5	10	30
Power Off	T13	ms	500	-	

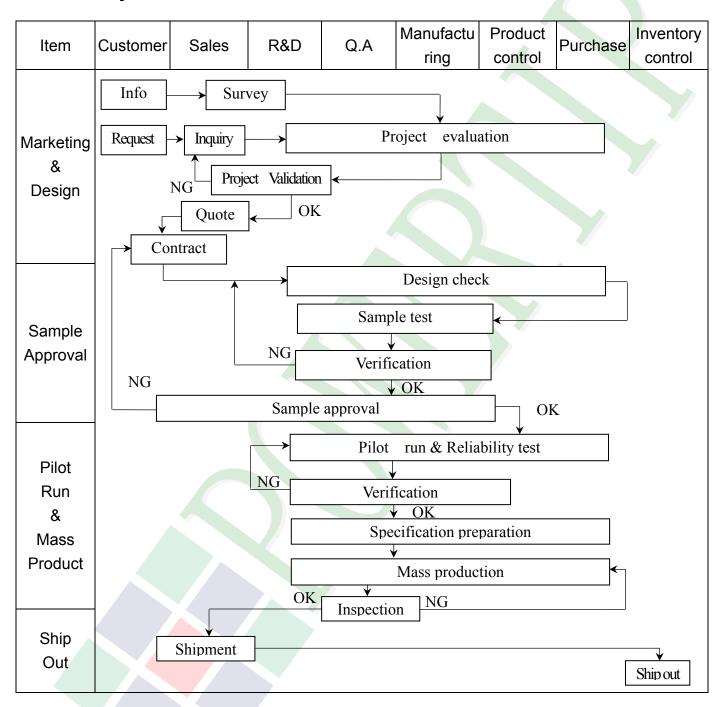


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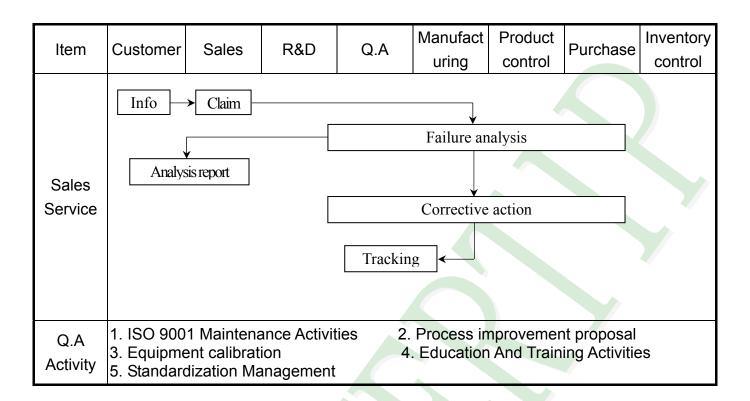


### 3. QUALITY ASSURANCE SYSTEM

# 3.1 Quality Assurance Flow Chart









### 3.2. Inspection Specification

◆Scope: The document shall be applied to TFT-LCD Module for 3. 5" −15" (Ver.B01).

♦ Inspection Standard: MIL-STD-105E Table Normal Inspection Single Sampling Level II.

**◆**Equipment: Gauge, MIL-STD, Powertip Tester, Sample

◆Defect Level: Major Defect AQL: 0. 4; Minor Defect AQL: 1. 5

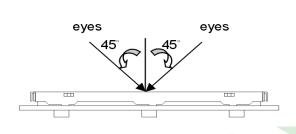
**♦**OUT Going Defect Level: Sampling.

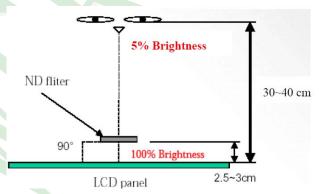
**♦**Standard of the product appearance test:

#### a. Manner of appearance test:

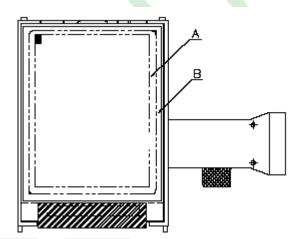
(1). The test best be under 20W×2 fluorescent light(about 300lux ~500lux) , and distance of view must be at 30~40 cm.

(2). The test direction is base on about around 45° of vertical line.





(3). Definition of area.



A area: viewing area

B area: Outside of viewing area

(4). Standard of inspection: (Unit: mm)



# ◆Specification For TFT-LCD Module 3. 5" ~15":

NO	Item	Criterion	Level			
	Product condition	1. 1The part number is inconsistent with work order of production.				
01		1. 2 Mixed product types.	Major			
		1. 3 Assembled in inverse direction.	Major			
02	Quantity	2. 1The quantity is inconsistent with work order of production.	Major			
03	Outline dimension	3. 1Product dimension and structure must conform to structure diagram.	Major			
		4. 1 Missing line character and icon.	Major			
		4. 2 No function or no display.	Major			
		4. 3 Display malfunction.	Major			
04	Electrical Testing	4. 4 LCD viewing angle defect.				
		4. 5 Current consumption exceeds product specifications.				
		4. 6Mura cannot be seen through 5% ND filter at 50% Gray, should be judged by the viewing angle of 90 degree.				
		Item Acceptance (Q'ty)				
		Bright Dot ≤ 4				
		$\mathbf{Dot} \qquad \mathbf{Dark} \ \mathbf{Dot} \qquad \leq 5$				
	Dot defect	Defect Joint Dot ≤ 3				
	(Bright dot,	Total ≤ 7				
05	Dark dot)	5. 1 Inspection pattern: full white, full black, Red, Green and	Minor			
	On -display	blue screens.				
	On -display	5. 2 It is defined as dot defect if defect area >1/2 dot.				
		5. 3 The distance between two dot defect $\geq 5$ mm.				
		5. 4 Bright dot: Dots appear bright and unchanged in visible with				
		5% ND filter is defined.				



# ◆Specification For TFT-LCD Module 3. 5″ ~15″:

NO	Item	Criterion						
	Black or white	Dimensi	B. 1 Round type (Non-display or display): $ \frac{\text{Dimension (diameter : } \Phi)}{\text{A area}} \frac{\text{Acceptant}}{\text{A area}} $ $ \Phi \le 0.25 \qquad \text{Ignore} $ $ 0.25 < \Phi \le 0.50 \qquad 5 $		nce (Q'ty) B area			
	Dot, scratch, contamination  Round type	0,120	$\Phi > 0.50$ Total	0 5	Ignore			
	$\begin{array}{c c} & & \\ \hline & & \\ \hline & & \\ \hline \end{array}$	6. 2 Line type(N	on-display or di	splay):				
06	<u>Y</u>	module size	Length (L)	Width (W)	Acceptanc A area	e (Q'ty) B area	Mino	
00	$\Phi = (x+y)/2$ Line type	3.5"	3.5" to less 9"	1 < 50 0	$W \le 0.03  03 < W \le 0.05  05 < W \le 0.10  W > 0.10$	Ignore 4 2 As round	Ignore	
		4	T		type 5 Ignore			
	→ı <sub>L</sub>	9" to 15"	L ≦10.0 0.	$05 < W \le 0.10$ $W > 0.10$	5 As round type	Ignore		
			T	otal	5			
		Dimension	n (diameter: Φ)	Accepta	nce (Q'ty)			
		Dimension	$\Phi \leq 0.25$	A area Ignore	B are	ea		
07	Polarizer Bubble	0.25 <	$<\Phi \le 0.50$	4			Mino	
	Bubble	0.50 <	< Φ ≤ 0.80	1	Ignor	re		
			$\frac{\Phi > 0.80}{\text{Total}}$	5				



# ◆Specification For TFT-LCD Module 3. 5″ ~15″:

NO	Item	Criterion	Level
		Symbols:  X: The length of crack Z: The thickness of crack t: The thickness of glass  X: The width of crack W: terminal length a: LCD side length	
		<ul><li>8. 1 General glass chip:</li><li>8. 1. 1 Chip on panel surface and crack between panels:</li></ul>	
08	The crack of glass	SP SP [NG]	Minor
		Seal width Z	
		$\leq$ a Crack can't exceed the half of SP width. 1/2 t < Z $\leq$ 2 t	



# **♦**Specification For TFT-LCD Module 3. 5″ ~15″:

NO	Item	Criterion	Level						
	Symbols:  X: The length of crack Z: The thickness of crack t: The thickness of glass  8. 1. 2 Corner crack:								
		X Z							
		X Y Z  ≤1/5 a Crack can't enter viewing area $Z$ ≤ 1/2 t							
		$\leq 1/5$ a Crack can't exceed the half of SP width. $1/2$ t $<$ Z $\leq 2$ t							
08	The crack of glass	8.2 Protrusion over terminal:	Minor						
		8.2.1 Chip on electrode pad:							
		X Y Z X Y Z							
		W							
		X Y Z							
		Front $\leq a$ $\leq 1/2  W$ $\leq t$							
		$\begin{array}{ c c c c c }\hline Back & \leq a & \leq W & \leq 1/2 t\\ \hline \end{array}$							



# ◆Specification For TFT-LCD Module 3. 5″~15″:

NO	Item	Criterion	Level
NO	The crack of glass	Symbols:  X: The length of crack Z: The thickness of crack t: The thickness of glass  8. 2. 2 Non-conductive portion:  X  X  Y  Z  S  Y  Z  S  Y  Z  S  Y  Z  S  Y  Z  S  S  S  S  S  S  S  S  S  S  S  S	Level
4		8.2.4 Cracking:  Not Allowed	



# ◆Specification For TFT-LCD Module 3. 5″ ~15″:

NO	Item	Criterion	Level
		9. 1 Backlight can't work normally.	Major
09	Backlight elements	9. 2 Backlight doesn't light or color is wrong.	Major
		9. 3 Illumination source flickers when lit.	Major
	General appearance	10. 1Pin type \quantity \quantity \dimension must match type in structure diagram.	Major
		10. 2 No short circuits in components on PCB or FPC.	Major
		10. 3 Parts on PCB or FPC must be: no wrong parts, missing parts or excess parts.	Major
10		10. 4 Product packaging must the same as specified on packaging specification sheet.	Minor
		10. 5 The folding and peeled off in polarizer are not acceptable.	Minor
		10. 6 The PCB or FPC between B/L assembled distance(PCB or FPC ) is ≤1.5 mm.	Minor



### 4. RELIABILITY TEST

# 4.1 Reliability Test Condition

(Ver.B01)

	rtenability rest con						(VCI.DOI)
NO.	TEST ITEM			TEST CO	NDITION		
1	High Temperature Storage Test	Keep in +80 ±5°C 240 hrs					
2	Low Temperature Storage Test	Keep in -30	±5℃ 240	hrs			
3	High Temperature / High Humidity Storage Test	Keep in +60 ℃ / 90% R.H duration for 240 hrs (Excluding the polarizer)					
			-30℃		→ +80°C →		
4	Temperature Cycling		30mins)	(5mins)	(30mins)	(5mins) <b>→</b>	
_	Storage Test			20 C	ycle		
		Air Discharg	ge:		<b>Contact Disc</b>	charge:	
		Apply 2 KV with 5 times		Apply 250 V	with 5 tin	nes	
	ESD Test	Discharge for			discharge for	r each pola	rity +/-
		1.Temperature ambiance : 15°C ~35°C					
5		2.Humidity relative : 30%~60%					
		3.Energy Storage Capacitance(Cs+Cd): 150pF±10%					
		4.Discharge Resistance(Rd): 330 Ω±10%					
		5.Discharge, mode of operation : Single Discharge (time between successive discharges at least 1 sec)					
			,			O	least 1 sec)
		(Tolerance if the output voltage indication: ±5%)  1. Sine wave 10~55 Hz frequency (1 min/sweep)					
, l	Vibration Test				•	)	
6	(Packaged)	2. The amplitude of vibration :1. 5 mm  3. Each direction (X \ Y \ Z) duration for 2 Hrs					
		3.Each direct	tion (X × Y	` Z) durati	on for 2 Hrs		1
			Packing V	Veight (Kg)	Drop Hei	ight (cm)	
			0	~ 45.4	12	22	
	<b>Drop Test</b>		45. 4	~ 90.8	7	6	
7	(Packaged)		90.8	~ 454	6	1	
			0ve	r 454	4	6	
		Drop Directi	on : X1 co	rner / 3 edge	es / 6 sides ea	ch 1time	•
U				ŭ			

#### **©Result Evaluation Criteria:**

Under the display quality test conditions with normal operations with normal operation state. Do not change these conditions as such changes may affect practical display function.

(Normal operation state)

Temperature: +20~30°C Humidity: 50~70%

Atmospheric pressure: 86~106Kpa



### 5. PRECAUTION RELATING PRODUCT HANDLING

#### **5.1 SAFETY**

- 5.1.1 If the LCD panel breaks, be careful not to get the liquid crystal to touch your skin.
- 5.1.2 If the liquid crystal touches your skin or clothes, please wash it off immediately by using soap and water.

#### **5.2 HANDLING**

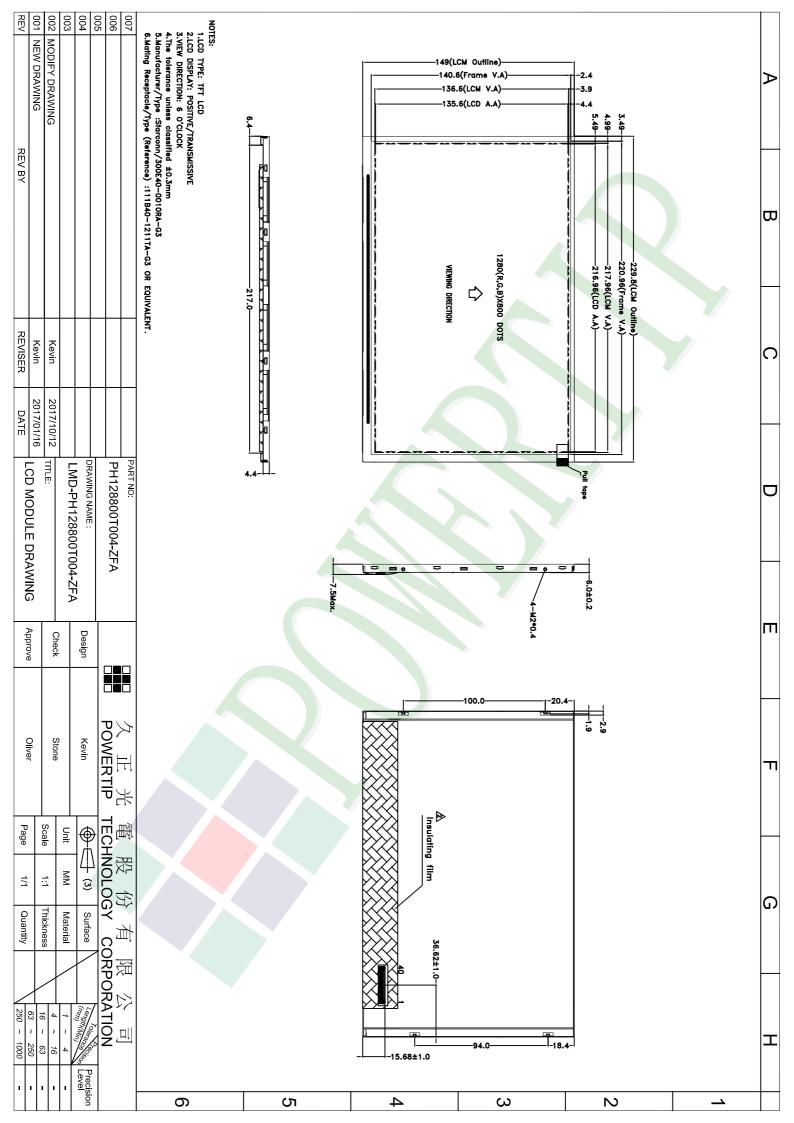
- 5.2.1 Avoid any strong mechanical shock which can break the glass.
- 5.2.2 Avoid static electricity which can damage the CMOS LSI—When working with the module, be sure to ground your body and any electrical equipment you may be using.
- 5.2.3 Do not remove the panel or frame from the module.
- 5.2.4 The polarizing plate of the display is very fragile. So, please handle it very carefully, do not touch, push or rub the exposed polarizing with anything harder than an HB pencil lead (glass, tweezers, etc.)
- 5.2.5 Do not wipe the polarizing plate with a dry cloth, as it may easily scratch the surface of plate.
- 5.2.6 Do not touch the display area with bare hands, this will stain the display area.
- 5.2.7 Do not use ketonics solvent & aromatic solvent. Use with a soft cloth soaked with a cleaning naphtha solvent.
- 5.2.8 To control temperature and time of soldering is  $320 \pm 10^{\circ}$ C and 3-5 sec.
- 5.2.9 To avoid liquid (include organic solvent) stained on LCM
- 5.2.10 Caution!( LCM products with Capacitive Touch Panel)
  Strong EMI-sources such as switch-mode power supplies (SMPS) can lead to touch malfunction (e.g. ghost-touches).
  - Therefore, the touch needs to be thoroughly tested inside the target application.
- 5.2.11 CAUTION: Continuously displaying same static image will result in high possibility of image sticking/image burn-in effect due to TFT panel characteristic.

#### **5.3 STORAGE**

- 5.3.1 Store the panel or module in a dark place where the temperature is  $25^{\circ}\text{C} \pm 5^{\circ}\text{C}$  and the humidity is below 65% RH.
- 5.3.2 Do not place the module near organics solvents or corrosive gases.
- 5.3.3 Do not crush, shake, or jolt the module.

#### 5.4 TERMS OF WARRANTY

- 5.4.1 Applicable warrant period The period is within thirteen months since the date of shipping out under normal using and storage conditions.
- 5.4.2 Unaccepted responsibility
  - This product has been manufactured to your company's specification as a part for use in your company's general electronic products. It is guaranteed to perform according to delivery specifications. For any other use apart from general electronic equipment, we cannot take responsibility if the product is used in nuclear power control equipment, aerospace equipment, fire and security systems or any other applications in which there is a direct risk to human life and where extremely high levels of reliability are required.



Check Approve Contact Ver.005 LCM包裝規格書 LCM Packaging Specifications Oliver Stone Kevin PKG-PH128800T004-ZFA Documents NO. (For Tray) 1.包裝材料規格表 (Packaging Material): (per carton) No. Item Model Dimensions (mm) 1Pcs Weight Quantity Total Weight 1 成品 (LCD) PH128800T004-ZFA 229.8 X 149 0.222 288 63.936 2 多層薄膜(1)POF OTFILM0BA03ABA 48 3 TRAY 盤 (2)Tray 192 TY00000000394 517 X 377 X 18.8 0.2 38.4 4 内盒(3)Product Box 28.8 558 X 393 X 68 0.6 48 BX00000000071 5 保利龍板(4)Polylon board 32 OTPLB00PL08ABA 550 X 393 X 20 0.0284 0.9088 外紙箱(5)Carton 6 570 X 410 X 265 1.4208 16 22.7328 BX57041027CCBA 7 舒美墊(6)EPE 224 0.7168 OTFOAMEP0001BA 333X 218 X 2.0 0.0032 8 棧板(7)Carton OTPALLET005ABA 1200 X 1000 X 140 8 1 8 9 2.總重量 (Total Weight ): 163.49 Kg±10% 3.單箱數量規格表 (Packaging Specifications and Quantity): (1)LCD quantity per box : no per tray 2 x no of tray 3 6 (2) Total LCD quantity in carton: quantity per box 6 x no of boxes 3 18 (3) Total LCM quantity in pallet: quantity per carton 18 x no. of cartons 16 288 Use empty tray 空盤 (1)多層薄膜 POF (5)外紙箱 (6)舒美墊 Carton **EPE** (2)TRAY 盤 Put products into the tray Tray (3)内盒 Product Box (4)保利龍板 Tray stacking Polylon board (7) PALLET В 特 記 事 項 (REMARK) 4. LCM上面放置2.0t EPE(舒美墊) 5. LCM下方放置1pcs, 2.0t EPE(舒美墊) 斜角 Detail B 1pcs(裁切後尺寸109x166.5) 333 圓角 · Tray 1 4. TRAY盤相疊時,需旋轉180度,請詳見B視圖 裁切線 Rotate tray 180 degrees and place on top of stack. Check the tray stack using Fig. B. 裁切線

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