SP	F	CI	FI	CA	TI	O	N	S
$\mathbf{v}$	_	v.				v		v

CUSTOMER .

SAMPLE CODE · SH102600T009-IBC03

MASS PRODUCTION CODE . PH102600T009-IBC03

SAMPLE VERSION . 01

SPECIFICATIONS EDITION . 008

DRAWING NO. (Ver.) . LMD-PH102600T009-IBC03 (Ver.003)

PACKAGING NO. (Ver.) . PKG-PH102600T009-IBC03 (Ver.003)

## **Customer Approved**

Date:

Approved	Checked	Designer
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Preliminary specification for design input

Specification for sample approval

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# **History of Version**

Date (mm / dd / yyyy)	Ver.	Edi.	Description	Page	Design by
06/21/2017	01	001	New Drawing	-	Yuan
09/01/2017	01	002	First Sample  Modify thickness \( \) dimension and LCM VA	- 4,Appendix	Yuan
10/05/2017	01	003	Modify DC Electrical Characteristics	5	Yuan
01/31/2018	01	004	Modify LCM Packaging Specifications	Appendix	Yuan
05/21/2018	01	005	Modify Optical Characteristics	6	Yuan
07/23/2018	01	006	ADD Dimension	Appendix	Yuan
09/11/2020	01	007	Modify LCM Packaging Specifications	Appendix	Yuan
04/01/2021	01	800	Modify Block Diagram  Modify Interface Pin Description	11 13	Yuan



#### **Contents**

#### 1. SPECIFICATIONS

- 1.1 Features
- 1.2 Mechanical Specifications
- 1.3 Absolute Maximum Ratings
- 1.4 DC Electrical Characteristics
- 1.5 Optical Characteristics
- 1.6 Backlight Characteristics
- 1.7 Touch Panel Characteristics

#### 2. MODULE STRUCTURE

- 2.1 Counter Drawing
- 2.2 Interface Pin Description
- 2.3 Timing Characteristics
- 2.4 AC Electrical Characteristics
- 2.5 Timing

#### 3. QUALITY ASSURANCE SYSTEM

- 3.1 Quality Assurance Flow Chart
- 3.2 Inspection Specification

#### 4. RELIABILITY TEST

4.1 Reliability Test Condition

## 5. PRECAUTION RELATING PRODUCT HANDLING

- 5.1 Safety
- 5.2 Handling
- 5.3 Storage
- 5.4 Terms of Warranty

#### Appendix:

- a. LCM Drawing
- b. LCM Packaging Specifications



## 1. SPECIFICATIONS

#### 1.1 Features

Item	Standard Value		
Display Type	1024 * 3 (RGB) * 600 Dots		
LCD Type	a-Si TFT , Normally white , Transmissive type		
Gray Scale Inversion Direction	6 o'clock		
Eyes Viewing Direction	12 o'clock		
Screen size(inch)	7.0 inch		
Color configuration	RGB-Strip		
Backlight Type	LED B/L		
Interface	LVDS Interface		
Other(controller/driver IC)	NT51008+NT52003 (Or Compatible IC)		
	THIS PRODUCT CONFORMS THE ROHS OF PTC		
ROHS	Detail information please refer website :		
	http://www.powertip.com.tw/news_detail.php?Key=1&cID=1		

# 1.2 Mechanical Specifications

Item	Standard Value	Unit
Outline Dimension	192.96(W) * 121.4(L) * 4.58(H)	mm

# LCD panel

Item	Standard Value		
Viewing Area	155.01(W) x 86.92 (L)	mm	
Active Area	154.21 (W) * 85.92 (L)	mm	

Note: For detailed information please refer to LCM drawing



# 1.3 Absolute Maximum Ratings

#### Module

Item	Symbol	Condition	Min.	Max.	Unit
Power Voltage	DV <sub>DD</sub>	-	-0.5	5	V
	Vdd	-	-0.5	15	V
	VGH	-	-0.3	42	V
	VGL	-	-20	0.3	V
Operating Temperature	Тор	-	-20	70	°C
Storage Temperature	T <sub>ST</sub>	-	-30	80	°C

## 1.4 DC Electrical Characteristics

Module GND = 0V, Ta =  $25^{\circ}C$ 

Item	Symbol	Condition	Min.	Тур.	Max.	Unit	Remark
	VDD	-	2.5	3.3	3.6	V	
	AVDD	-	8	11	13.5	V	
	VGH		19.7	20	20.3	V	
	VGL	7-	-7.1	-6.8	-6.5	V	
Input signal Voltage	VCOM	-	3.3	3.8	4.3	V	
Input Signal	VIH	-	0.7Vdd	-	VDD	V	
Voltage	VIL	-	0	-	0.3 VDD	V	
	IDD	V <sub>DD</sub> = 3.3 V Pattern= Red	-	40	60	mA	
	ladd	Avdd=11.0V Pattern= Red	1	25	45	mA	
Supply Current	Ідн	V <sub>GH</sub> =20V Pattern= Red	-	0.3	0.5	mA	Note1
	IGL	VGL=-6.8V Pattern= Red	-	0.3	0.5	mA	
	IVcom	VCOM=3.8V Pattern= Red	-	8	15	uA	

Note1:Maximum current display



# 1.5 Optical Characteristics

## **TFT LCD Module**

VDD = 3.3 V, Ta=25°C

TI I LOD MOGGIC							,	14 20 0
Item		Symbol	Condition	Min.	Тур.	Max.	unit	
Response time	Tr	+ Tf	Ta = 25°C θX, θY = 0°	-	30	45	ms	Note 2
	Тор	θΥ+		-	60	-		
Viewing angle	Bottom	θΥ-	CR ≥ 10	-	60	1	Dog	Note 4
viewing angle	Left	θX-	CR 2 10	-	60	-	Deg.	Note 4
	Right	θΧ+		-	60			
Contrast rati	0	CR		500	600	-	-	Note 3
	\\/bito	Х		0.23	0.28	0.33		
	White	Υ	Ta = 25°C θX , θY = 0°	0.29	0.34	0.39	_	
	Red	Х		0.62	0.67	0.72		
Color of CIE		Y		0.27	0.32	0.37		Note 1
Coordinate ( With B/L )	Green	Х		0.27	0.32	0.37		Note1
(		Y		0.55	0.60	0.65		
	Dluc	Х		0.09	0.14	0.19		
	Blue	Y		0.01	0.04	0.09		
Average Brightr	ness							
Pattern=white display		IV	IF=200 mA	310	330	-	cd/m <sup>2</sup>	Note1
(With LCD)*1								
Uniformity (With LCD)*	2	△B	IF=200 mA	70	-	-	%	Note1



#### Note 1:

\*1 : △B=B(min) / B(max) \* 100%

\*2 : Measurement Condition for Optical Characteristics:

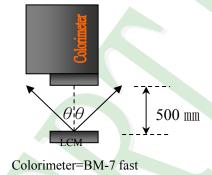
a: Environment: 25°C±5°C / 60±20%R.H, no wind, dark room below 10 Lux at typical lamp current and typical operating frequency.

b : Measurement Distance:  $500 \pm 50 \text{ mm}$ ,  $(\theta = 0^\circ)$ 

c: Equipment: TOPCON BM-7 fast, (field 1°), after 10 minutes operation.

d: The uncertainty of the C.I.E coordinate measurement ±0.01, Average Brightness ± 4%





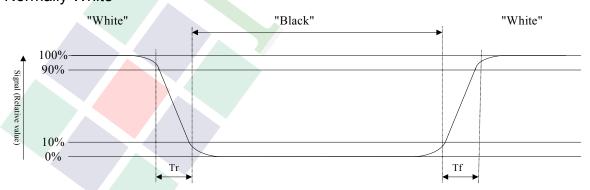
To be measured at the center area of panel with a viewing cone of 1° by Topcon luminance meter BM-7, after 10 minutes operation (module)

#### Note2: Definition of response time:

The output signals of photo detector are measured when the input signals are changed from "black" to "white" (falling time) and from "white" to "black" (rising time), respectively. The response time is defined as the time interval between the 10% and 90% of Amplitudes.

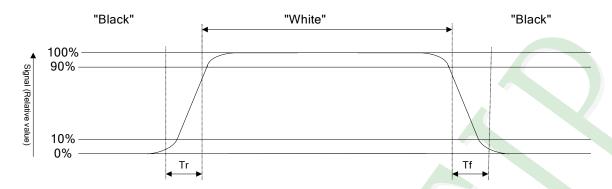
#### Refer to figure as below:

#### Normally White





#### Normally Black



Note3: Definition of contrast ratio:

Contrast ratio is calculated with the following formula

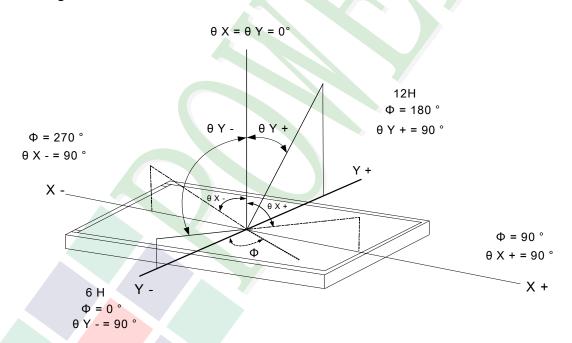
Photo detector output when LCD is at "White" state

Contrast ratio (CR) =

Photo detector output when LCD is at "Black" state

Note4: Definition of viewing angle:

Refer to figure as below:



Note5: Applying with spectrophotometer in the condition of 400 to 700nm, 10nm/each; in accordance with JIS Z 8701 2 degree viewing XYZ system, measuring the reflective rate of 5 degree



# 1.6 Backlight Characteristics

Maximum Ratings

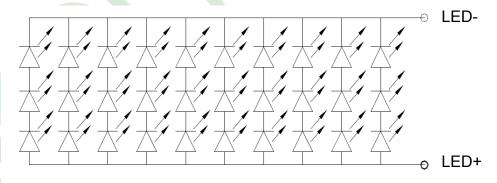
Item	Symbol	Conditions	Min.	Max.	Unit
LED Forward Current	IF	Ta =25°C	-	300	mA
LED Reverse Voltage	VR	Ta =25°℃	-	5	V
Power Dissipation	PD	Ta =25°℃	-	2040	W

## **Backlight Characteristics**

Item	Symbol	Conditions	Min.	Тур.	Max.	Unit
Forward Voltage	VF		9.0	9.6	10.2	V
Average Brightness (Without LCD )	IV	IF=200mA	12500	13200	-	cd/m <sup>2</sup>
CIE Color Coordinate	Х	IF-200IIIA	0.25	0.28	0.31	
(Without LCD)	Υ		0.28	0.31	0.34	1
Uniformity *1	∆B		80	-	-	*2
Color			White			

\*1: This value will be changed while mass production.

\*2 : △B=B(min) / B(max)% B/L Internal Circuit Diagram



Other Description

Item	Conditions	Description
Life Time	Ta =25°C IF=200 mA	20000 hrs



## 1.7 Touch Panel Characteristics

#### **Features**

Item	Standard Value	
Touch Panel Size	7.0"	
Touch type	Capacitive Touch Panel	
Input Method	True Multi-Touch Capacitive Touch Panel True Multi-touch with up to 5 Points of Absolution	
Output Interface	l <sup>2</sup> C	
IC	FT5426	

#### I<sup>2</sup>C Address

Bit 7	Bit 6	Bit 5	Bit 4	Bit 3	Bit 2	Bit 1	Bit 0
0	1	1	1	0	0	0	R/W

Bit 0: 0 for Write / 1 for Read

#### **Mechanical Specifications**

Item	Standard Value	Unit
Viewing Area	155.01 (W) * 86.92 (L)	mm

#### **DC Electrical Characteristics**

Item	Symbol	Condition	Min.	Тур.	Max.	Unit
LCM driving voltage	V <sub>DD</sub>	25°C	-	3.3	-	<b>V</b>
Input Signal Voltage	VIH		0.85 x V <sub>DD</sub>	-	-	V
iliput Signal Voltage	VIL		-	1	0.15 x V <sub>DD</sub>	٧

## **Optical Characteristics**

Item	Standard Value	
Response Time	≤25ms	
Total light transmittance	85% or more	ı
Surface Hardness	≥7H	_



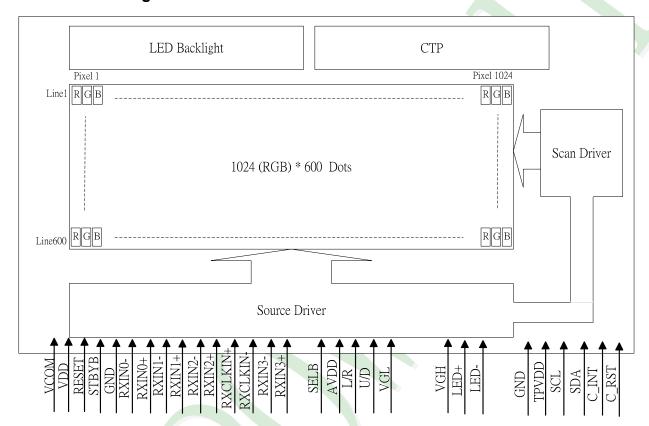
## 2. MODULE STRUCTURE

# 2.1 Counter Drawing

## 2.1.1 LCM Mechanical Diagram

\* See Appendix

#### 2.1.2 Block Diagram







# 2.2 Interface Pin Description

Pin No.	Symbol	Function
1	VCOM	Common Voltage
2	VDD	Power Voltage for digital circuit
3	VDD	Power Voltage for digital circuit
4	NC	No connection
5	Reset	Global reset pin
6	STBYB	Standby mode, Normally pulled high STBYB = "1", normal operation STBYB = "0", timing controller, source driver will turn off, all output are High-Z
7	GND	Ground
8	RXIN0-	- LVDS differential data input
9	RXIN0+	+ LVDS differential data input
10	GND	Ground
11	RXIN1-	- LVDS differential data input
12	RXIN1+	+ LVDS differential data input
13	GND	Ground
14	RXIN2-	- LVDS differential data input
15	RXIN2+	+ LVDS differential data input
16	GND	Ground
17	RXCLKIN-	- LVDS differential clock input
18	RXCLKIN+	+ LVDS differential clock input
19	GND	Ground
20	RXIN3-	- LVDS differential data input
21	RXIN3+	+ LVDS differential data input
22	GND	Ground
23	NC	No Connection
24	NC	No Connection
25	GND	Ground



Pin No.	Symbol	Function
26	NC	No Connection
27	NC	No Connection
28	SELB	6bit/8bit mode select  If LVDS input data is 6 bits ,SELB must be set to High;  If LVDS input data is 8 bits ,SELB must be set to Low.
29	AVDD	Power for Analog Circuit
30	GND	Ground
31	LED-	LED Cathode
32	LED-	LED Cathode
33	L/R	Horizontal inversion When L/R="0", set right to left scan direction. When L/R="1", set left to right scan direction.
34	U/D	Vertical inversion When U/D="0", set top to bottom scan direction. When U/D="1", set bottom to top scan direction.
35	VGL	Gate OFF Voltage
36	NC	No Connection
37	NC	No Connection
38	VGH	Gate ON Voltage
39	LED+	LED Anode
40	LED+	LED Anode



# **Capacitive Touch Panel (CTP) Interface**

Pin No.	Symbol	Function
1	GND	Ground.
2	TPVDD	Power.
3	SCL	I <sup>2</sup> C Clock.
4	SDA	I <sup>2</sup> C Data.
5	C_INT	The interrupt from the CTP to the Host H: CTP interrupt not requested L: CTP request interrupt
6	C_RST	RESET.

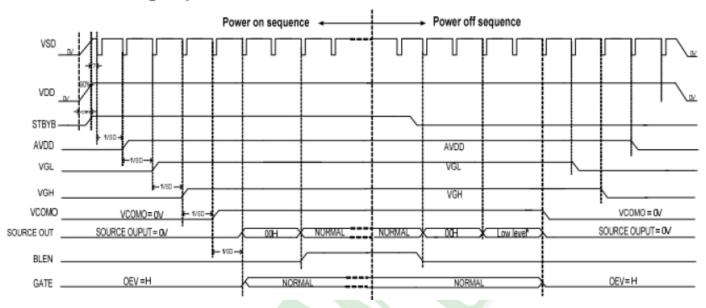


### 2.3 Timing Characteristics

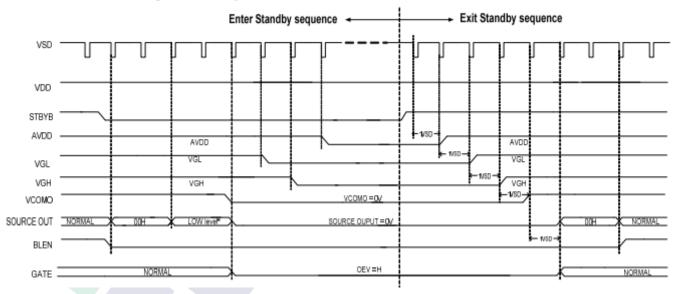
### 2.3.1 Power Sequence

In order to prevent IC from power on reset fail, the rising time (T<sub>POR</sub>) of the digital power supply VDD should be maintained within the given specifications. Refer to "AC Characteristics" for more detail on timing.

#### Power-On/Off Timing Sequence:



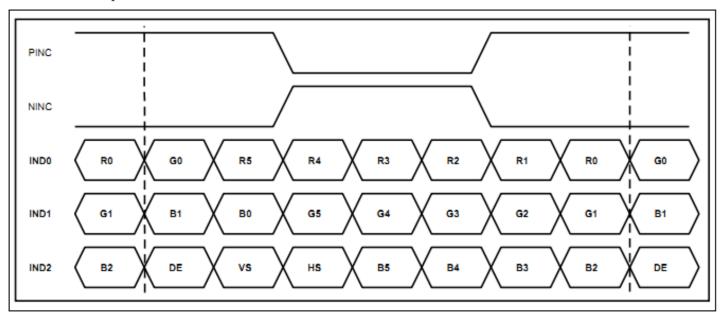
#### Enter and Exit Standby Mode Sequence:



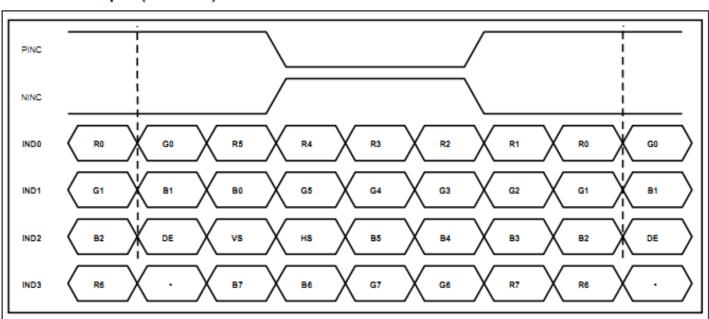


# 2.3.2 Data Input Format for LVDS

## 6bit LVDS input



#### 8-bit LVDS input (HSD='L')

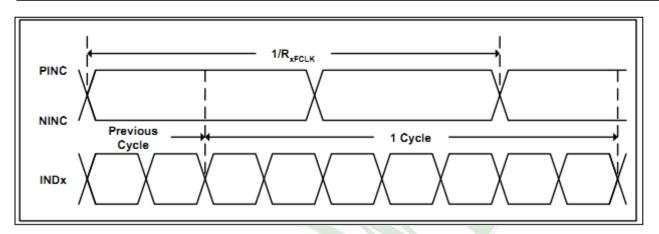




## 2.4 AC Electrical Characteristics

#### LVDS mode

Parameter	Symbol	Min.	Тур.	Max.	Unit	Condition
Clock frequency	R <sub>xFCLK</sub>	20		71	MHz	
Input data skew margin	T <sub>RSKM</sub>	500			pS	V <sub>ID</sub>   = 400mV R <sub>XVCM</sub> = 1.2V R <sub>XFCLK</sub> = 71 MHz
Clock high time	T <sub>LVCH</sub>		4/(7* R <sub>xFCLK</sub> )		ns	
Clock low time	T <sub>LVCL</sub>		3/(7* R <sub>xFCLK</sub> )		ns	
PLL wake-up time	TenPLL			150	uS	



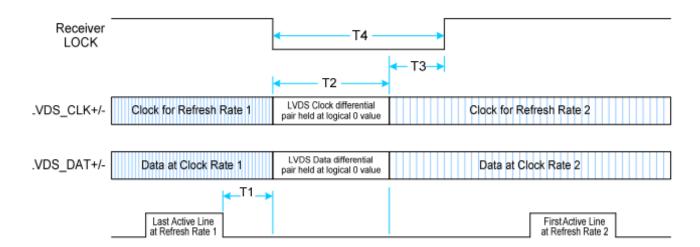




#### 2.5 Timing

#### SDRRS (seamless display refresh rate switching)

When Showing the still picture, it is accept to reduce the refresh rate from 60Hz to low refresh rate (for example 40Hz). The purpose is mainly for power saving. INTEL defined a timing chart switch between different refresh rate. Following this timing chart, the switch between different refresh rates is seamless for end user.



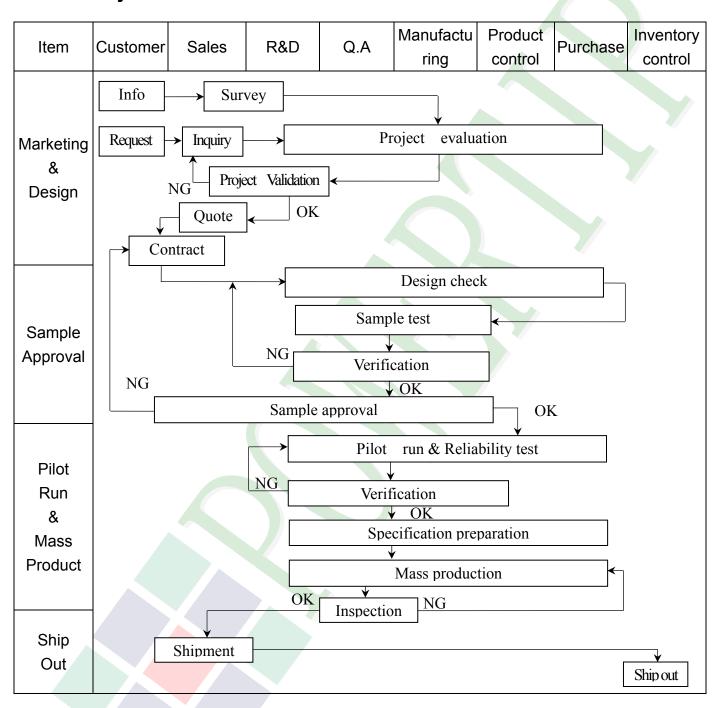
- T1 Min delay from start of vert blank to start of timing change: 2 lines (HSYNC periods)
- T2 Max delay for clock to transition to new frequency: 100us
- T3 Max receiver lock delay from stable clock: Display specific (TBD)
- T4 Max period during which panel maintains display (T2+T3): Display specific (TBD)



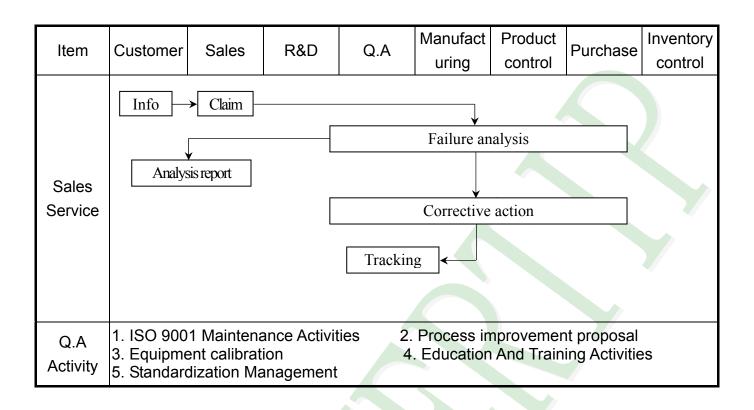


## 3. QUALITY ASSURANCE SYSTEM

### 3.1 Quality Assurance Flow Chart









## 3.2. Inspection Specification

◆Scope: The document shall be applied to TFT-LCD Module for 3.5" ~15" (Ver.B01).

◆Inspection Standard: MIL-STD-105E Table Normal Inspection Single Sampling Level Ⅱ.

**◆**Equipment : Gauge · MIL-STD · Powertip Tester · Sample

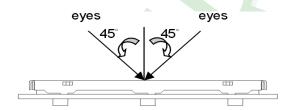
◆Defect Level: Major Defect AQL: 0.4; Minor Defect AQL: 1.5

**OUT Going Defect Level: Sampling.** 

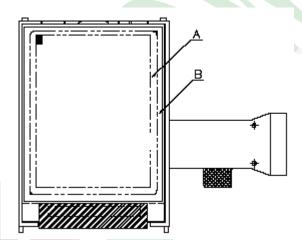
**♦**Standard of the product appearance test:

a. Manner of appearance test:

- (1). The test best be under 20W×2 fluorescent light, and distance of view must be at 30 cm.
- (2). The test direction is base on about around 45° of vertical line.



(3). Definition of area.



A area: viewing area

B area: Outside of viewing area

(4). Standard of inspection: (Unit: mm)



# **♦**Specification For TFT-LCD Module 3. 5″ ~15″:

NO	Item	Criterion	Level				
		1. 1The part number is inconsistent with work order of production.					
01	Product condition	1. 2 Mixed product types.	Major				
		, 3 Assembled in inverse direction.					
02	Quantity	2. 1The quantity is inconsistent with work order of production.	Major				
03	Outline dimension	3. 1 Product dimension and structure must conform to structure diagram.	Major				
		4. 1 Missing line character and icon.	Major				
		4. 2 No function or no display.	Major				
	Electrical Testing	4. 3 Display malfunction.					
04		4. 4 LCD viewing angle defect.					
		4. 5 Current consumption exceeds product specifications.					
		4. 6 Mura can not be seen through 5% ND filter. (Mura: Under the normal examination angle of view,the picture has the non-uniform phenomenon.)	Minor				
		Item Acceptance (Q'ty)					
		Bright Dot ≤ 4					
	Dot defect	Dot Dark Dot ≤ 5					
		Defect   Joint Dot   ≤ 3					
05	(Bright dot \ Dark dot)	Total $\leq 7$	Minor				
03	On -display  5. 1 Inspection pattern: full white, full black, Red, Green and blue screens.  5. 2 It is defined as dot defect if defect area >1/2 dot.  5. 3 The distance between two dot defect ≥5 mm.  5. 4 Bright dot that can not be seen through 5% ND filter						



## **♦** Specification For TFT-LCD Module 3. 5″ ~15″:

NO NO	Item	Criterion					
		6. 1 Round type ( Non-display or display):	Level				
		Dimension (diameter : Φ) Acceptance (Q'ty) A area B area					
	Black or white dot > scratch >	$ \Phi \leq 0.25 \qquad \text{Ignore} \\ 0.25 < \Phi \leq 0.50 \qquad 5 $					
	contamination	$\begin{array}{c cccc} \hline & \Phi & > 0.50 & & & \\ \hline & Total & & 5 & & \\ \hline \end{array}$					
	Round type $\rightarrow X \leftarrow \downarrow$	6. 2 Line type( Non-display or display):					
06	Y	module size	Minor				
00	$\Phi = (x+y)/2$	$ \begin{array}{c ccccccccccccccccccccccccccccccccccc$	TVIIIIOI				
	Line type	3.5" to less 9" $ \begin{array}{ c c c c c c c c c c c c c c c c c c c$					
	✓ ✓ W	Total 5					
	→ L ←	$ \begin{array}{c ccccccccccccccccccccccccccccccccccc$					
		9" to 15" $W > 0.10$ As round type Ignore					
		Total 5					
		Dimension (diameter : Φ) Acceptance (Q'ty)					
		$\Phi \leq 0.25 \qquad \qquad \begin{array}{c cccc} A \text{ area} & B \text{ area} \\ \hline & & & & \\ \hline & & & & \\ \hline & & & & \\ \hline & & & &$					
07	Polarizer Bubble	$0.25 < \Phi \leq 0.50 \qquad \qquad 4$	Minor				
	Dubble	$0.50 < \Phi \le 0.80$ 1 Ignore					
		$\Phi > 0.80 \qquad 0$					
		Total 5					



## ◆Specification For TFT-LCD Module 3. 5″ ~15″:

NO	Item	Criterion		Level
		Z: The thickness of crack	Y : The width of crack. V : terminal length a : LCD side length	
		8. 1 General glass chip: 8. 1. 1 Chip on panel surface and cra	ck between panels:	
		Z Z	Z Y	
08	The crack of glass	SP Y  [OK]	[NG]	Minor
		Seal width Z	Y	
		X	z	
		≤ a Crack can't enter viewing area	≦1/2 t	
		≤ a Crack can't exceed the half of SP width.	$1/2 t < Z \leq 2 t$	



## ◆Specification For TFT-LCD Module 3. 5″ ~15″:

Z: The thickness of crack t: The thickness of glass  8. 1. 2 Corner crack:    X	Criterion L				Level				
	Symbols:  X: The length of crack Z: The thickness of crack t: The thickness of glass  Y: The width of crack. W: terminal length a: LCD side length								
	X V								
The crack of glass  8. 2 Protrusion over terminal: 8. 2. 1 Chip on electrode pad:  X Y Z							t		
8. 2. 1 Chip on electrode pad:  X  X  X  X  X  Z  X  X  X  X  X  X  X		1	]	1/2	t <	<b>Z</b> ≦	≦ 2 t		
X Y Z	8.2 Protrusion over terminal:					Minor			
X Y Z									
X Y Z	//	\ <u>\</u>	<u>}</u>		X	Y	Z		
	X	X	X	X	W	<b>X</b>			
Front $\leq a$ $\leq 1/2 \mathrm{W}$ $\leq t$									
						<b>≤</b> t			
$\begin{array}{ c c c c c } \hline Back & \leq a & \leq W & \leq 1/2 t \\ \hline \end{array}$	V	W	W	W		≦ :	1/2 t		



## **♦**Specification For TFT-LCD Module 3. 5″ ~15″:

NO	Item	Criterion		
NO 08	The crack of glass		Level	
		$\begin{array}{c cccc} X & Y & Z \\ & \leq a & \leq 1/3 \text{ W} & \leq t \\ \hline \textbf{8.2.4 Cracking} & & & \\ \hline \textbf{Not Allowed} & & & \\ \hline \end{array}$		



**♦** Specification For TFT-LCD Module 3. 5″ ~15″:

NO	Item	Criterion	
09		9. 1 Backlight can't work normally.	Major
	Backlight elements	9. 2 Backlight doesn't light or color is wrong.	Major
		9. 3 Illumination source flickers when lit.	Major
10	General	10. 1 Pin type \quantity \quantity \dimension must match type in structure diagram.	Major
		10. 2 No short circuits in components on PCB or FPC.	Major
		10. 3 Parts on PCB or FPC must be the same as on the production characteristic chart .There should be no wrong parts, missing parts or excess parts.	Major
		10. 4 Product packaging must the same as specified on packaging specification sheet.	Minor
		10. 5 The folding and peeled off in polarizer are not acceptable.	Minor
		10. 6 The PCB or FPC between B/L assembled distance(PCB or FPC) is ≤1.5 mm.	Minor



# 4. 1 Reliability Test Condition

NO.	TEST ITEM	TEST CONDITION (Ver.DO1)			
1	High Temperature Storage Test	Keep in +80 ±2°C 96 hrs Surrounding temperature, then storage at normal condition 4hrs.			
2	Low Temperature Storage Test	Keep in −30 ±2°C 96 hrs Surrounding temperature, then storage at normal condition 4hrs.			
3	High Temperature / High Humidity Storage Test	Keep in +60°C / 90% R.H duration for 96 hrs Surrounding temperature, then storage at normal condition 4hrs. (Excluding the polarizer)			
4	ESD Test	Air Discharge: (include mobile phone) Apply 2 KV with 5 times Discharge for each polarity +/-  1. Temperature ambiance:15°C ~35°C 2. Humidity relative:30% ~60% 3. Energy Storage Capacitance(Cs+Cd):150pF±10% 4. Discharge Resistance(Rd):330 Ω±10% 5. Discharge, mode of operation: Single Discharge (time between successive discharges at least 1 s) (Tolerance if the output voltage indication: ±5%)			
5	Temperature Cycling Storage Test	$-30^{\circ}\text{C} \rightarrow +25^{\circ}\text{C} \rightarrow +80^{\circ}\text{C} \rightarrow +25^{\circ}\text{C}$ $(30\text{mins})  (5\text{mins})  (5\text{mins})$ $10 \text{ Cycle}$ Surrounding temperature, then storage at normal condition 4hrs.}			
6	Vibration Test (Packaged)	<ol> <li>Sine wave 10~55 Hz frequency (1 min)</li> <li>The amplitude of vibration :1.5 mm</li> <li>Each direction (X \ Y \ Z) duration for 2 Hrs</li> </ol>			
7	Drop Test (Packaged)	Packing Weight (Kg)  0 ~ 45. 4  45. 4 ~ 90. 8  90. 8 ~ 454  Over 454  Drop direction: ** 1 corner / 3 ed	76 61 46		



#### 5. PRECAUTION RELATING PRODUCT HANDLING

#### 5.1 SAFETY

- 5.1.1 If the LCD panel breaks, be careful not to get the liquid crystal to touch your skin.
- 5.1.2 If the liquid crystal touches your skin or clothes, please wash it off immediately by using soap and water.

#### 5.2 HANDLING

- 5.2.1 Avoid any strong mechanical shock which can break the glass.
- 5.2.2 Avoid static electricity which can damage the CMOS LSI—When working with the module, be sure to ground your body and any electrical equipment you may be using.
- 5.2.3 Do not remove the panel or frame from the module.
- 5.2.4 The polarizing plate of the display is very fragile. So, please handle it very carefully, do not touch, push or rub the exposed polarizing with anything harder than an HB pencil lead (glass, tweezers, etc.)
- 5.2.5 Do not wipe the polarizing plate with a dry cloth, as it may easily scratch the surface of plate.
- 5.2.6 Do not touch the display area with bare hands, this will stain the display area.
- 5.2.7 Do not use ketonic solvent & aromatic solvent. Use with a soft cloth soaked with a cleaning naphtha solvent.
- 5.2.8 To control temperature and time of soldering is  $320 \pm 10^{\circ}$ C and  $3 \sim 5$  sec.
- 5.2.9 To avoid liquid (include organic solvent) stained on LCM.
- 5.2.10 Caution! (LCM products with Capacitive Touch Panel)
  Strong EMI-sources such as switch-mode power supplies (SPS) can lead to touch malfunction (e.g., ghost-touches). Therefore, the touch needs to be thoroughly tested inside the target application.
- 5.2.11 CAUTION: Continuously displaying same static image will result in high possibility of image sticking/image burn-in effect due to TFT panel characteristic.
- 5.2.12 Double-sided tape designed to be attached with the customer's mechanical device, please follow up the rules and regulations published by the original manufacturer of double-side tape for the attachment operation.

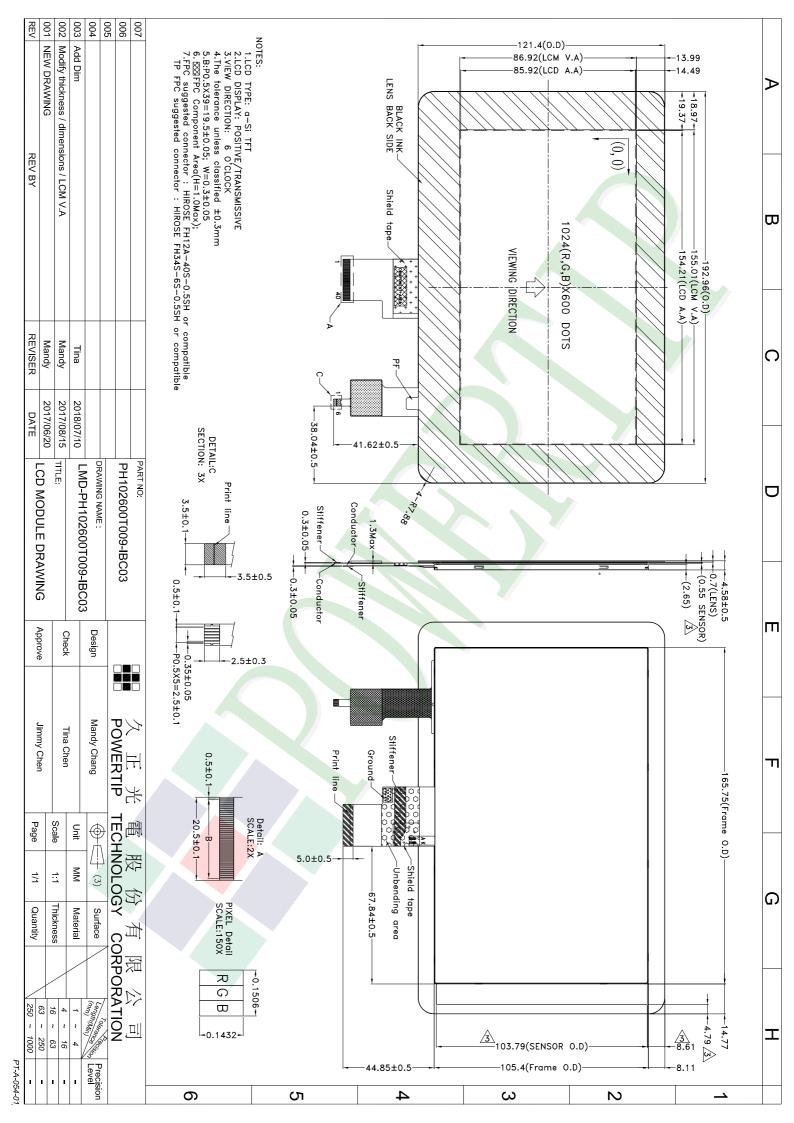
#### **5.3 STORAGE**

- 5.3.1 Store the panel or module in a dark place where the temperature is  $25^{\circ}$ C  $\pm 5^{\circ}$ C and the humidity is below 65% RH.
- 5.3.2 Do not place the module near organics solvents or corrosive gases.
- 5.3.3 Do not crush, shake, or jolt the module.

#### **5.4 TERMS OF WARRANTY**

- 5.4.1 Applicable warrant period

  The period is within thirteen months since the date of shipping out under normal using and storage conditions.
- 5.4.2 Unaccepted responsibility
  - This product has been manufactured to your company's specification as a part for use in your company's general electronic products. It is guaranteed to perform according to delivery specifications. For any other use apart from general electronic equipment, we cannot take responsibility if the product is used in nuclear power control equipment, aerospace equipment, fire and security systems or any other applications in which there is a direct risk to human life and where extremely high levels of reliability are required.



Approve Check Contact Ver.003 LCM包裝規格書 Tina Documents NO. PKG-PH102600T009-IBC03 LCM Packaging Specifications Jimmy Jimmy 1.包裝材料規格表 (Packaging Material): (per carton) No. Item Model Dimensions (mm) 1Pcs Weight Total Weight Quantity 1 成品 (LCM) PH102600T009-IBC03 192.96X121.4X4.58 10.122 0.1687 60 2 靜電袋(1)Antistatic Bag BAG240170ARABA 240 X 170 0.0048 60 0.288 3 氣泡袋(2)Bubble Bag 170 X 150 60 0.27 BAG170150BRABA 0.0045 4 A9隔板(3)A9 Partition BX0000000058 245 X 125 X 4 0.0204 1.3056 64 5 B9隔板(4)B9 Partition BX0000000057 295 X 125 X 4 0.0209 8 0.1672 海綿墊(5)Foam Rubber Cushion 8 6 OTFOAM00006ABA 290 X 240 X 10 0.02 0.16 7 C5内盒(6)Product Box BX0000000059 310 X 255 X 155 0.248 4 0.992 8 外紙箱(7)Carton BX52732536CCBA 527 X 325 X 360 0.83 1 0.83 9 保麗龍板(8)Polylon board OTPLB00000017 510 X 310 X 15 0.025  $\sqrt{3}$ 3 0.075 2.一 整箱總重量 (Total LCD Weight in carton ): 14.21 Kg±10% 3.單箱數量規格表 (Packaging Specifications and Quantity): (1)Quantity Of Spacer: A9隔板 X 16 , B9隔板 X 2 (2)Total LCM quantity in carton: quantity per box 60 15 x no of boxes (5)海綿墊 Foam Rubber Cushion (8)保麗龍板 (1)靜電袋+(2)氣泡袋+LCM Polylon board Antistatic Bag+Bubble Bag+LCM (3)(4)隔板 Partition (註 Remark 1) (5)海綿墊 Foam Rubber Cushion 11 (7)外紙箱 Carton (6) C5内盒 Product Box 特 記 事 項 (REMARK) 4. LCM排放示意圖(前後間隔不放置): 4. LCM placed as figure showing: (First and last slot should be empty)

Ø 模組(LCM) X 1pcs.