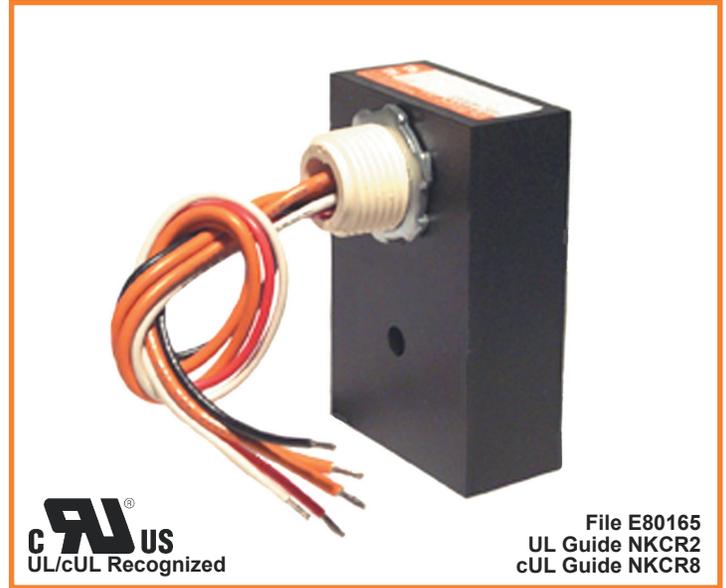




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QSC SERIES DIGITAL ENCAPSULATED TIME DELAY MODULES



UL
UL/cUL Recognized

File E80165
UL Guide NKCR2
cUL Guide NKCR8

FEATURES

- C/MOS Digital Circuitry
- Time Delays to 1000 Minutes
- No First Cycle Effect
- Fully Solid State and Encapsulated
- 0.5% Repeat Accuracy
- Seven Different Modes of Operation
- Output Rated at 1 Ampere Continuous, 10 Amperes Inrush
- Fixed or Adjustable Time Delays
- Small Size
- UL/cUL Recognized

SPECIFICATIONS

1. Time Delay

- 1.1 Type: C/MOS Digital Circuitry
- 1.2 Range: From 0.05 Seconds to 1000 Minutes
Fixed Delays Available (See Time Delay Range Chart)
- 1.3 Repeat Accuracy: $\pm 0.5\%$ Under Fixed Conditions
- 1.4 Setting Accuracy: $\pm 10\%$
- 1.5 Reset Time: 50 Milliseconds Maximum
- 1.6 Recycle Time: 100 Milliseconds During Timing
50 Milliseconds After Timing
- 1.7 Time Delay vs. Voltage and Temperature: $\pm 2\%$

2. Input

- 2.1 Operating Voltage: 24, 120, & 230 VAC, 12, & 24/28 VDC
- 2.2 Tolerance: $\pm 20\%$ of Nominal
- 2.3 Frequency: 50 - 60 Hertz

3. Output

- 3.1 Type: Solid State
- 3.2 Form: SPST
- 3.3 Rating: 1 Amp Steady State (10 Amp Inrush, 20mA min.)
- 3.4 Life: 100,000,000 Operations Minimum Under Full Load

4. Protection

- 4.1 Transient: ± 1500 Volts for 150 Microseconds
- 4.2 Polarity: DC Units Are Reverse Polarity Protected
- 4.3 Dielectric Breakdown: 1500 Volts RMS Minimum

5. Mechanical

- 5.1 Mounting: 1/2" Conduit Nipple/Locknut
- 5.2 Termination: 6" (Min.) #20 AWG Wire Leads
- 5.3 Style: Encapsulated 2" x 3"

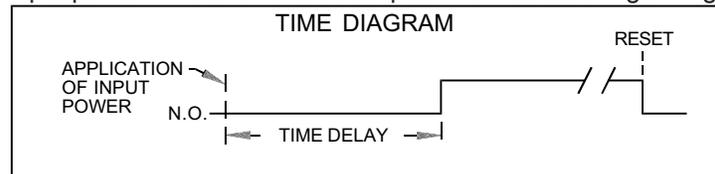
6. Environmental

- 6.1 Operating Temperature: -20°C to $+80^{\circ}\text{C}$
- 6.2 Storage Temperature: -30°C to $+85^{\circ}\text{C}$
- 6.3 Humidity: 95% Relative Non-Condensing

MODE OF OPERATION

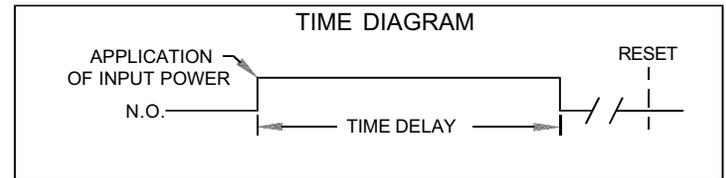
DELAY ON MAKE

Upon application of power to the input terminals, the time delay begins. At the completion of the pre-selected time delay, the output contact transfers. Reset is accomplished by removal of input power. There is no false output when reset during timing.



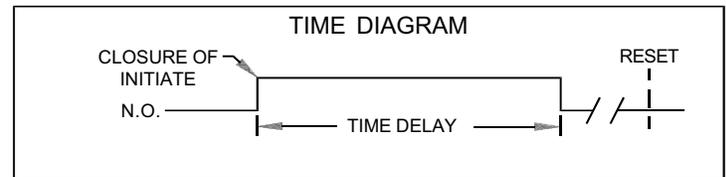
INTERVAL

Upon application of power to the input terminals, the output contact immediately transfers and the time delay begins. At the completion of the pre-selected time delay, the output contact reverts to its original position. Reset is accomplished by removal of input power.



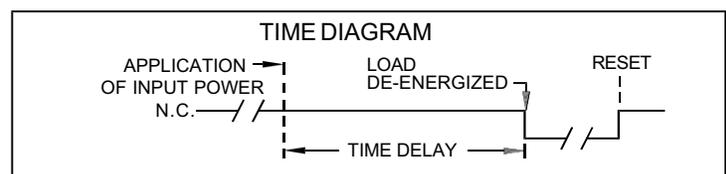
SINGLE SHOT

Power must be applied to the input at all times prior to and during timing. Upon closure of the initiate switch (momentary or maintained) the output contact transfers and the time delay begins. At the completion of the pre-selected delay period, the output contact reverts to its original position. Removal of input power will reset the control.



DELAY ON MAKE, NORMALLY CLOSED QSCC

The output is in a normally closed state. Upon application of power to the input terminals, the output contact transfers and the time delay begins. At the completion of the time delay the output contact drops out. Removal of input power from terminal 3 resets the delay and the output contact reverts to its original closed position.



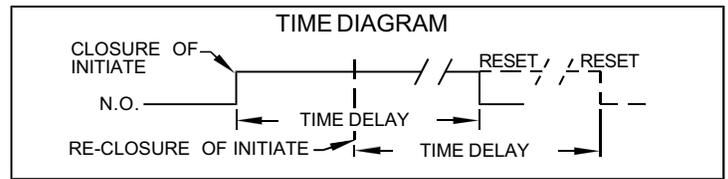
SERIES

QMSC

RETRIGGERABLE ONE-SHOT

QOSC

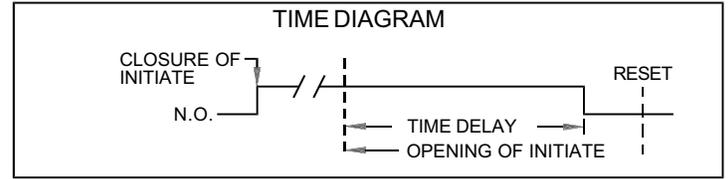
Power must be applied to the input at all times prior to and during timing. Upon closure of the initiate switch (momentary or maintained) the output contact transfers and the time delay begins. At the completion of the pre-selected delay period, the output contact reverts to its original position. **NOTE:** Momentary or maintained closure of initiate switch during timing will reset the time delay.



DELAY ON BREAK

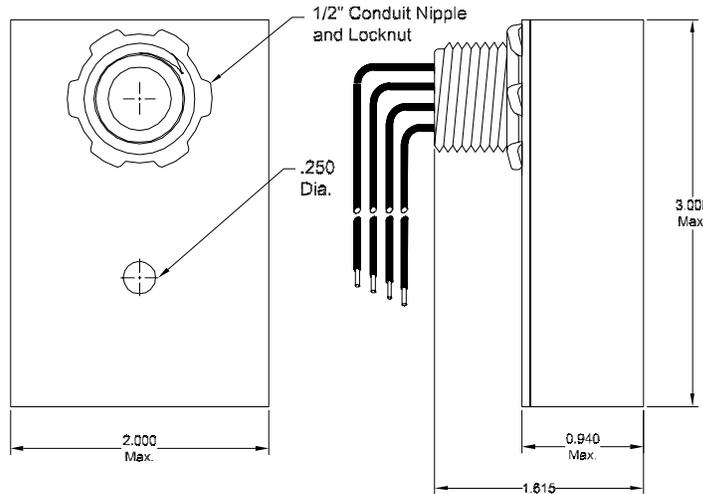
QBSC/QTSC*

Power must be applied to the input at all times prior to and during timing. Upon closure of the initiate switch, the output contact transfers and remains transferred if no further action is taken. When the initiate switch is opened, the time delay begins. At the completion of the pre-selected delay period the output contact reverts to its original position. Closure of initiate during timing will reset the delay period. Removal of input power will reset the control.

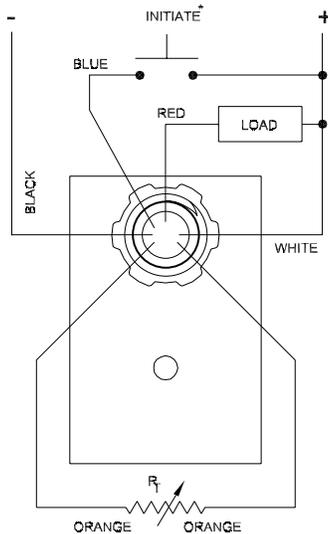


*QTSC is the same except it is trailing edge triggered. Load energizes when initiate switch is opened.

DIMENSIONS

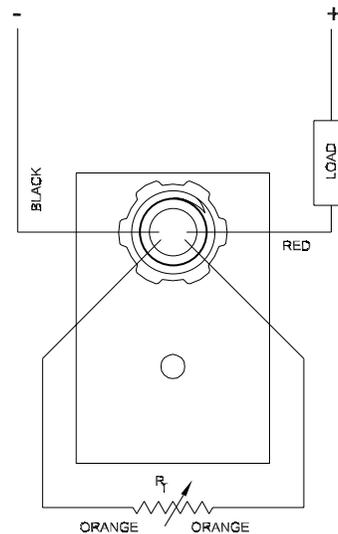


CONNECTION DIAGRAMS



ALL OTHERS

***NOT USED ON QCSC & QISC**



QMSC

ORDERING INFORMATION

SERIES	INPUT VOLTAGE	CYCLE	TIME DELAY RANGE
QBSC	1 - 12 VDC	1 - Fixed	See Time Delay Range Chart
QCSC	2 - 24/28 VDC	2 - External Adjust	
QISC	3 - 5 VDC		
QMSC	4 - 24 VAC		
QOSC	5 - 120 VAC		
QSSC	6 - 230 VAC		
QTSC	9 - 36 VDC		