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Q SERIES DIGITAL ENCAPSULATED TIME DELAY MODULES

FEATURES

- C/MOS Digital Circuitry
- Time Delays to 1000 Minutes
- No First Cycle Effect
- Fully Solid State and Encapsulated
- 0.5% Repeat Accuracy
- Seven Different Modes of Operation
- Output Rated at 1 Ampere Continuous, 10 Amperes Inrush
- Fixed, Local, or Externally Adjustable Time Delays
- Small Size
- UL/cUL Recognized

SPECIFICATIONS

1. Time Delay

- 1.1 Type: C/MOS Digital Circuitry
- 1.2 Range: From 0.05 Seconds to 1000 Minutes
Fixed Delays Available (See Time Delay Chart)
- 1.3 Repeat Accuracy: $\pm 0.5\%$ Under Fixed Conditions
- 1.4 Setting Accuracy: $\pm 10\%$
- 1.5 Reset Time: 50 Milliseconds Maximum
- 1.6 Recycle Time: 100 Milliseconds During Timing
50 Milliseconds After Timing
- 1.7 Time Delay vs. Voltage and Temperature: $\pm 2\%$

2. Input

- 2.1 Operating Voltage: 24, 120, & 230 VAC
12, 24/28, & 36 VDC
- 2.2 Tolerance: $\pm 20\%$ of Nominal
- 2.3 Frequency: 50 - 60 Hertz

3. Output

- 3.1 Type: Solid State
- 3.2 Form: SPST
- 3.3 Rating: 1 Amp Steady State, (10 Amp Inrush, 20mA Min.)
- 3.4 Life: 100,000,000 Operations Minimum Under Full Load

4. Protection

- 4.1 Transient: ± 1500 Volts for 150 Microseconds
- 4.2 Polarity: DC Units Are Reverse Polarity Protected
- 4.3 Dielectric Breakdown: 1500 Volts RMS Minimum

5. Mechanical

- 5.1 Mounting: One #8 or #10 Screw
- 5.2 Termination: 1/4" Quick Connect Terminals
- 5.3 Style: Surface Mount Encapsulated

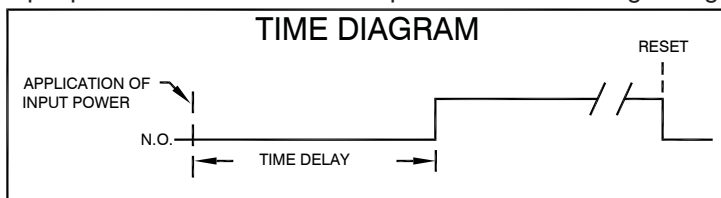
6. Environmental

- 6.1 Operating Temperature: -20°C to $+80^{\circ}\text{C}$
- 6.2 Storage Temperature: -30°C to $+85^{\circ}\text{C}$
- 6.3 Humidity: 95% Relative Non-Condensing

MODE OF OPERATION

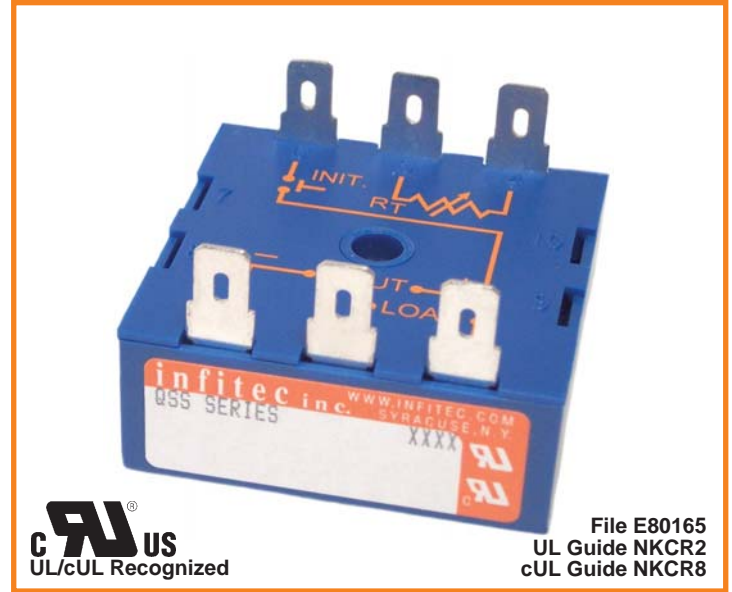
DELAY ON MAKE

Upon application of power to the input terminals, the time delay begins. At the completion of the pre-selected time delay, the output contact transfers. Reset is accomplished by removal of input power. There is no false output when reset during timing.



SERIES

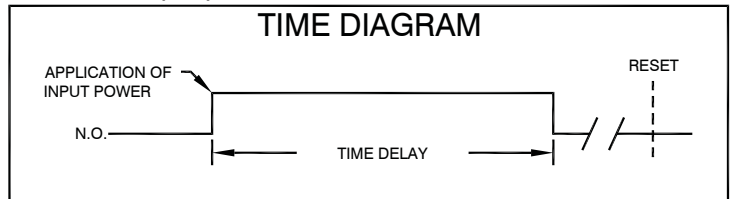
QMS, QMSA



File E80165
 UL Guide NKCR2
 cUL Guide NKCR8

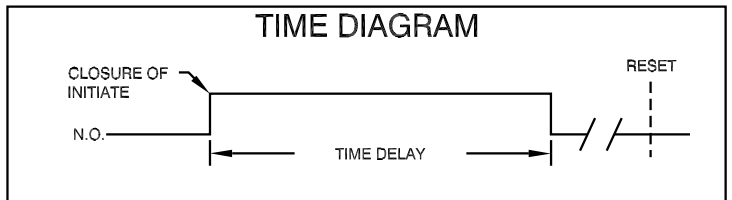
INTERVAL

Upon application of power to the input terminals, the output contact immediately transfers and the time delay begins. At the completion of the pre-selected time delay, the output contact reverts to its original position. Reset is accomplished by removal of input power.



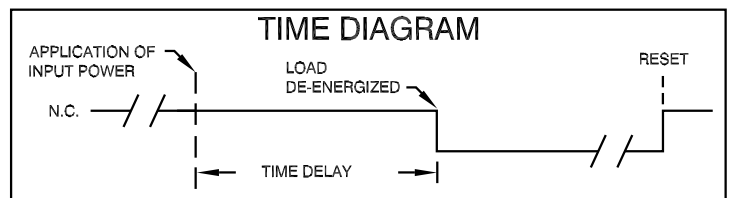
SINGLE SHOT

Power must be applied to the input at all times prior to and during timing. Upon closure of the initiate switch (momentary or maintained) the output contact transfers and the time delay begins. At the completion of the pre-selected delay period, the output contact reverts to its original position. Removal of input power will reset the control.



DELAY ON MAKE, NORMALLY CLOSED QCS

The output is in a normally closed state. Upon application of power to the input terminals, the output contact transfers and the time delay begins. At the completion of the time delay the output contact drops out. Removal of input power from terminal 3 resets the delay and the output contact reverts to its original closed position.

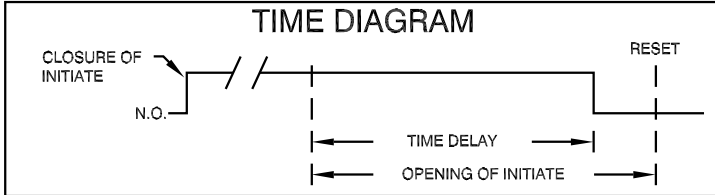


DELAY ON BREAK

QBS, QTS*

Power must be applied to the input at all times prior to and during timing. Upon closure of the initiate switch, the output contact transfers and remains transferred if no further action is taken. When the initiate switch is opened, the time delay begins. At the completion of the pre-selected delay period the output contact reverts to its original position. Closure of initiate during timing will reset the delay period. Removal of input power will reset the control.

*QTS is the same except it is trailing edge triggered. Load energizes when initiate switch is opened.

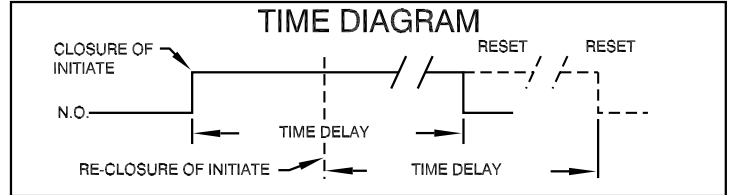


RETRIGGERABLE ONE-SHOT

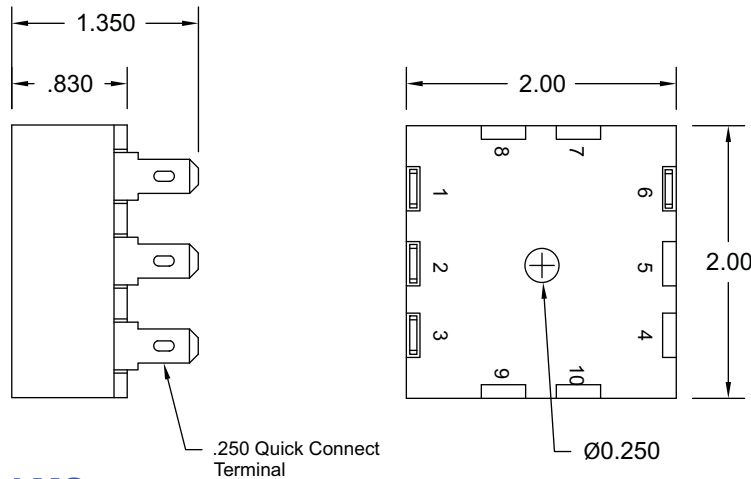
QOS

Power must be applied to the input at all times prior to and during timing. Upon closure of the initiate switch (momentary or maintained) the output contact transfers and the time delay begins. At the completion of the pre-selected delay period, the output contact reverts to its original position.

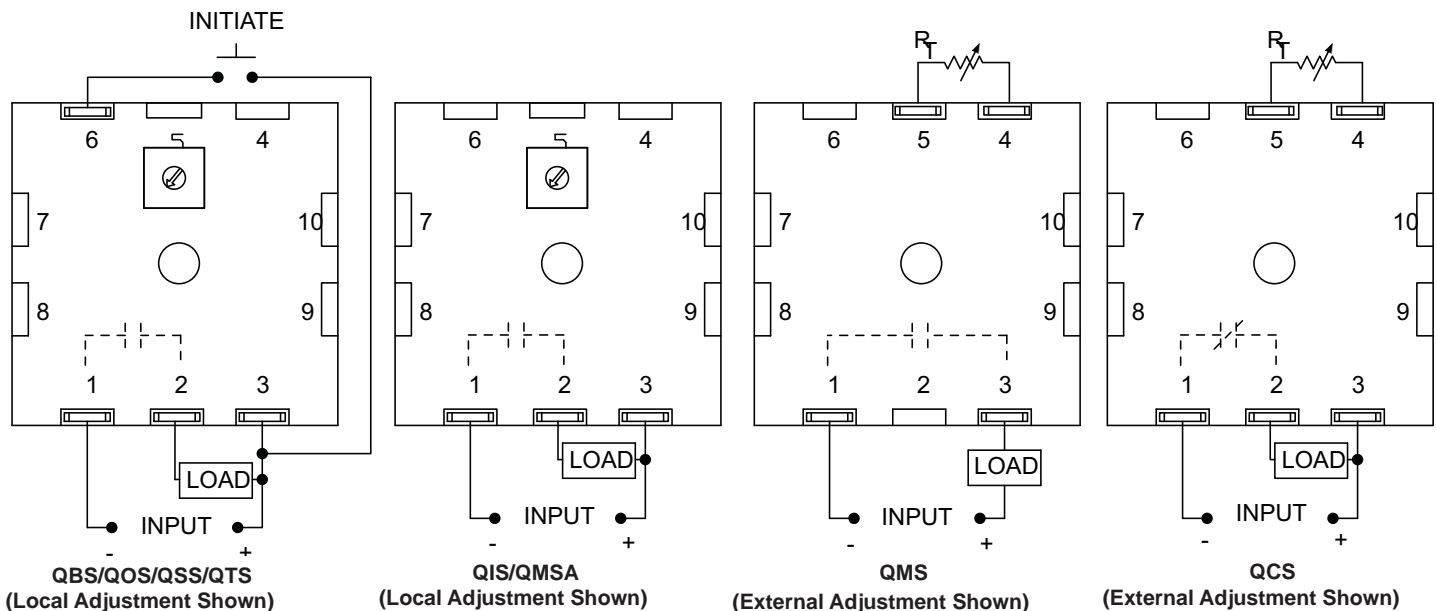
NOTE: Momentary or maintained closure of initiate switch during timing will reset the time delay.



DIMENSIONS



CONNECTION DIAGRAMS



ORDERING INFORMATION

SERIES	INPUT VOLTAGE	ADJUSTMENT	TIME DELAY RANGE
QBS	1 - 12 VDC	0 - Local Adjust	See Time Delay Range Chart
QCS	2 - 24/28 VDC	1 - Fixed	
QIS	3 - 5 VDC	2 - External Adjust	
QMS	4 - 24 VAC		
QMSA	5 - 120 VAC		
QOS	6 - 230 VAC		
QSS	7 - 120/230 VAC		
QTS	9 - 36 VDC		