Raspberry Pi Pico

As soon as the RP2040 has booted, PICO_POWER_EN must be pulled high to maintain power supply to the board.

Ensure copper clearance for antenna.

3V3 LDO / “always on” supply / VSYS EN

Provides the “always on” V+ rail (between 1.8V and 3.3V) needed by the system when in low power (shutdown) mode. Nothing draws much power from this rail to ensure that modules are using minimal battery power when idle.

The regulator is supplied by VSYS when VBUS is present, or directly from VBAT otherwise.

Using the built in 3V3_EN signal on the Pico draws too much power in sleep mode so we have our own cut off for VSYS which can completely disconnect the Pico.

Wakeup events

To conserve power the module spends most of its time sleeping. An on-board RTC will periodically wake the module to take a reading.

The module will also wake when USB power is applied, user presses the STATUS switch, or when an external trigger fires.

All wake triggers are active high apart from the RTC alarm.

RTC

PCF85063A

I2C: 0x51

1.8-5.5V

VDD CLKO INT SDA SCL OSCO GND OSCI

QW/ST / Battery connectors

Battery input voltage between 2V and 5.5V - i.e. a protected LiPo cell or 2/3xAAA battery holder. Battery connector mounting pads connected to ground plane (no thermals) for mechanical strength.

User interface

Using the built in 3V3_EN signal on the Pico draws too much power in sleep mode so we have our own cut off for VSYS which can completely disconnect the Pico.
**BME688 enviromental sensor**

- CSB (chip select) tied to 1V8 to put into I2C mode.
- SDO tied to 1V8 selects I2C address 0x77.
- BME688 decoupled with C10

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**BH1745 Colour and Light Sensor**

- 2.3-3.6V
- VCC, SDA, SCL, INT, GND, ADDR
- 16-bit 144-channel ADC
- 10-bits 12-channel ADC