First Power On

Here is what should be happening when you first power on the ShastaPlus FPGA board.

Three LEDs should be lite.

Green LED: indicates 3.3 volts.
Blue LED: indicates the ARM processor is up and running.
The Blue LED will toggle with every USB command.
Yellow LED: should be blinking at a fast rate.

The yellow LED is connected to the FPGA as WF_LED. The FPGA image shipped is WF_blink_with_switch. If the LED is blinking, then FPGA is up and running.

Pushing the user white button will change the rate of blinking. This button is connected to the FPGA as WF_BUTTON.

The RED button is the reset for the ARM processor. Pushing this button will reset the ARM processor, which in turn will reload the FPGA. Also the USB interface will be reset, and an USB re-enumeration will occur.