



Solder shorting block to disable clk source.

FPGA switch and LEDs

J1 Boot0
boot0 = 0, boot from flash
boot0 = 1, bootloader (shorted)

Board revision {PA0,PB1} 00 = revA

U2D

I0B_0a	46
I0B_2a	47
I0B_3b_G6	44
I0B_4a	48
I0B_5b	45
I0B_6a	2
I0B_8a	4
I0B_9b	3
VCCIO_2	1

U2B

IOT_36b	25
IOT_37a	23
IOT_38b	27
IOT_39a	26
IOT_41a	28
IOT_42b	31
IOT_43a	32
IOT_44b	34
IOT_45a_G1	37
IOT_46b_G0	35
IOT_48b	36
IOT_49a	43
IOT_50b	38
IOT_51a	42
RGB0	41
RGB1	40
RGB2	39
VCCIO_0	33

U2C
ICE40UP5K

CDONE	7
CRESET	8
I0B_13b	6
I0B_16a	9
I0B_18a	10
I0B_20a	11
I0B_22a	12
I0B_23b	21
I0B_24a	13
I0B_25b_G3	19
I0B_29b	20
I0B_31b	18
SPI_MOSI	14
SPI_MISO	17
SPI_CLK	15
SPI_CS	16
VCC	22

J11

VCC_33	1
SPI_MISO	3
CDONE	4
CRESET	5
SPI_CS	6
SPI_MOSI	7
IOT_37a	8
I0B_25b_G3	9
I0B_29b	10
I0B_31b	12
I0B_23b	13
IOT_36b	14
IOT_39a	15
IOT_41a	16
IOT_38b	17
IOT_44b	18
IOT_46b_G0	19
IOT_48b	20

J12

VCC_5	1
SPI_CLK	2
IOT_50b	3
RGB2	4
IOT_49a	5
I0B_13b	6
I0B_16a	7
I0B_8a	8
I0B_9b	9
I0B_6a	10
I0B_4a	11
I0B_5b	12
I0B_2a	13
I0B_0a	14
RGB1	15
I0B_3b_G6	16
RGB1	17
I0B_4b	18
NEO_OUT	19
NEO_OUT	20