

## Quick Start Guide

1. Connect the FPGA device to your computer using a USB-A to USB-Micro cable.
  - a) If you are running Linux, please save this udev rule (<https://webfpga.io/60-webfpga.rules>) and reload your udev configuration (or reboot).
3. Navigate to <https://webfpga.io> using Google Chrome or Opera.
4. Click "Connect Device" on the left-side toolbar and select "Cascadia WebUSB FPGA Programmer" from the pop-up menu.
5. Click "Run Synthesis" and wait for synthesis to complete. The console should output "Bitstream loaded in browser."
6. Click "Flash Device" and wait for flashing to complete.

## Device Control

The red push button resets the device.

The white push button is user-readable. In your Verilog code, use an input wire named `WF_BUTTON` to read its state.

## More Tutorials and Information

Visit <https://blog.webfpga.io> for more in-depth tutorials and write-ups, including video and documented source code.

If you run into any issues, please visit our support forum at <https://forum.webfpga.io>.