

Key

- Ground
- 3.3v output 100ma Max
- 5v output 200ma Max when USB attached
- pin # User FPGA pin
- pin # Shared User FPGA pins with Flash
- FPGA control signals
- WebFPGA dedicated pins to on board resources
- 16MHz Clock OSC output/User Pin*
- pin # FPGA RGB open drain/User Pin**
- Neopixel Data Out

* Pin is used for clock input on Global signal G0.
Clock OSC output can be disabled by shorting J2, then the FPGA pin can be used as an user defined output or input.

** FPGA RGB pins are open drain, to use as an output to be driven high, need to use the SB_OD macro and a pullup.

Dedicated on board connections

- 31 — WF_LED, yellow LED, active low.
- 32 — WF_NEO, Addressable RGB LED.
- 36 — WF_BUTTON, push button, active low when pushed.
- 10 — WF_CPU1, MCU connection, PB1 100K pulldown.
- 11 — WF_CPU2, MCU connection, PA0 100K pulldown.
- 12 — WF_CPU3, MCU connection, PA2.
- 13 — WF_CPU4, MCU connection, PA3.

