

K-Frame with Thermal-Magnetic Trip Unit Technology

Table 4. Thermal-Magnetic Breaker/Frame

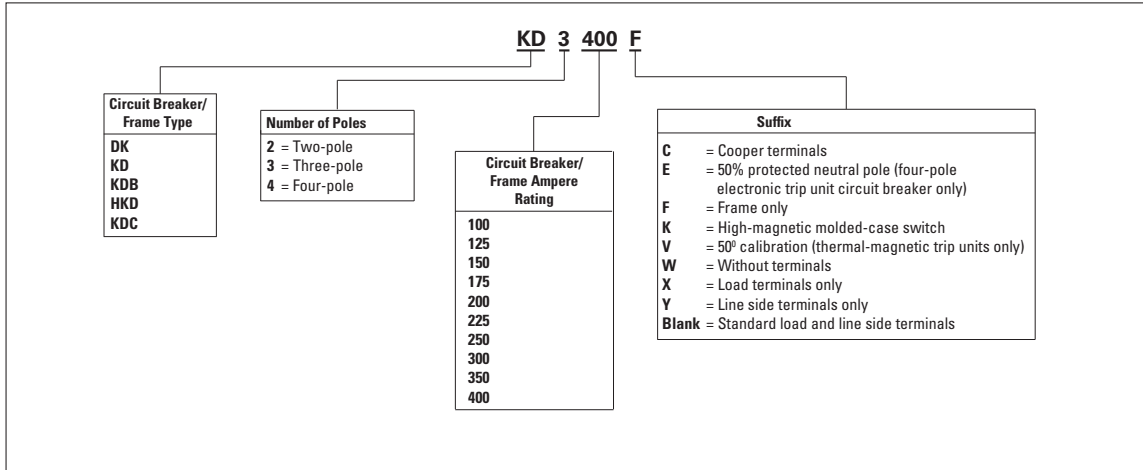
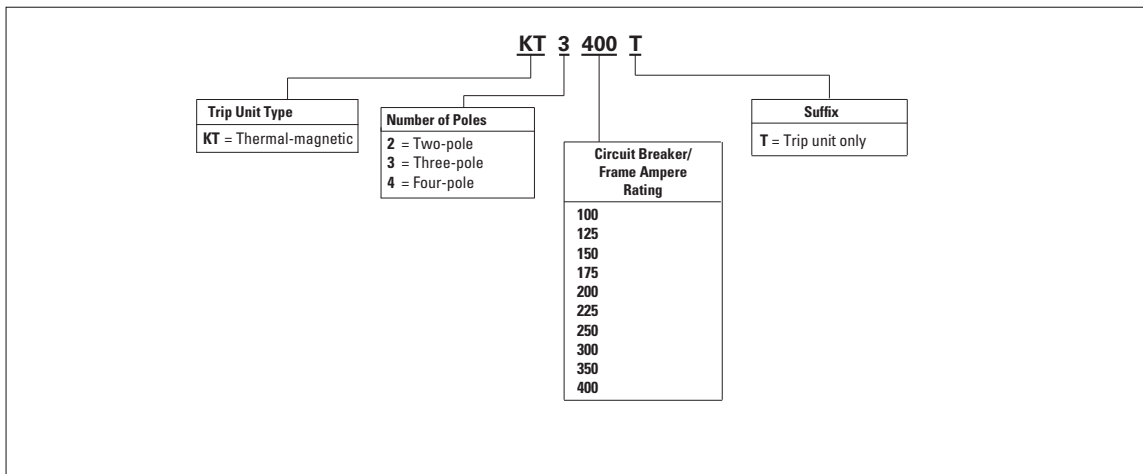
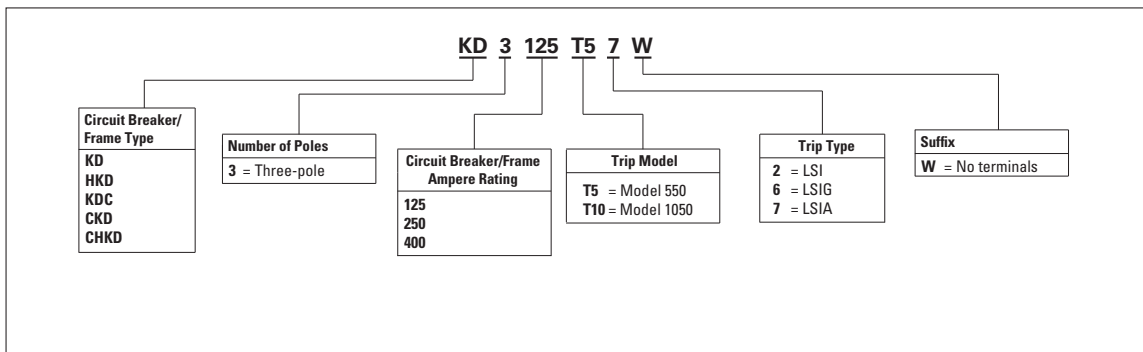


Table 5. Thermal-Magnetic Trip Unit



K-Frame with OPTIM 550/1050 Trip Unit Technology

Table 6. OPTIM 550/1050 Circuit Breaker/Frame



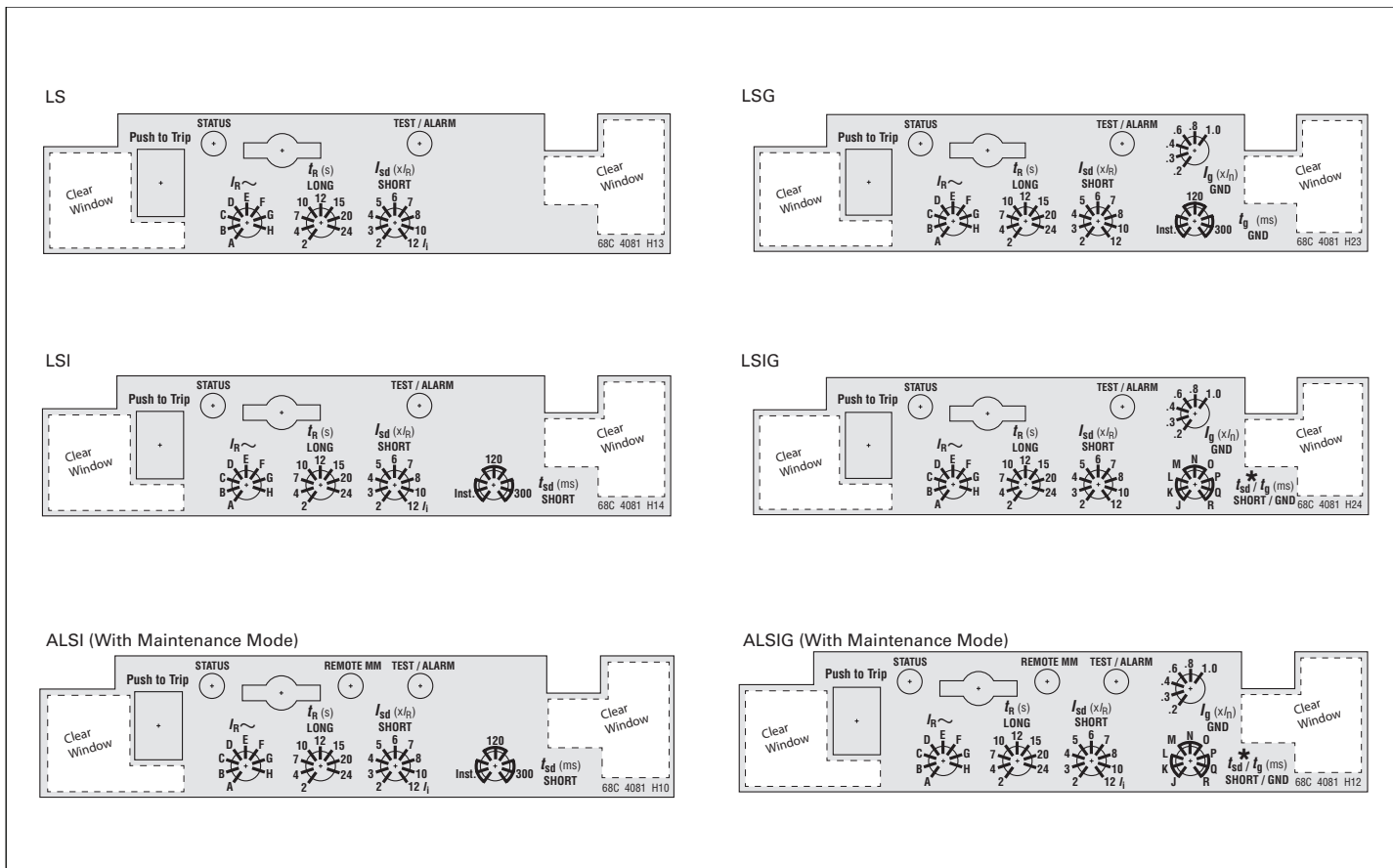


Figure 1. Digitrip 310+ Namplates

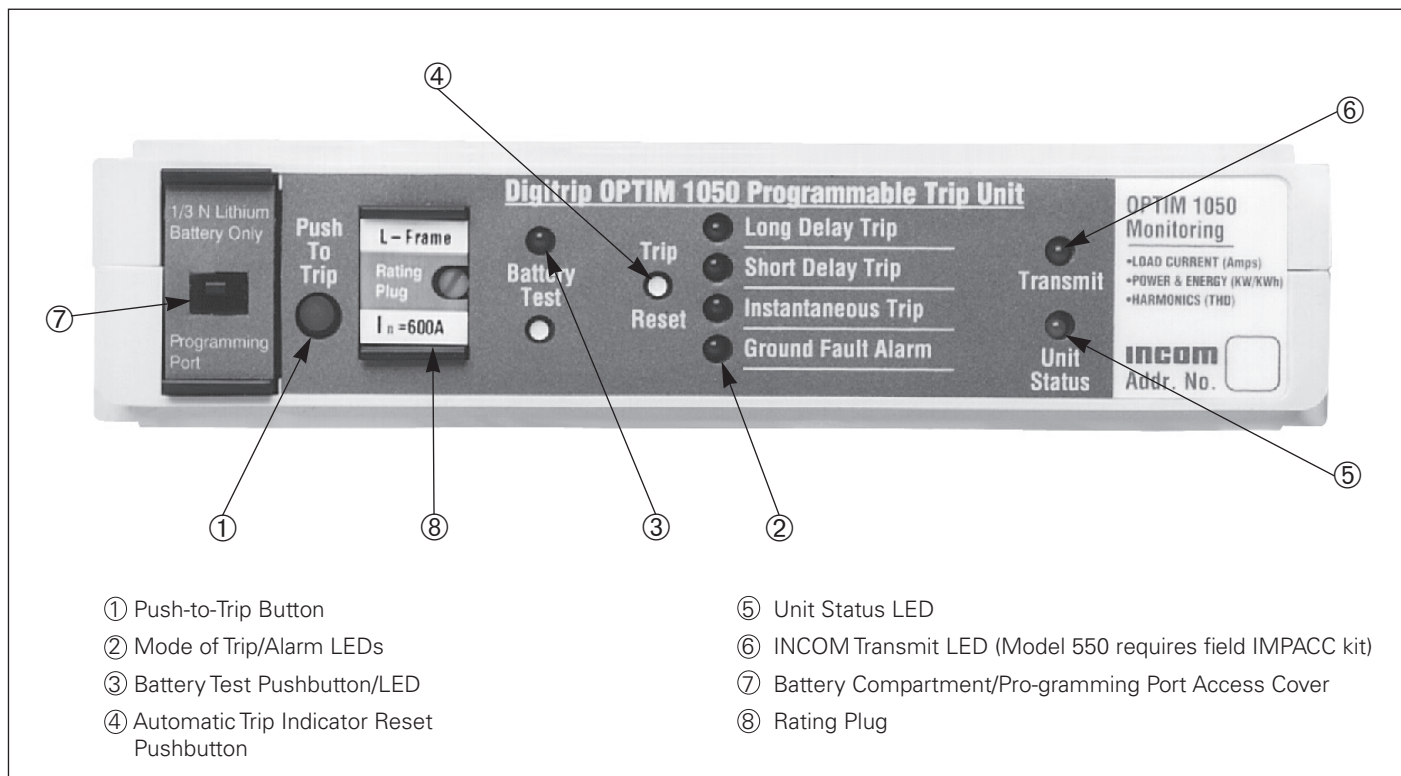


Figure 2. Front View of L-Frame Type OPTIM Trip Unit (K and N-Frame Designs are Similar)

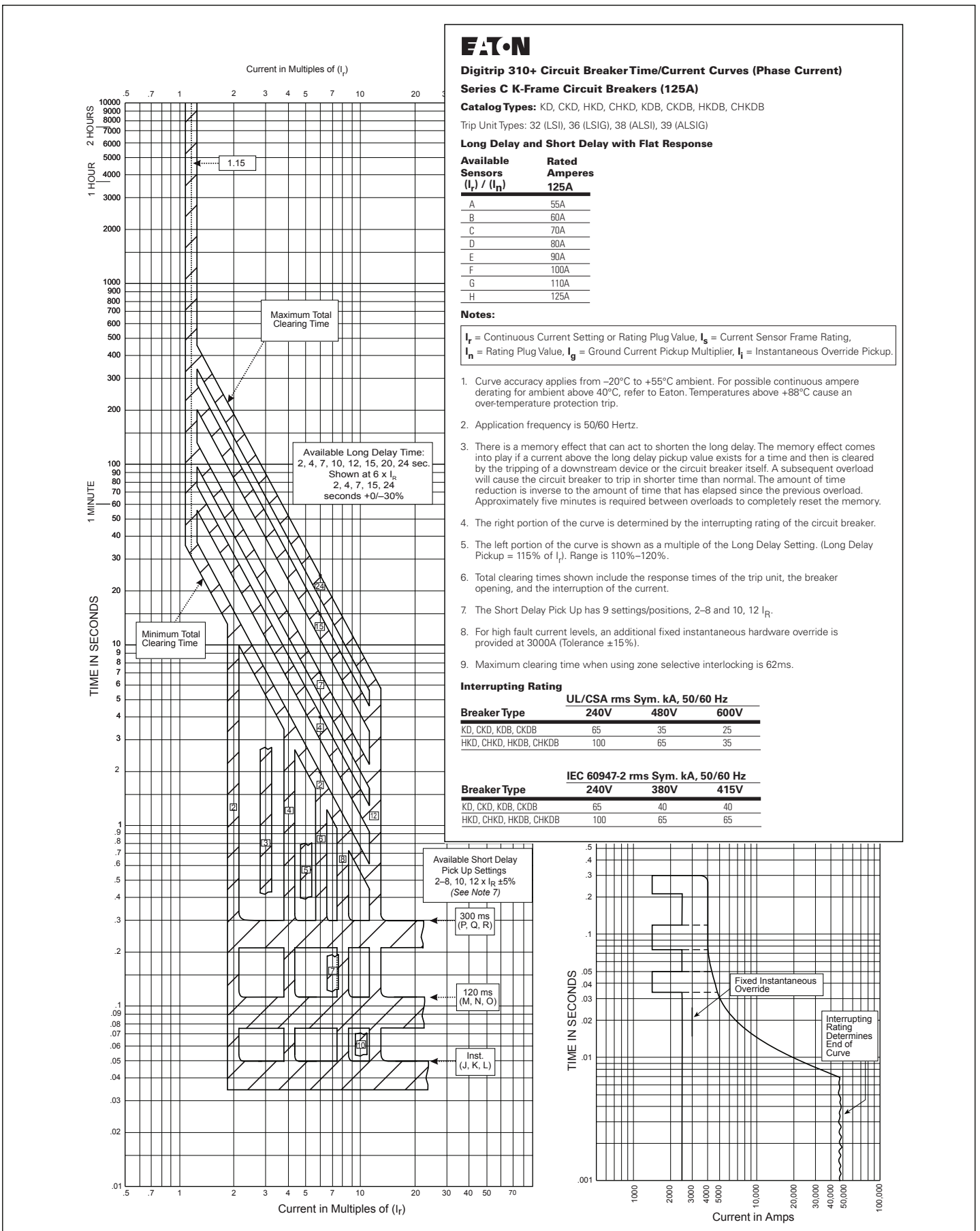


Figure 3. Digitrip 310+ Trip Units (125A), Long Delay and Short Delay with Flat Response and Override (LSI, LSIG, ALSI, ALSIG) – Curve Number TD012014EN, September 2015

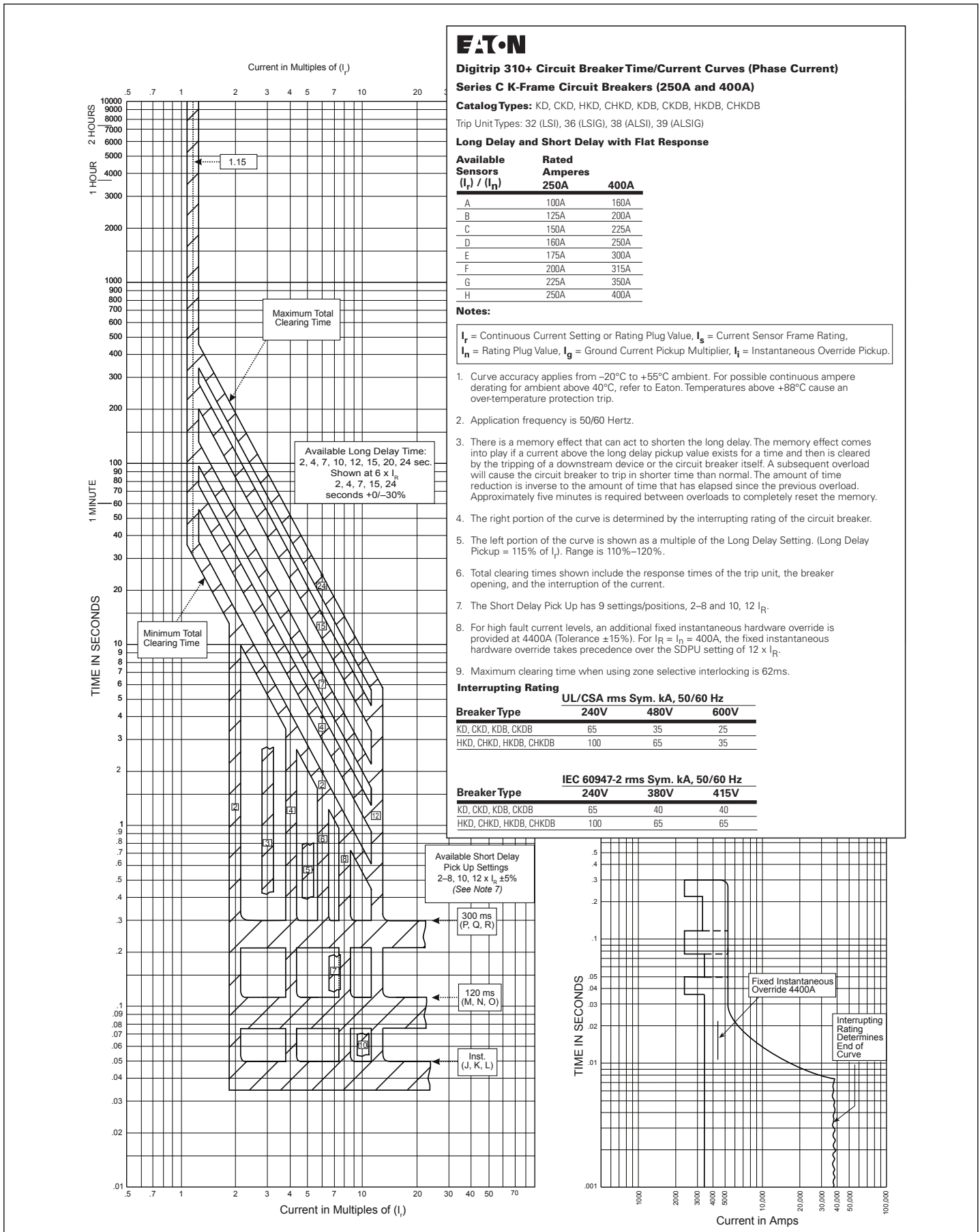


Figure 4. Digitrip 310+ Trip Units (250A and 400A), Long Delay and Short Delay with Flat Response and Override (LSI, LSIG, ALSI, ALSIG) - Curve Number TD012007EN, September 2015

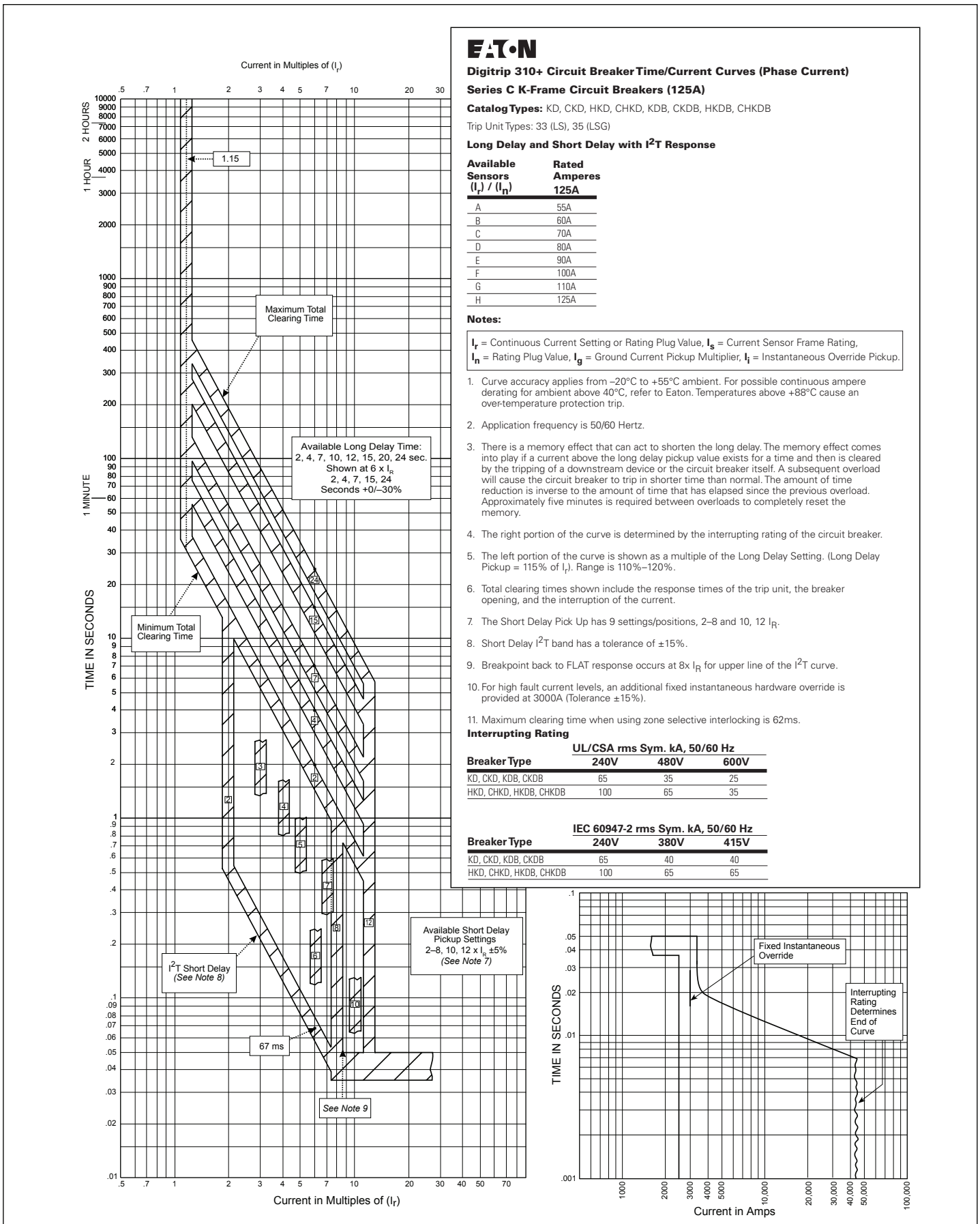


Figure 5. Digitrip 310+ Trip Units (125A), Long Delay Response and Short Delay with I²T Response Curve and Override (LS, LSG) - TD012015EN, September 2015

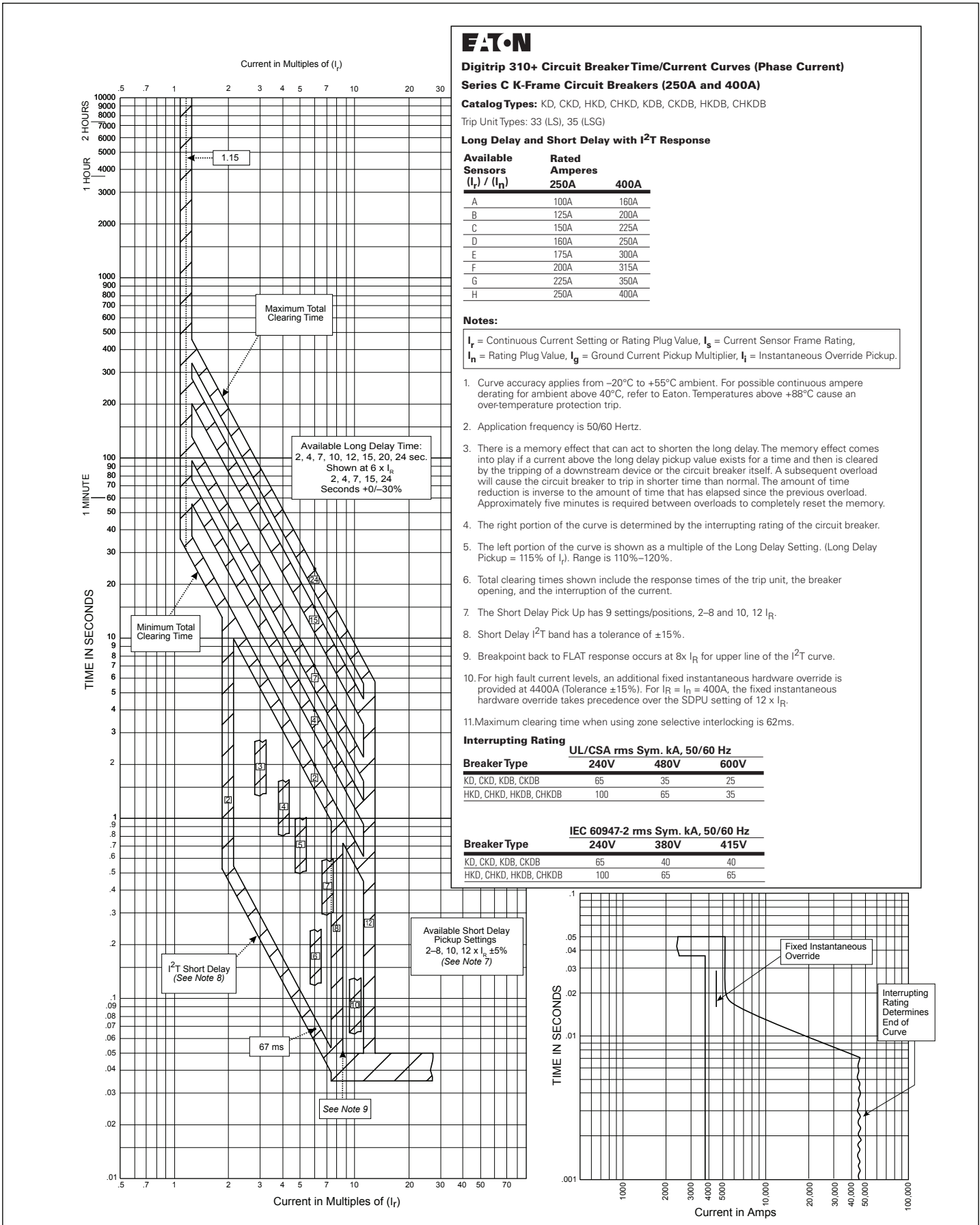
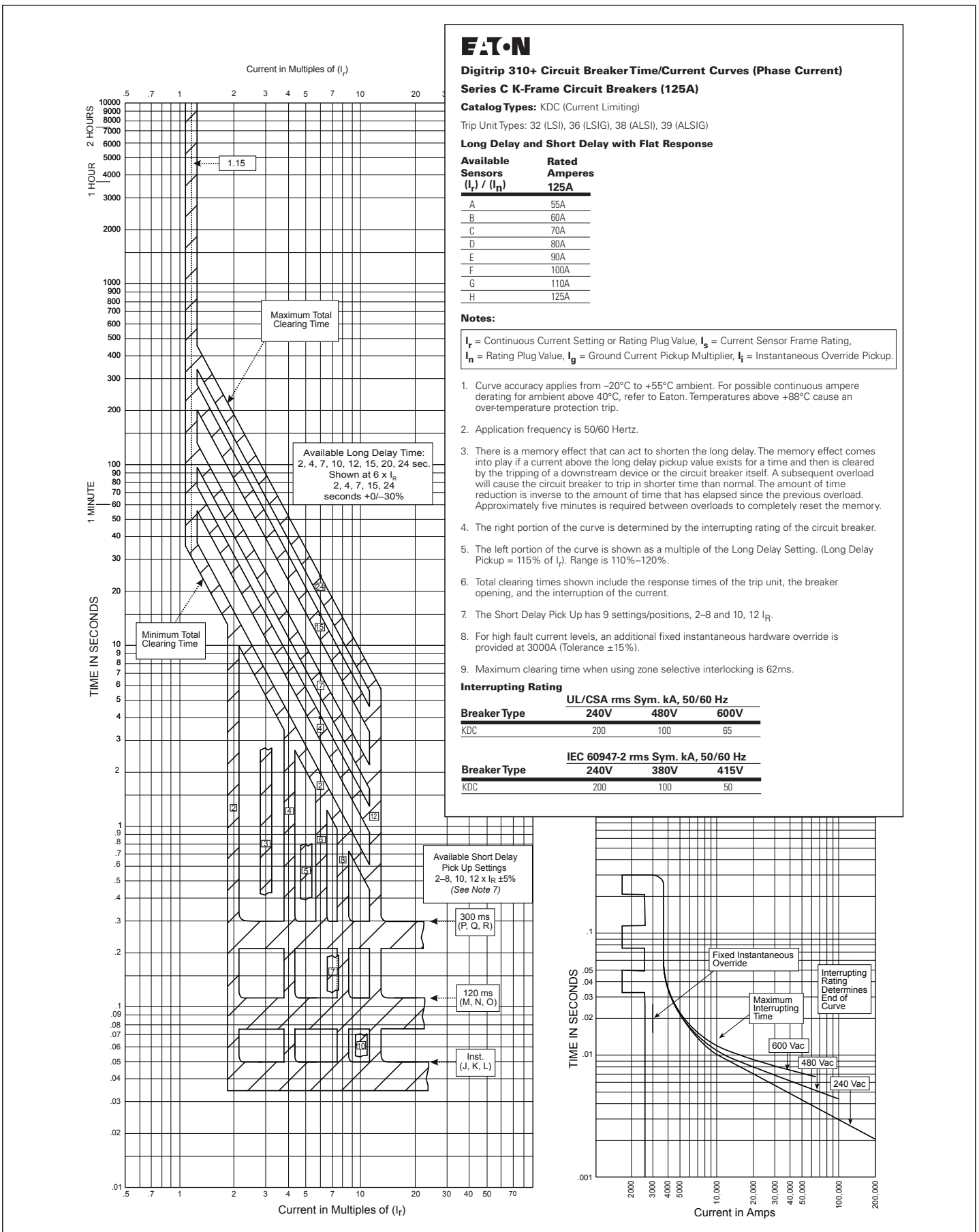
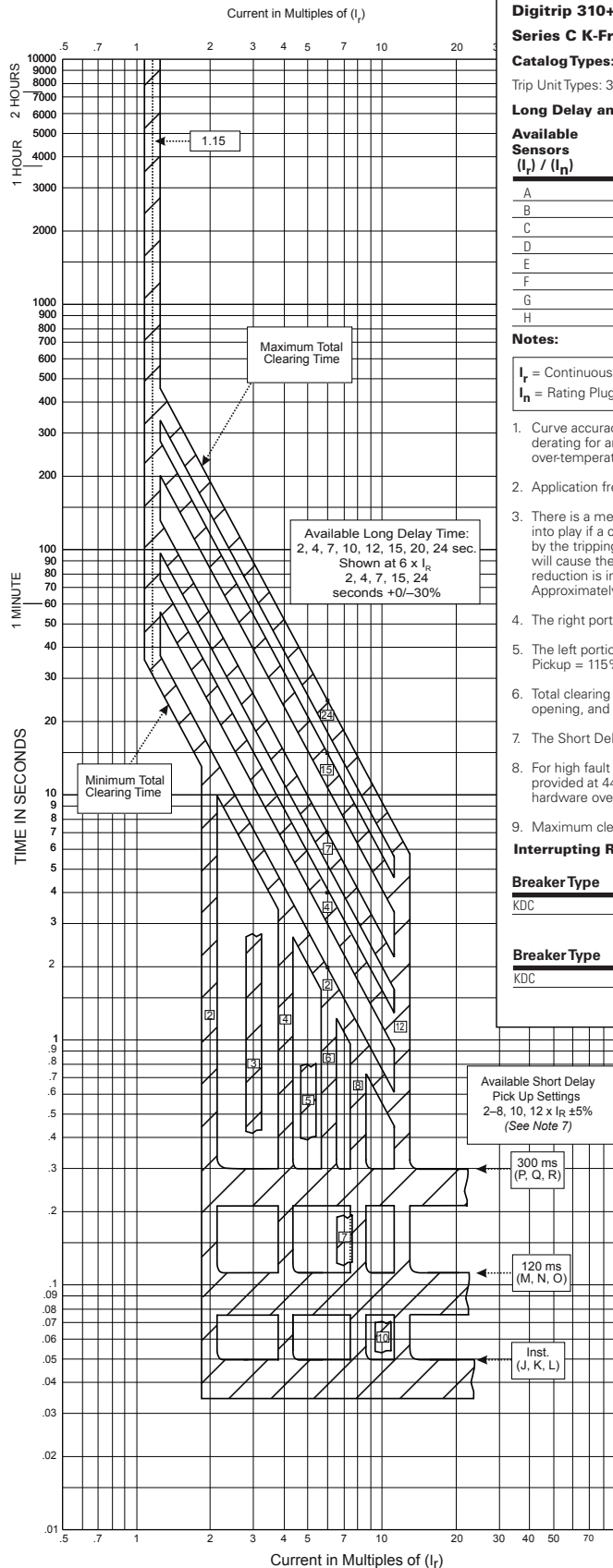


Figure 6. Digitrip 310+ Trip Units (250A and 400A), Long Delay Response and Short Delay with I²T Response Curve and Override (LS, LSG) – Curve Number TD012008EN, September 2015





Digitrip 310+ Circuit Breaker Time/Current Curves (Phase Current)
Series C K-Frame Circuit Breakers (250A and 400A)

Catalog Types: KDC (Current Limiting)

Trip Unit Types: 32 (LSI), 36 (LSIG), 38 (ALSI), 39 (ALSIG)

Long Delay and Short Delay with Flat Response

Available Sensors (I _r) / (I _n)	Rated Amperes 250A	400A
A	100A	160A
B	125A	200A
C	150A	225A
D	160A	250A
E	175A	300A
F	200A	315A
G	225A	350A
H	250A	400A

Notes:

I_r = Continuous Current Setting or Rating Plug Value, I_g = Current Sensor Frame Rating,
I_n = Rating Plug Value, I_g = Ground Current Pickup Multiplier, I_i = Instantaneous Override Pickup.

- Curve accuracy applies from -20°C to +55°C ambient. For possible continuous ampere derating for ambient above 40°C, refer to Eaton. Temperatures above +88°C cause an over-temperature protection trip.
- Application frequency is 50/60 Hertz.
- There is a memory effect that can act to shorten the long delay. The memory effect comes into play if a current above the long delay pickup value exists for a time and then is cleared by the tripping of a downstream device or the circuit breaker itself. A subsequent overload will cause the circuit breaker to trip in shorter time than normal. The amount of time reduction is inverse to the amount of time that has elapsed since the previous overload. Approximately five minutes is required between overloads to completely reset the memory.
- The right portion of the curve is determined by the interrupting rating of the circuit breaker.
- The left portion of the curve is shown as a multiple of the Long Delay Setting. (Long Delay Pickup = 115% of I_r). Range is 110%–120%.
- Total clearing times shown include the response times of the trip unit, the breaker opening, and the interruption of the current.
- The Short Delay Pick Up has 9 settings/positions, 2–8 and 10, 12 I_r.
- For high fault current levels, an additional fixed instantaneous hardware override is provided at 4400A (Tolerance ±15%). For I_r = I_n = 400A, the fixed instantaneous hardware override takes precedence over the SDPU setting of 12 x I_r.
- Maximum clearing time when using zone selective interlocking is 62ms.

Interrupting Rating

Breaker Type	UL/CSA rms Sym. kA, 50/60 Hz		
	240V	480V	600V
KDC	200	100	65

Breaker Type	IEC 60947-2 rms Sym. kA, 50/60 Hz		
	240V	380V	415V
KDC	200	100	50

Available Short Delay Pick Up Settings
2–8, 10, 12 x I_r ±5%
(See Note 7)

300 ms (P, Q, R)

120 ms (M, N, O)

Inst. (J, K, L)

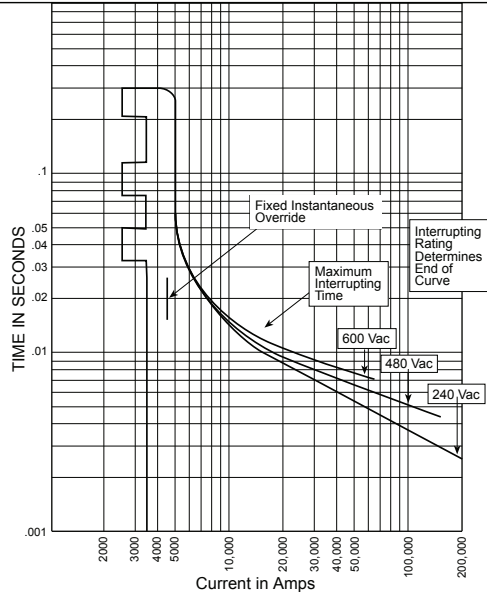
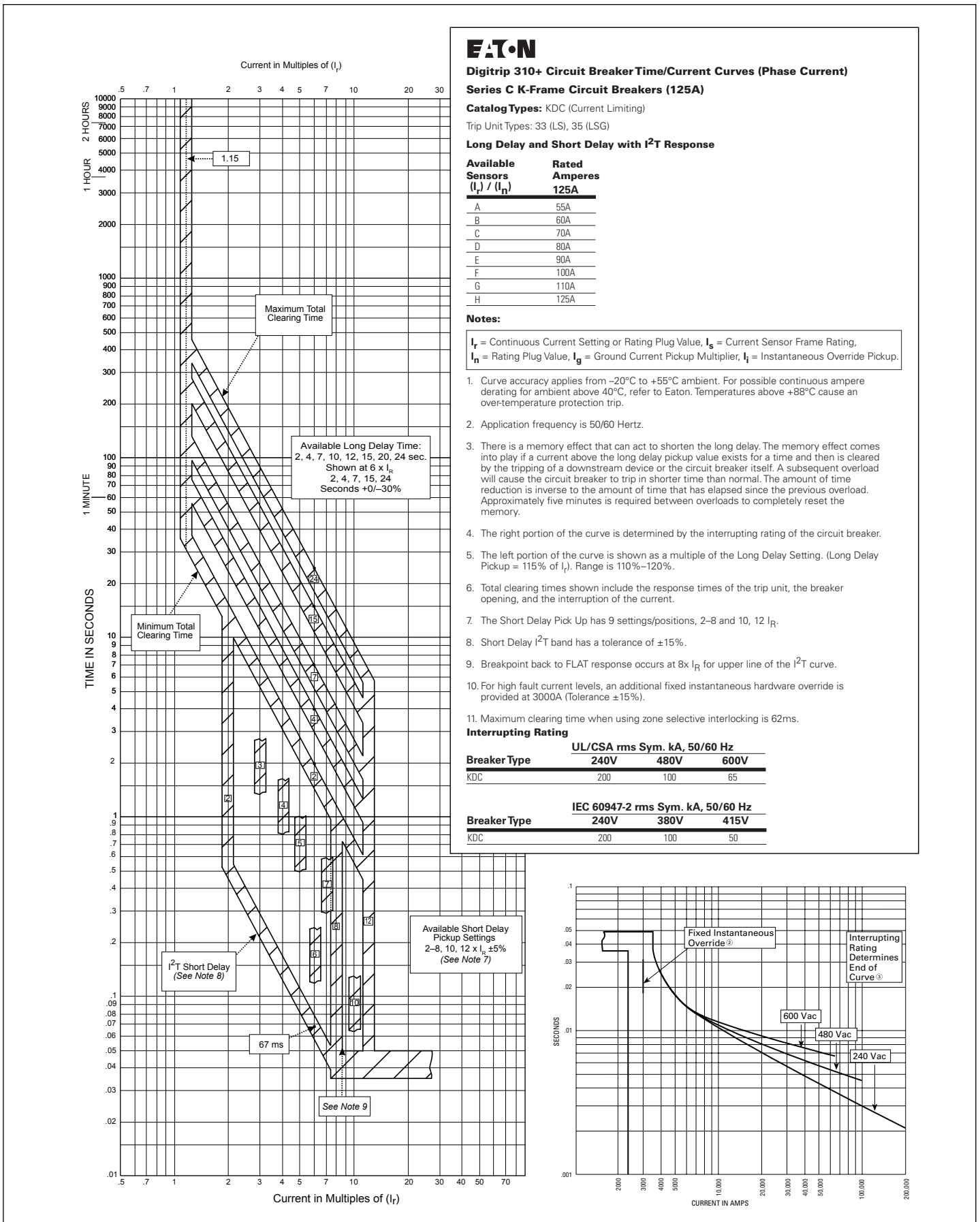


Figure 4. Digitrip 310+ Trip Units (250A and 400A), Long Delay and Short Delay with Flat Response and Override (LSI, LSIG, ALSI, ALSIG) - Curve Number TD012012EN, September 2015



Digitrip 310+ Circuit Breaker Time/Current Curves (Phase Current)
Series C K-Frame Circuit Breakers (125A)

Catalog Types: KDC (Current Limiting)

Trip Unit Types: 33 (LS), 35 (LSG)

Long Delay and Short Delay with I²T Response

Available Sensors (I _r / I _n)	Rated Amperes
A	55A
B	60A
C	70A
D	80A
E	90A
F	100A
G	110A
H	125A

Notes:

I_r = Continuous Current Setting or Rating Plug Value, I_s = Current Sensor Frame Rating, I_n = Rating Plug Value, I_g = Ground Current Pickup Multiplier, I_i = Instantaneous Override Pickup.

- Curve accuracy applies from -20°C to +55°C ambient. For possible continuous ampere derating for ambient above 40°C, refer to Eaton. Temperatures above +88°C cause an over-temperature protection trip.
- Application frequency is 50/60 Hertz.
- There is a memory effect that can act to shorten the long delay. The memory effect comes into play if a current above the long delay pickup value exists for a time and then is cleared by the tripping of a downstream device or the circuit breaker itself. A subsequent overload will cause the circuit breaker to trip in shorter time than normal. The amount of time reduction is inverse to the amount of time that has elapsed since the previous overload. Approximately five minutes is required between overloads to completely reset the memory.
- The right portion of the curve is determined by the interrupting rating of the circuit breaker.
- The left portion of the curve is shown as a multiple of the Long Delay Setting. (Long Delay Pickup = 115% of I_r). Range is 110%–120%.
- Total clearing times shown include the response times of the trip unit, the breaker opening, and the interruption of the current.
- The Short Delay Pick Up has 9 settings/positions, 2–8 and 10, 12 I_r.
- Short Delay I²T band has a tolerance of ±15%.
- Breakpoint back to FLAT response occurs at 8x I_R for upper line of the I²T curve.
- For high fault current levels, an additional fixed instantaneous hardware override is provided at 3000A (Tolerance ±15%).
- Maximum clearing time when using zone selective interlocking is 62ms.

Interrupting Rating

Breaker Type	UL/CSA rms Sym. kA, 50/60 Hz		
	240V	480V	600V
KDC	200	100	65

Breaker Type	IEC 60947-2 rms Sym. kA, 50/60 Hz		
	240V	380V	415V
KDC	200	100	50

Figure 5. Digitrip 310+ Trip Units (125A), Long Delay Response and Short Delay with I²T Response Curve and Override (LS, LSG) - TD012017EN, September 2015



Digitrip 310+ Circuit Breaker Time/Current Curves (Phase Current)
Series C K-Frame Circuit Breakers (250A and 400A)

Catalog Types: KDC (Current Limiting)

Trip Unit Types: 33 (LS), 35 (LSG)

Long Delay and Short Delay with I²T Response

Available Sensors (I _r / I _n)	Rated Amperes	
	250A	400A
A	100A	160A
B	125A	200A
C	150A	225A
D	160A	250A
E	175A	300A
F	200A	315A
G	225A	350A
H	250A	400A

Notes:

I_r = Continuous Current Setting or Rating Plug Value, I_s = Current Sensor Frame Rating, I_n = Rating Plug Value, I_g = Ground Current Pickup Multiplier, I_i = Instantaneous Override Pickup.

- Curve accuracy applies from -20°C to +55°C ambient. For possible continuous ampere derating for ambient above 40°C, refer to Eaton. Temperatures above +88°C cause an over-temperature protection trip.
- Application frequency is 50/60 Hertz.
- There is a memory effect that can act to shorten the long delay. The memory effect comes into play if a current above the long delay pickup value exists for a time and then is cleared by the tripping of a downstream device or the circuit breaker itself. A subsequent overload will cause the circuit breaker to trip in shorter time than normal. The amount of time reduction is inverse to the amount of time that has elapsed since the previous overload. Approximately five minutes is required between overloads to completely reset the memory.
- The right portion of the curve is determined by the interrupting rating of the circuit breaker.
- The left portion of the curve is shown as a multiple of the Long Delay Setting. (Long Delay Pickup = 115% of I_r). Range is 110%–120%.
- Total clearing times shown include the response times of the trip unit, the breaker opening, and the interruption of the current.
- The Short Delay Pick Up has 9 settings/positions, 2–8 and 10, 12 I_R.
- Short Delay I²T band has a tolerance of ±15%.
- Breakpoint back to FLAT response occurs at 8x I_R for upper line of the I²T curve.
- For high fault current levels, an additional fixed instantaneous hardware override is provided at 4400A (Tolerance ±15%). For I_R = I_n = 400A, the fixed instantaneous hardware override takes precedence over the SDPU setting of 12 x I_R.
- Maximum clearing time when using zone selective interlocking is 62ms.

Interrupting Rating

Breaker Type	UL/CSA rms Sym. kA, 50/60 Hz		
	240V	480V	600V
KDC	200	100	65

Breaker Type	IEC 60947-2 rms Sym. kA, 50/60 Hz		
	240V	380V	415V
KDC	200	100	50

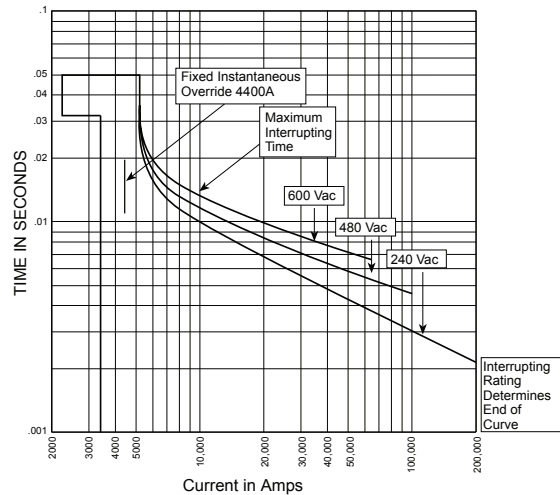
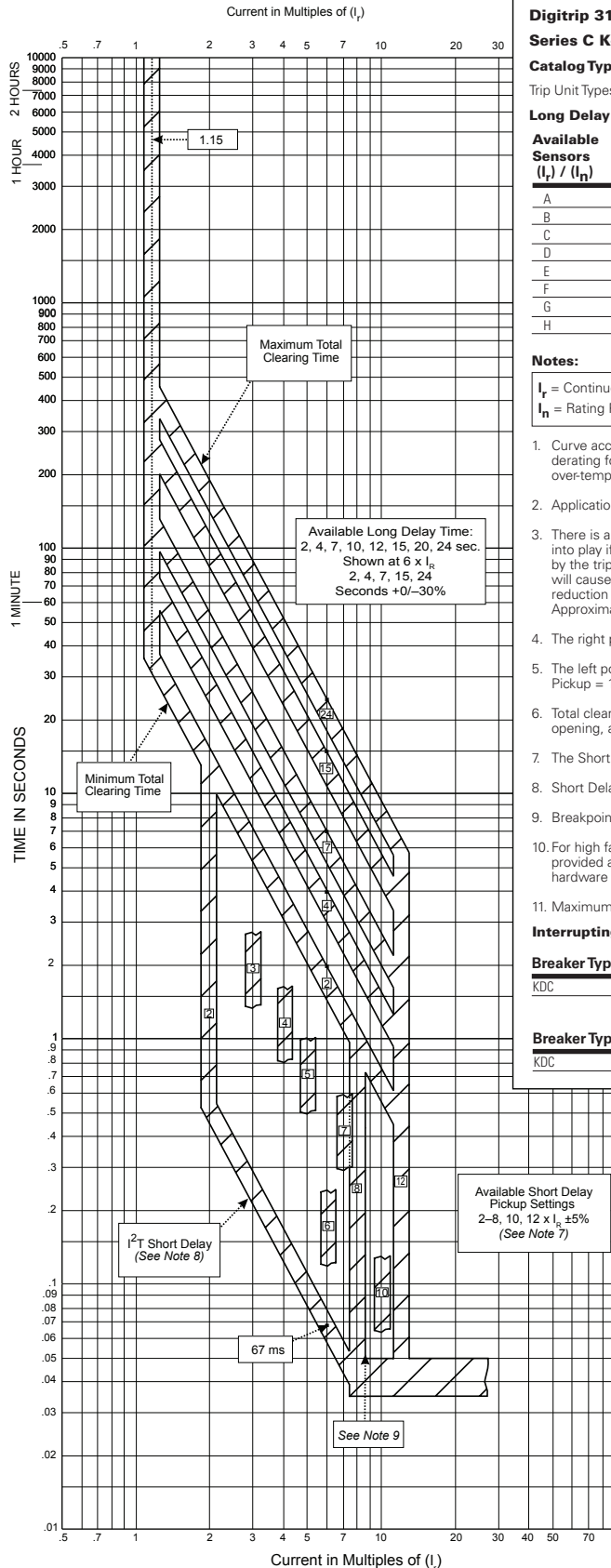


Figure 6. Digitrip 310+ Trip Units (250A and 400A), Long Delay Response and Short Delay with I²T Response Curve and Override (LS, LSG) – Curve Number TD012013EN, September 2015

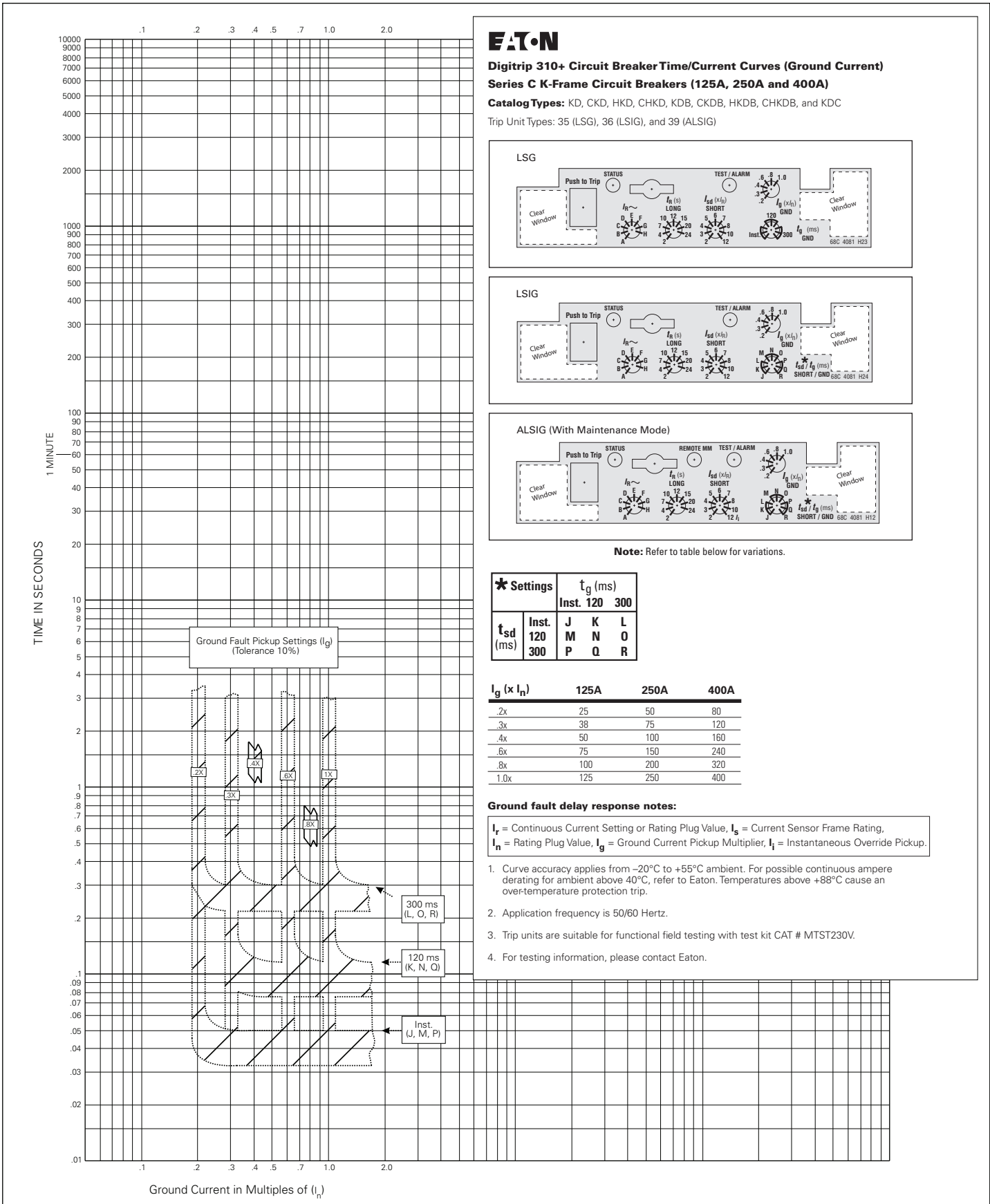
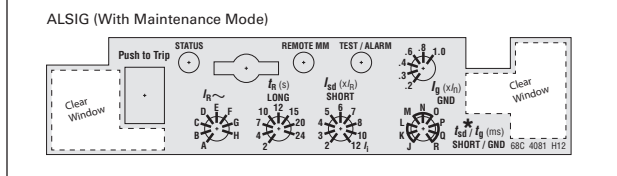
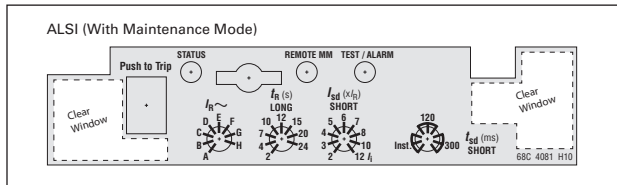


Figure 7. Ground Fault Delay Response Curve (LSG, LSIG, ALSIG) - Curve Number TD012009EN



Digitrip 310+ Circuit Breaker Time/Current Curves
Maintenance Mode Setting
Series C K-Frame Trip Unit Nameplates

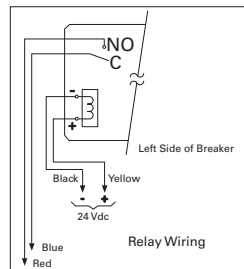


I_r = Continuous Current Setting or Rating Plug Value, I_s = Current Sensor Frame Rating,
 I_n = Rating Plug Value, I_g = Ground Current Pickup Multiplier, I_1 = Instantaneous Override Pickup.

Notes:

1. The Maintenance Mode feature must be ENABLED via application of 24 Vdc for these curves to apply. The blue LED is lit when in Maintenance Mode.
2. The end of the curve is determined by the interrupting rating of the circuit breaker.
3. Total clearing times shown include the response times of the trip unit, the breaker opening, and the interruption of the current.
4. Nominal Values (Pickup) (Tolerance is $\pm 15\%$) $2.5 \times I_r$.
5. The total clearing times shown are conservative and consider the maximum response time of the trip unit, the circuit breaker opening, and the interruption of the current in the worst case conditions such as: maximum rated voltages, single-phase interruption, and minimum power factor. Faster clearing times are possible depending on the specific system conditions.

Contact Eaton for additional information.



Maintenance Mode Trip

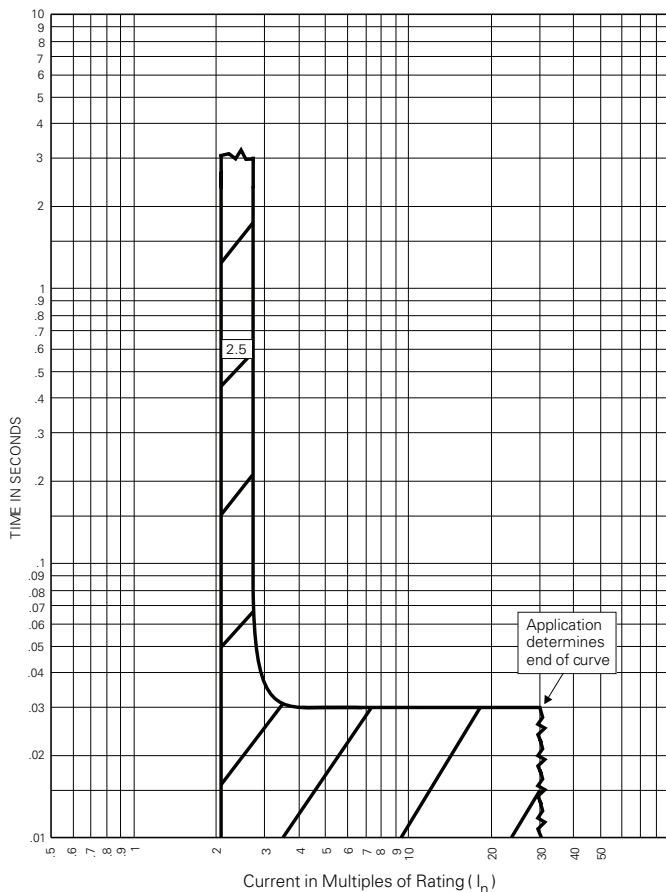


Figure 8. Maintenance Mode Setting (ALSI, ALSIG) - Curve Number TD012010EN, February 2014

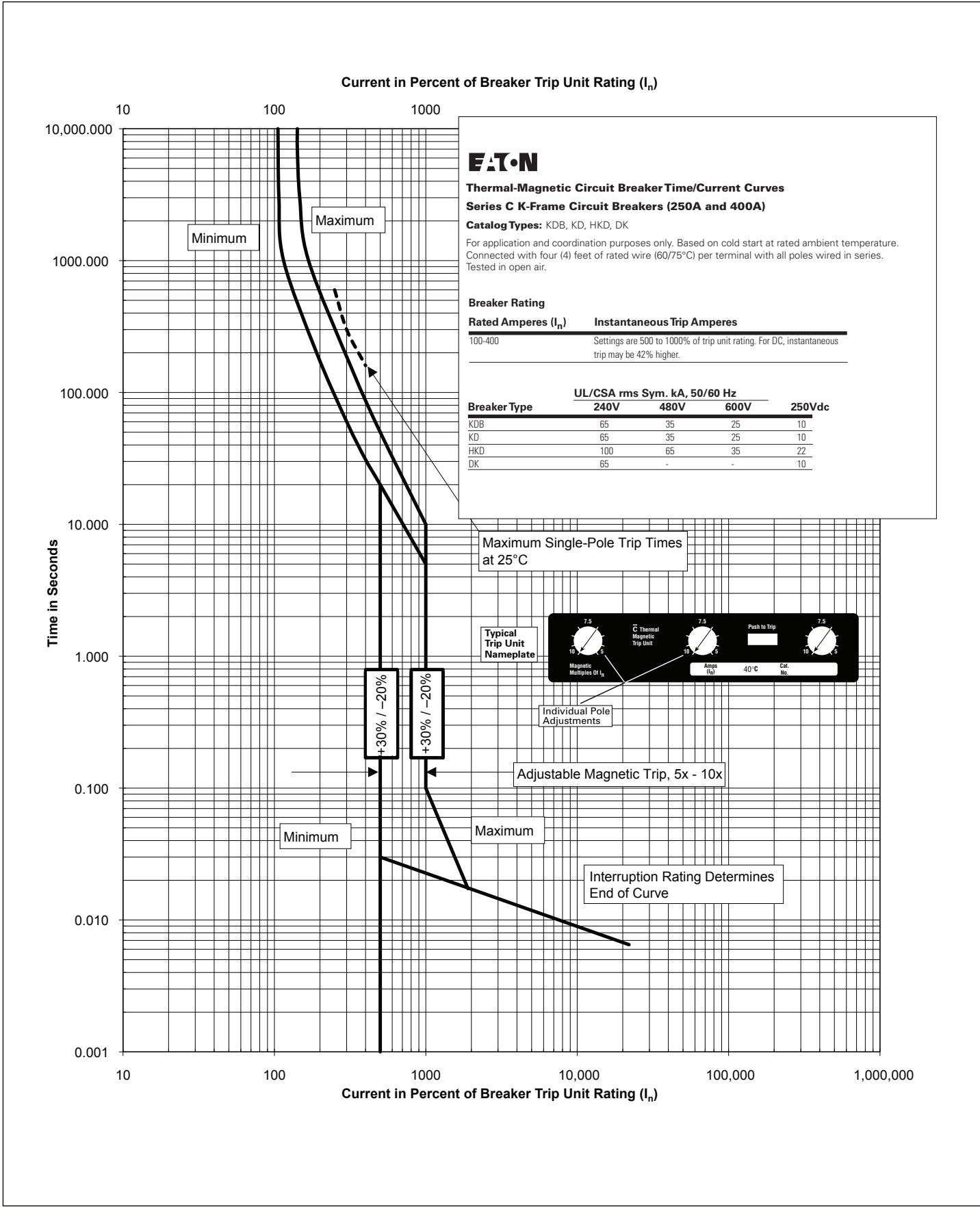


Figure 9. Thermal-Magnetic Series C Types KDB, KD, HKD Circuit Breakers - Curve Number SC-4118-87B, February 2014

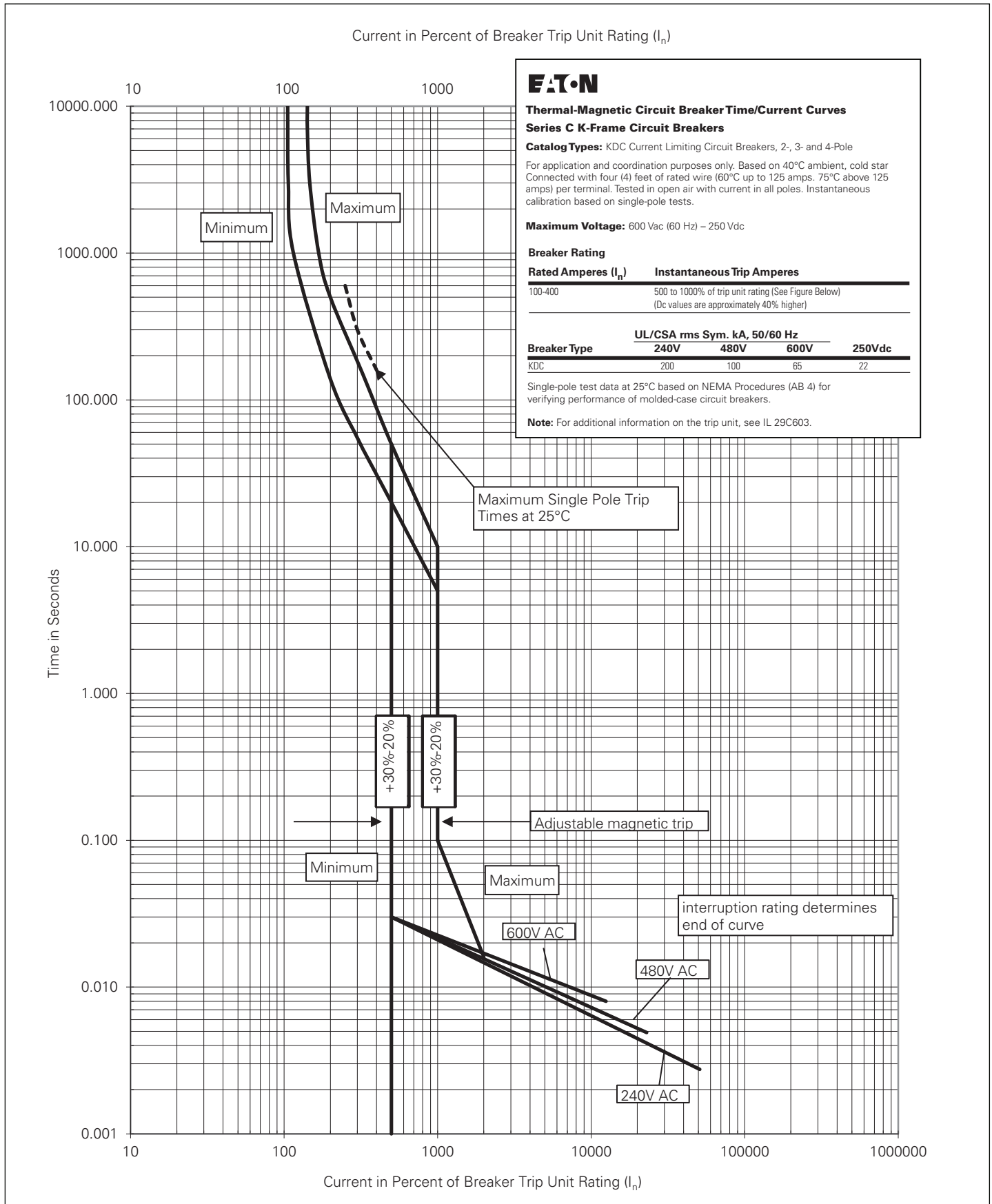


Figure 10. Thermal-Magnetic Series C Type KDC Circuit Breakers - Curve Number SC-4119-87B, February 2014

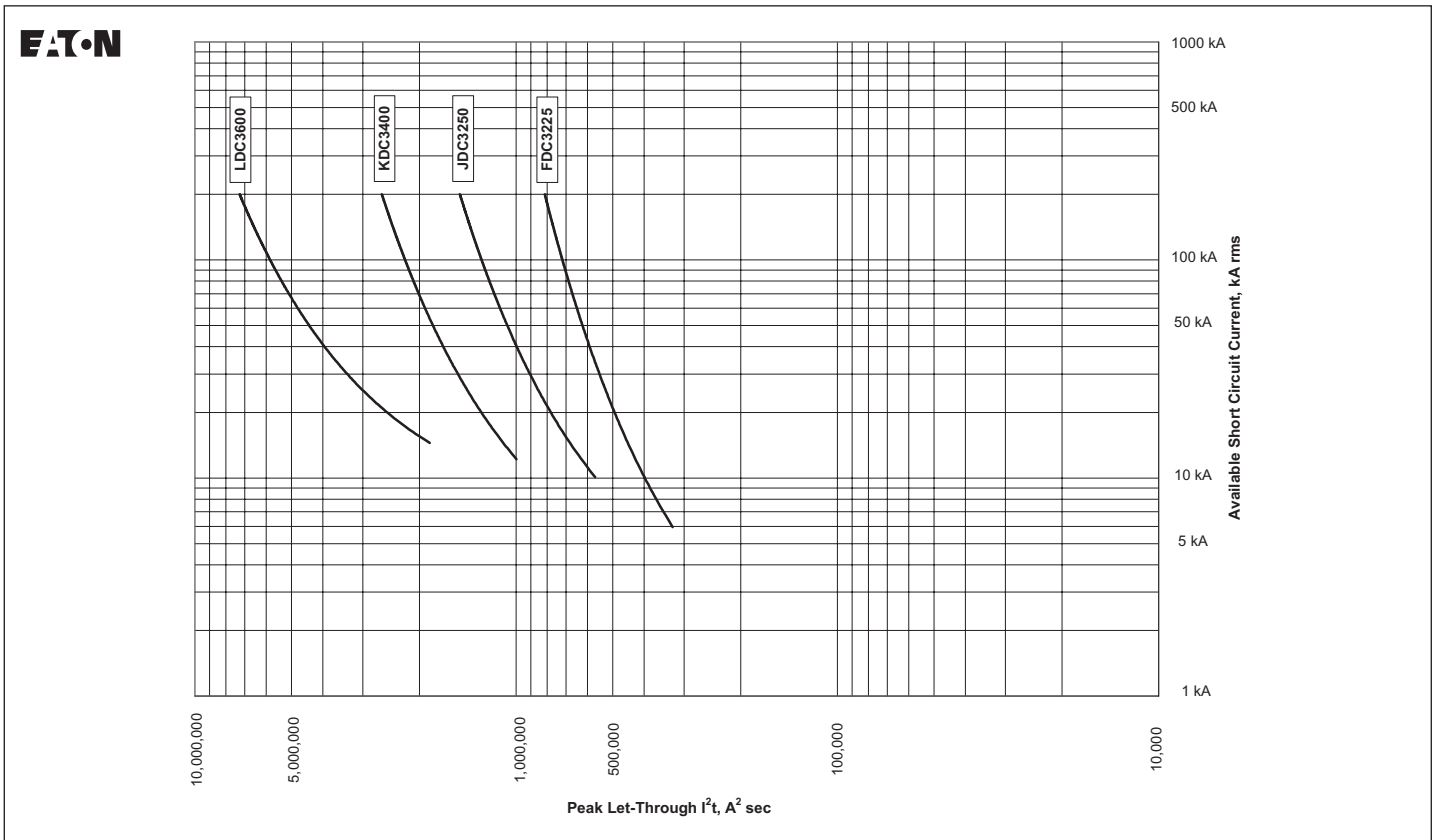


Figure 11. Peak Let-Through I^2t Curve — 240 V - Curve Number AD-29-166A

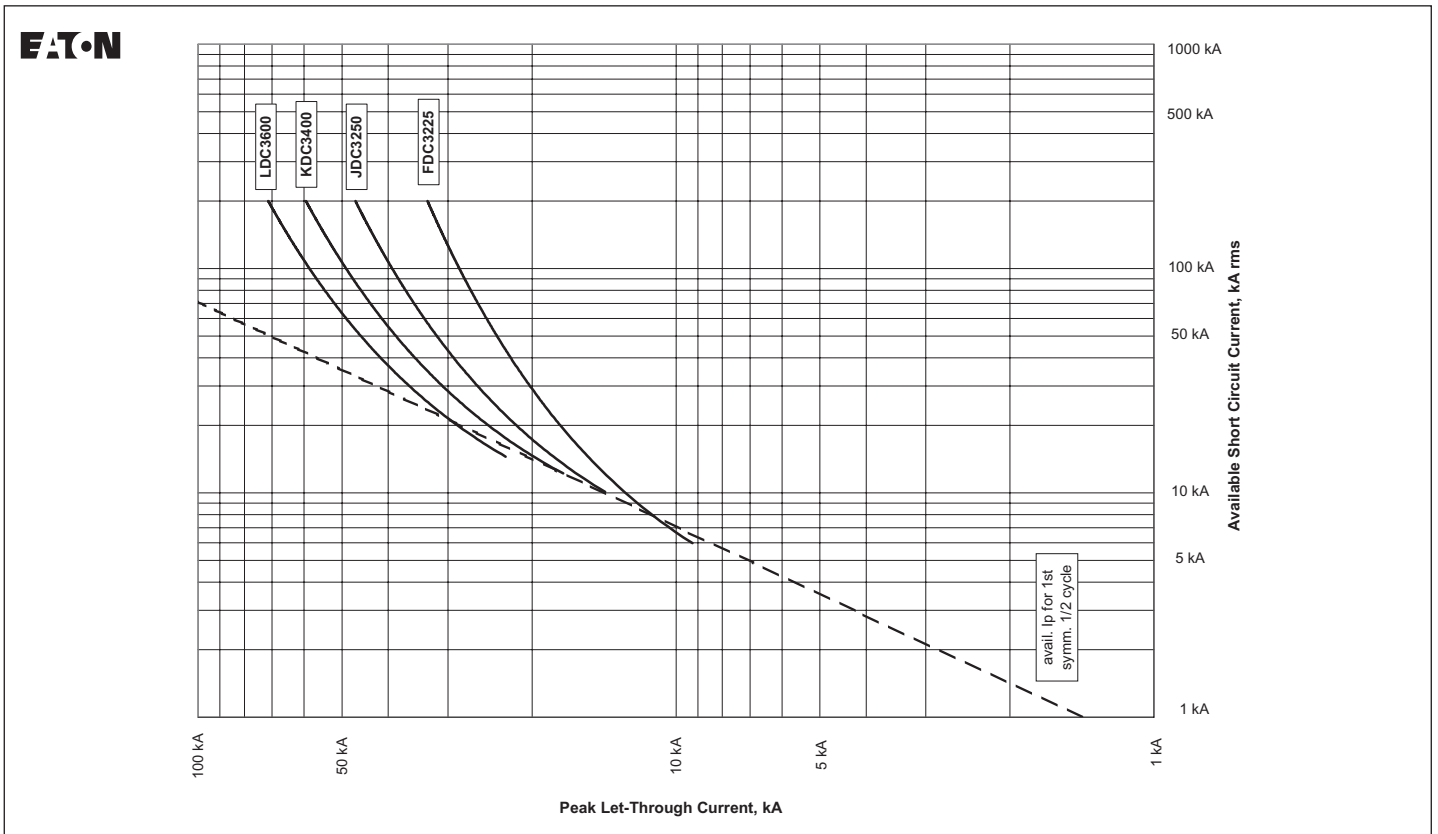


Figure 12. Peak Let-Through Current Curve — 240 V - Curve Number AD-29-166A

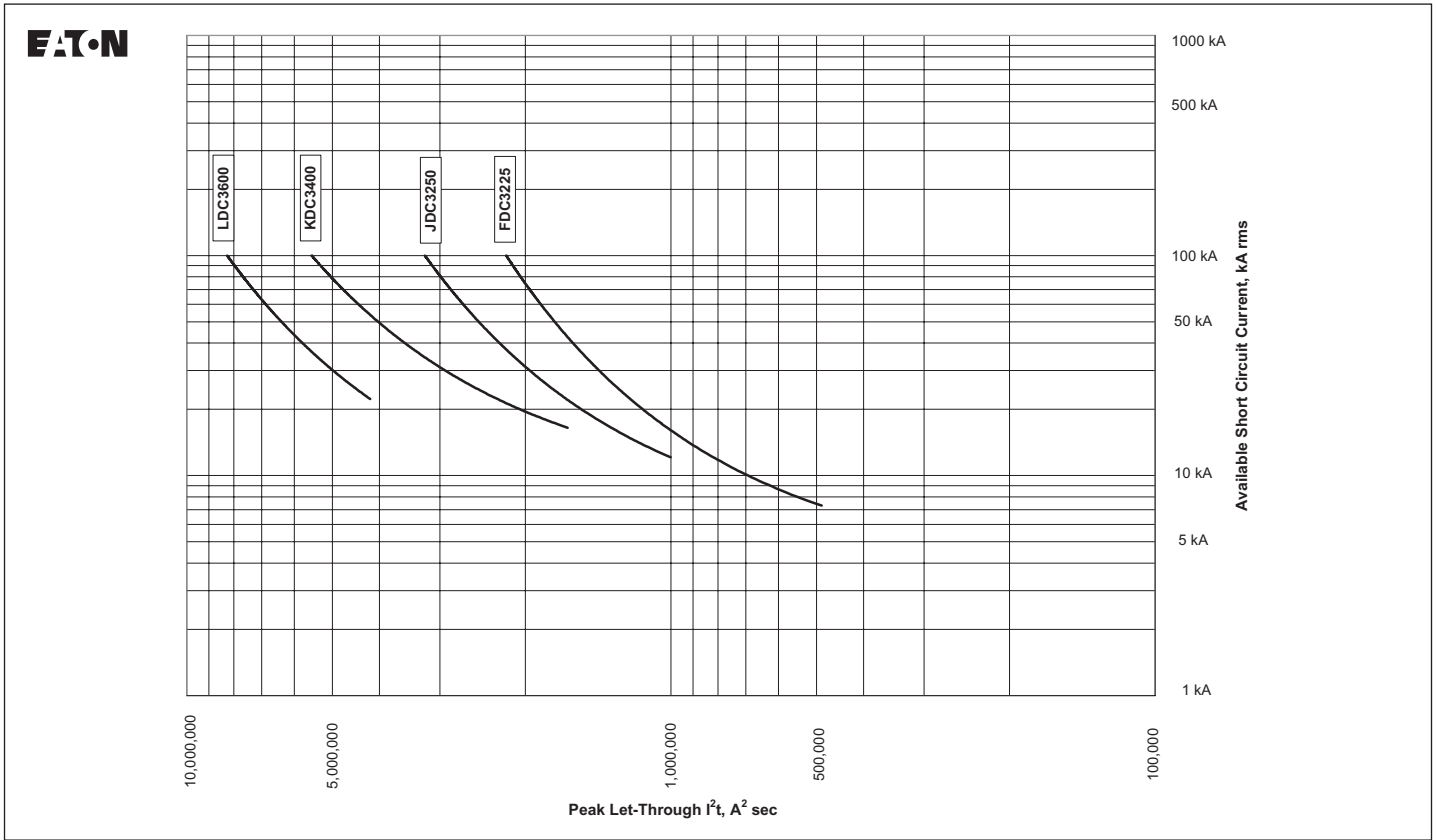


Figure 13. Peak Let-Through I²t Curve — 480 V - Curve Number AD-29-166B

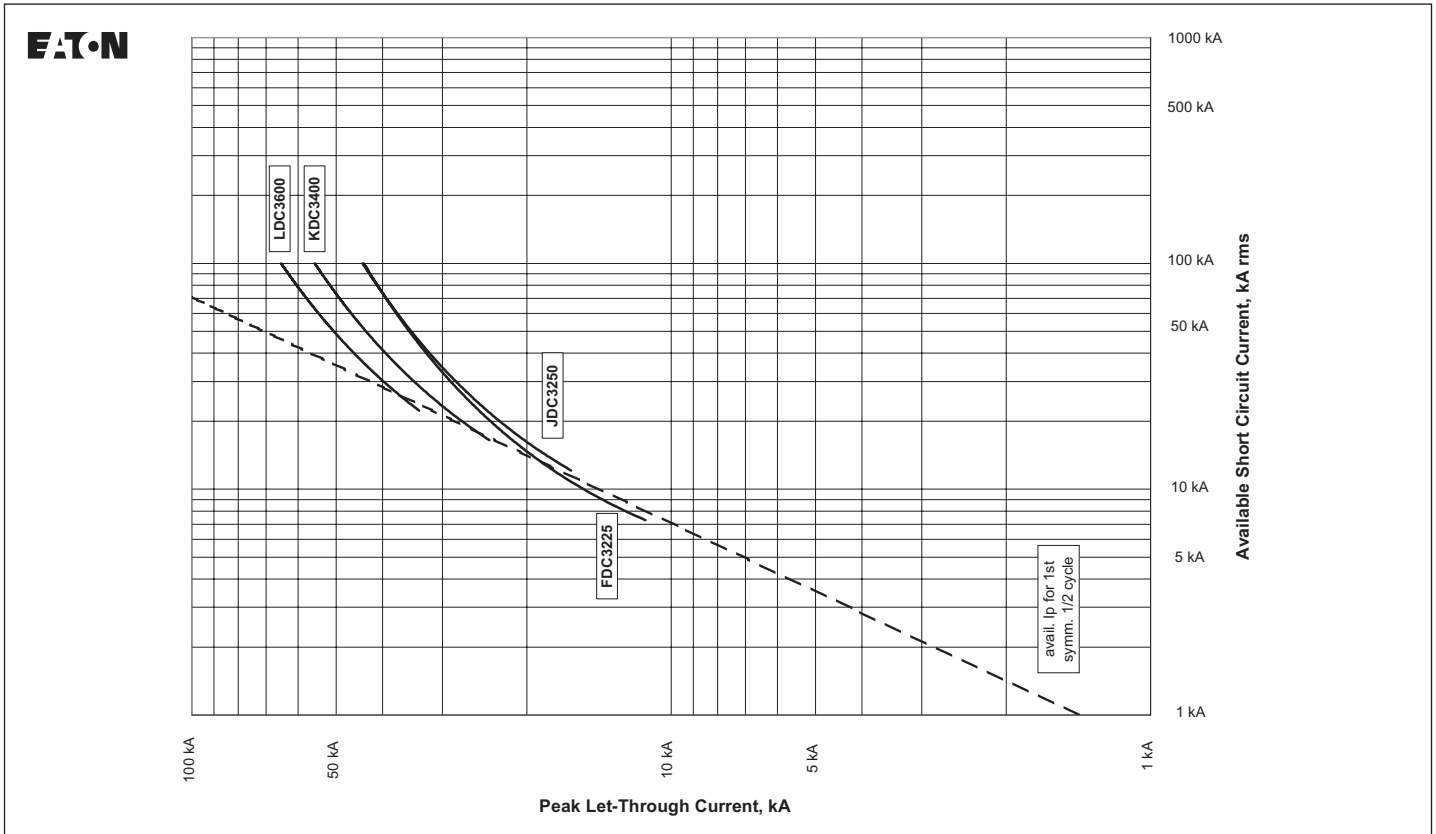


Figure 14. Peak Let-Through Current — 480 V - Curve Number AD-29-166B

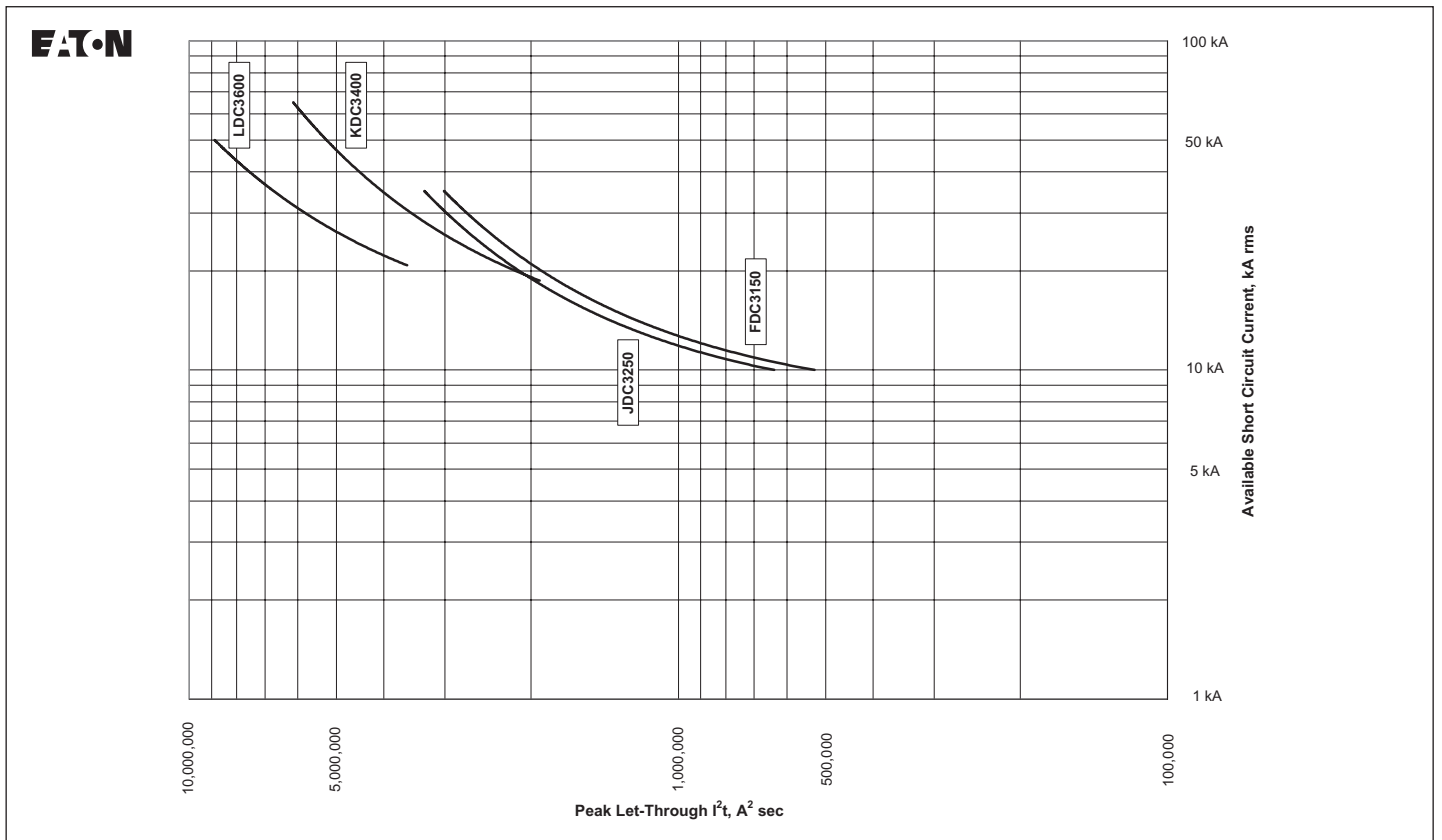


Figure 15. Peak Let-Through I²t — 600 V - Curve Number AD-29-166C

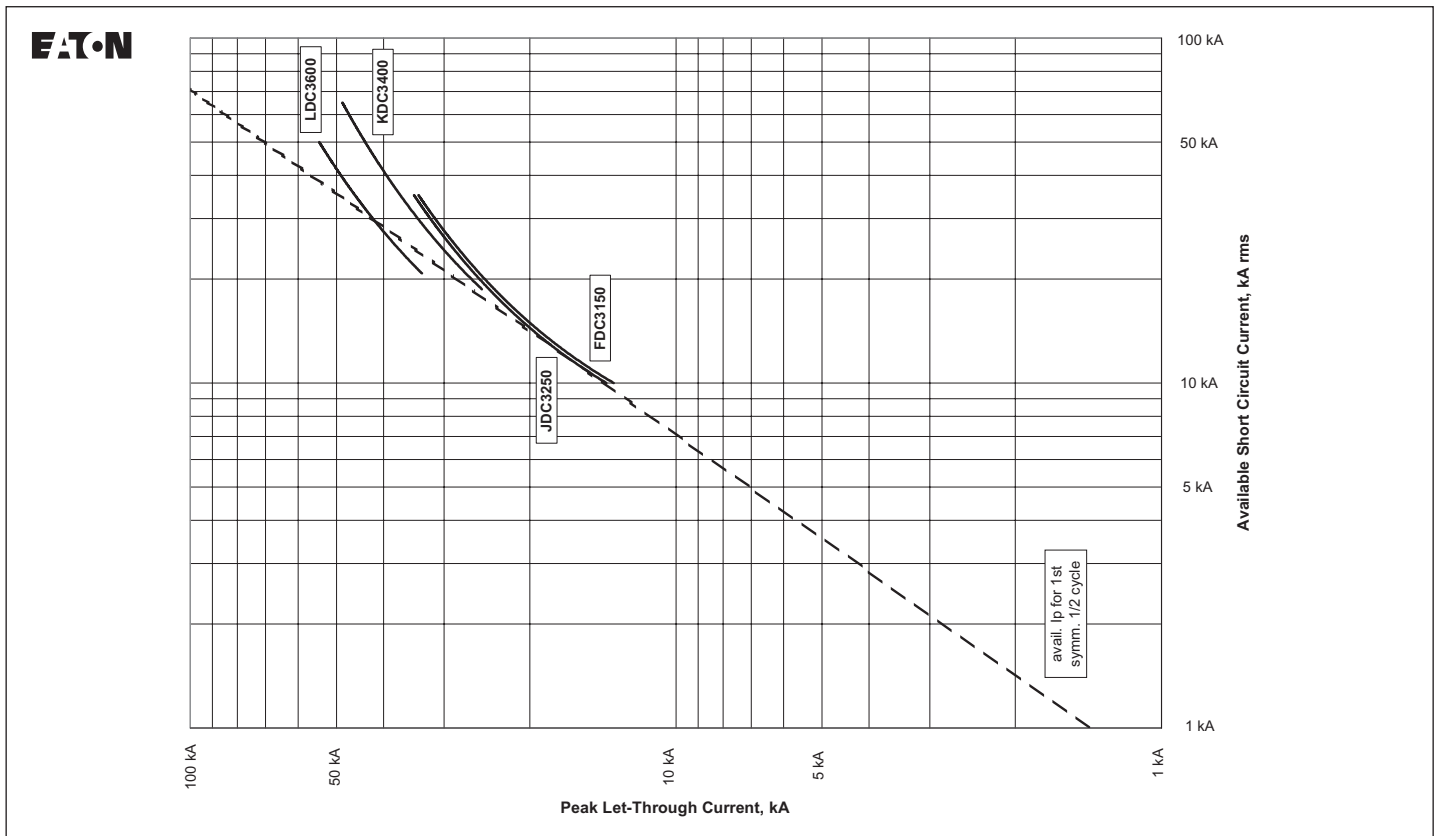


Figure 16. Peak Let-Through Current — 600 V - Curve Number AD-29-166C

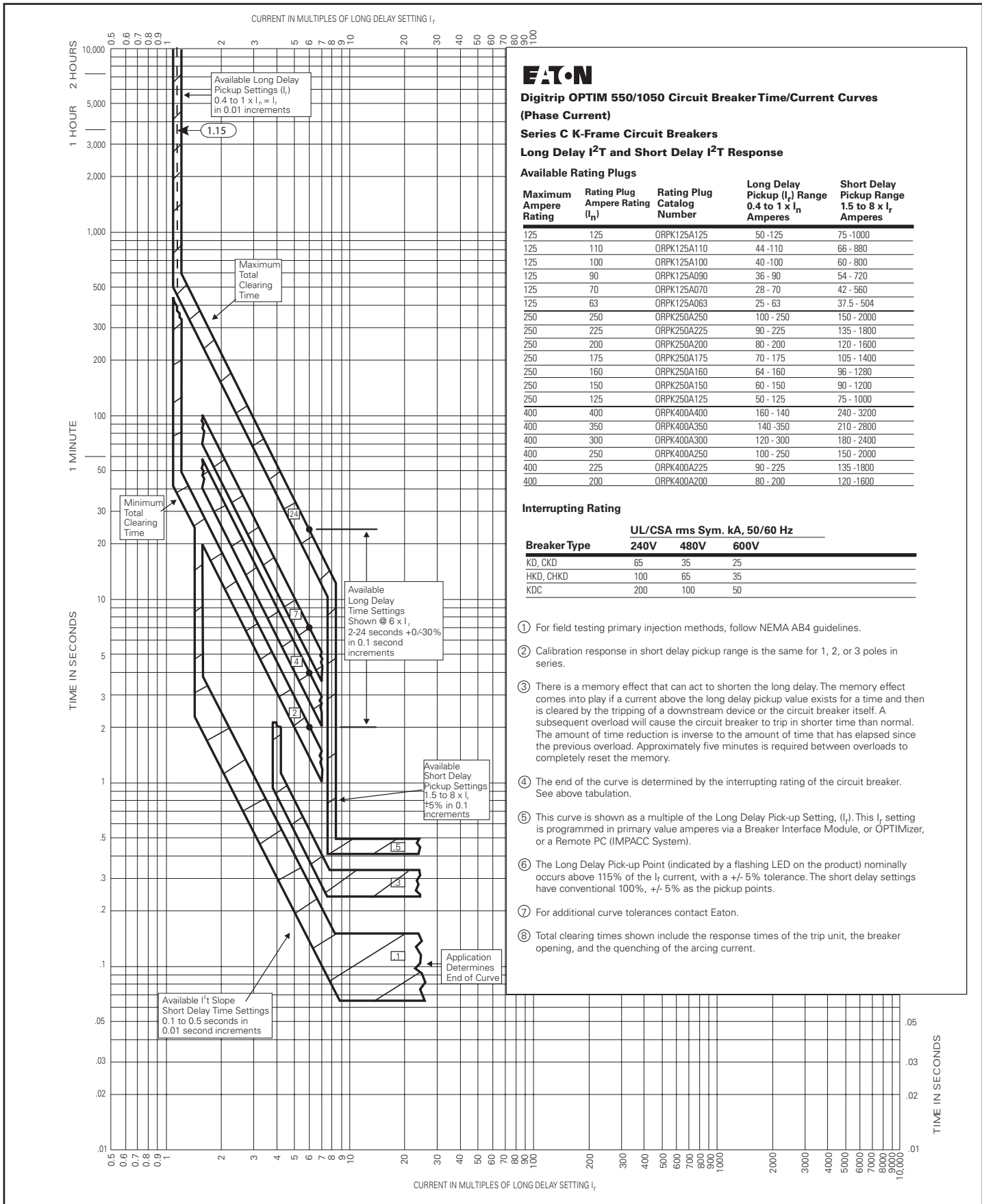


Figure 17. Digitrip Optim Long Delay I²T and Short Delay I²T Response - Curve Number SC-6924-98, May 1998

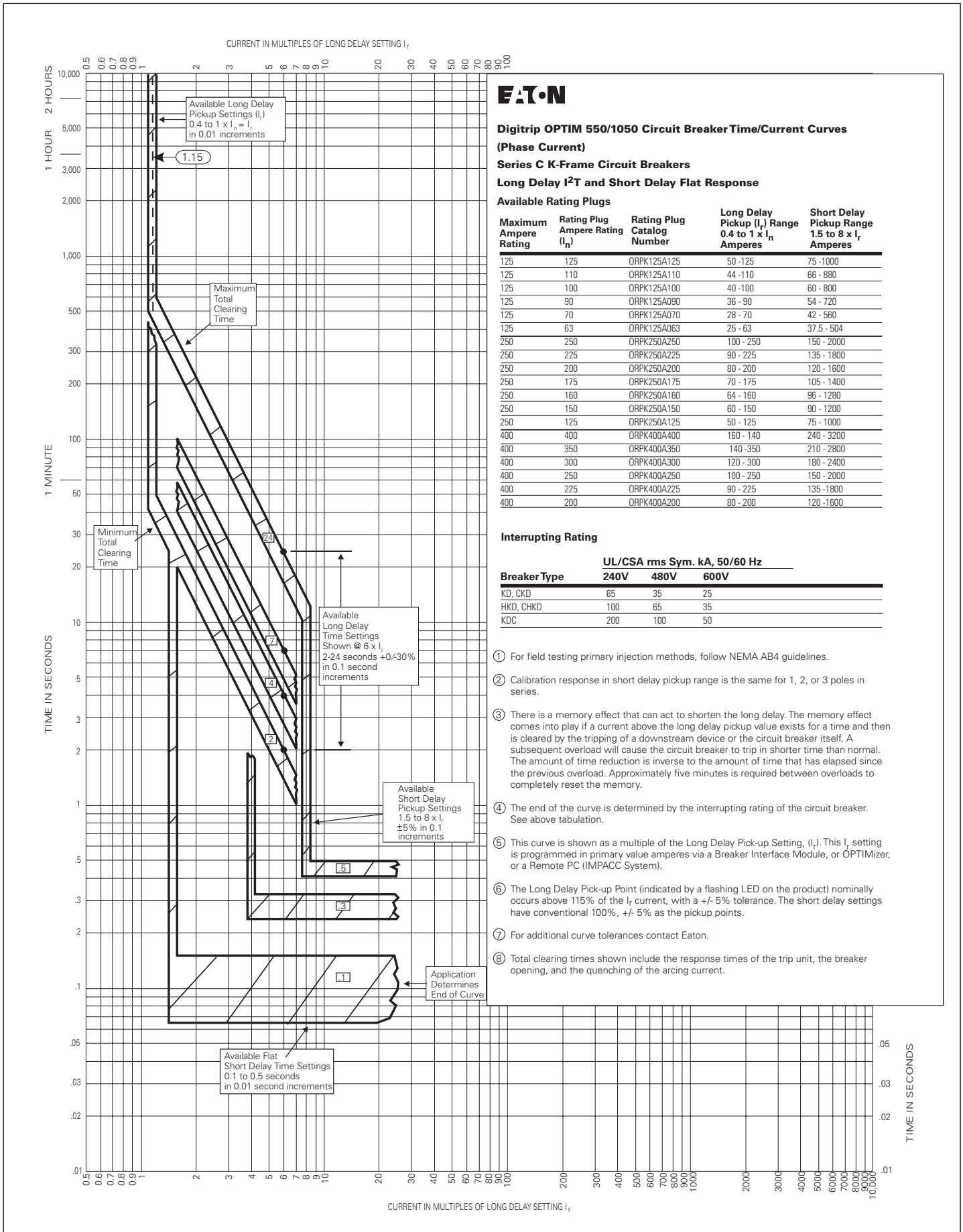


Figure 18. Digitrip Optim Long Delay I²T and Short Delay Flat Response - Curve Number SC-6925-98, May 1998

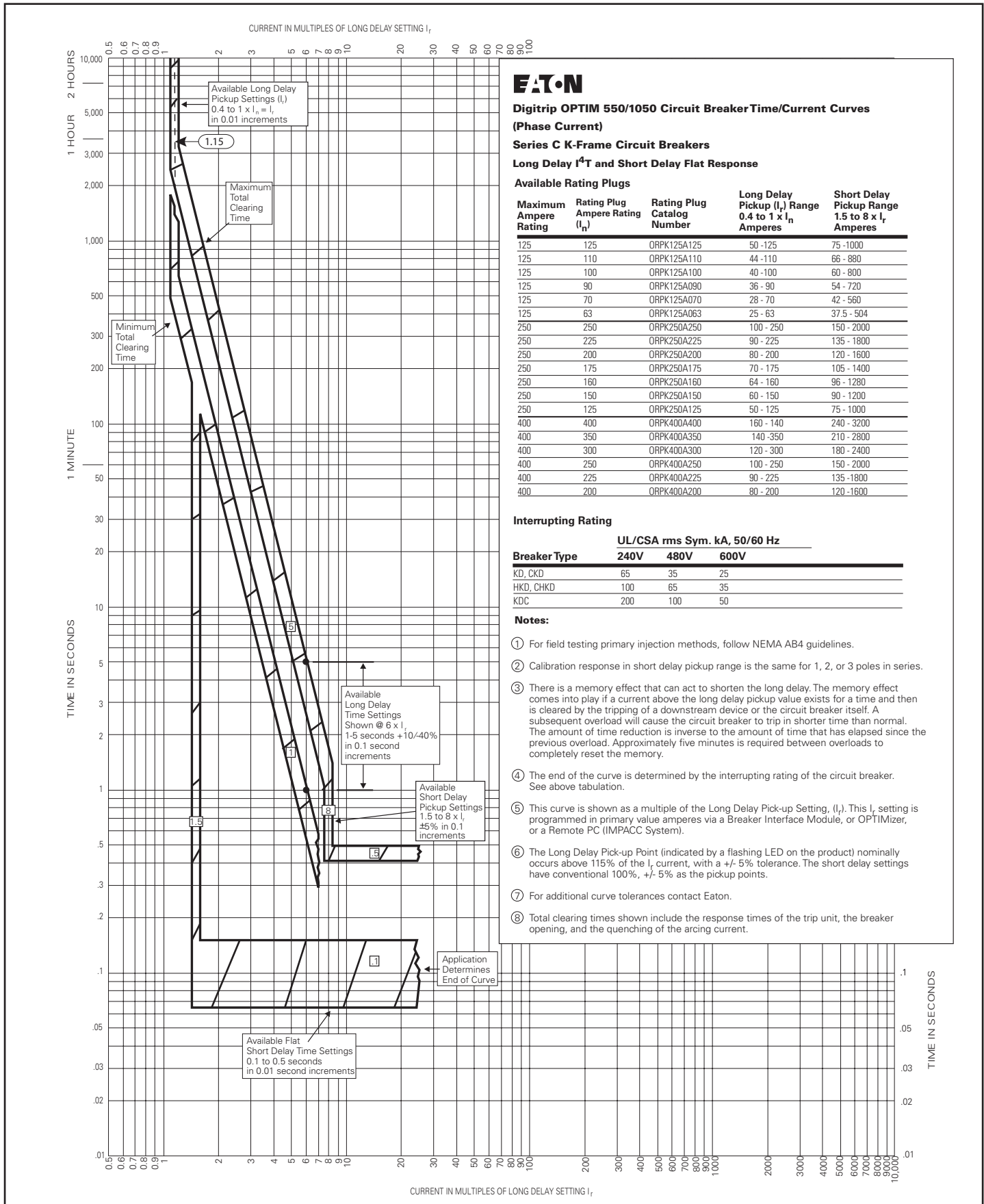


Figure 19. Digitrip Optim Long Delay I⁴T and Short Delay Flat Response - Curve Number SC-6926-98, May 1998

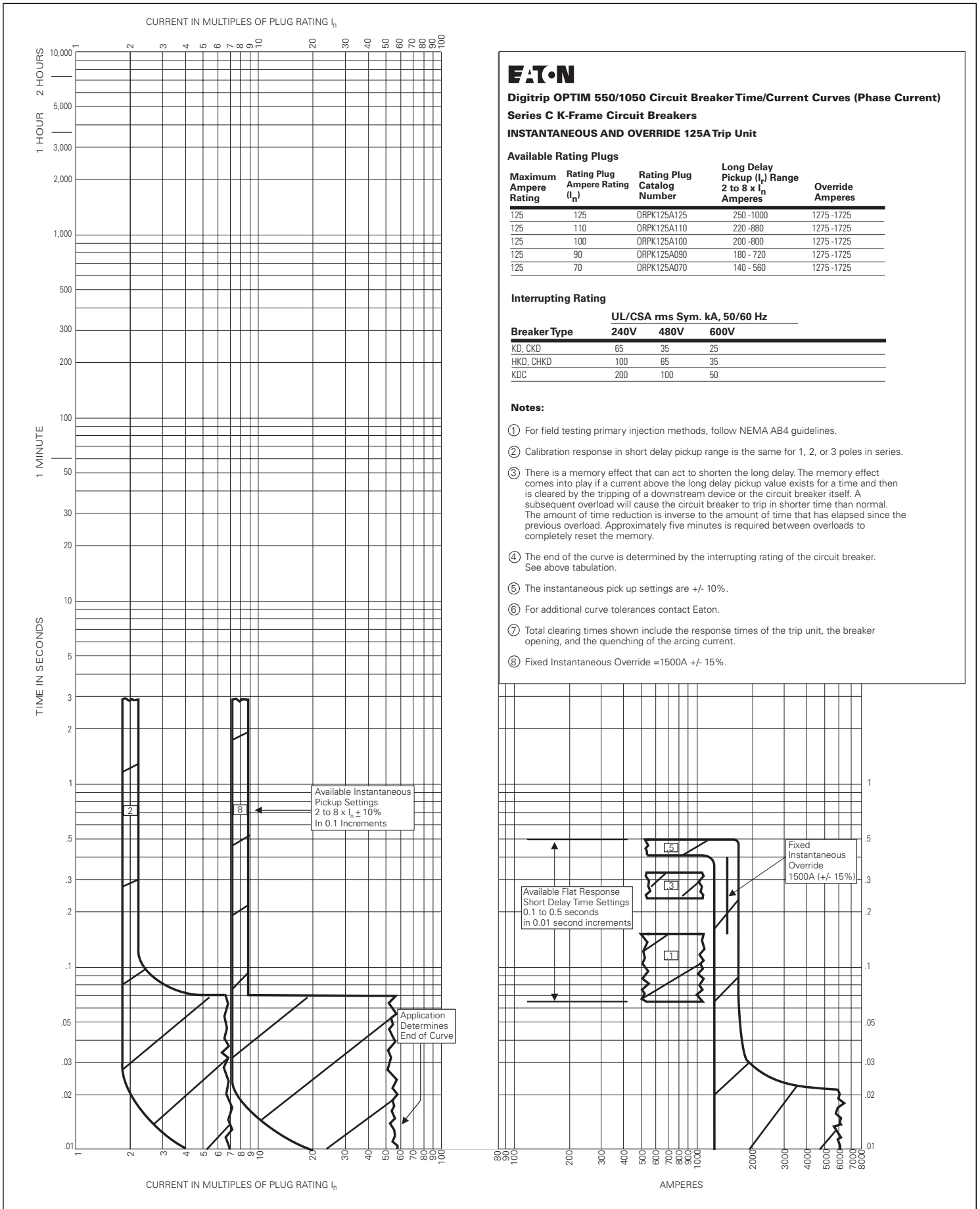


Figure 20. Digitrip Optim Instantaneous and Override, 125A Trip Unit - Curve Number SC-6927-98, May 1998

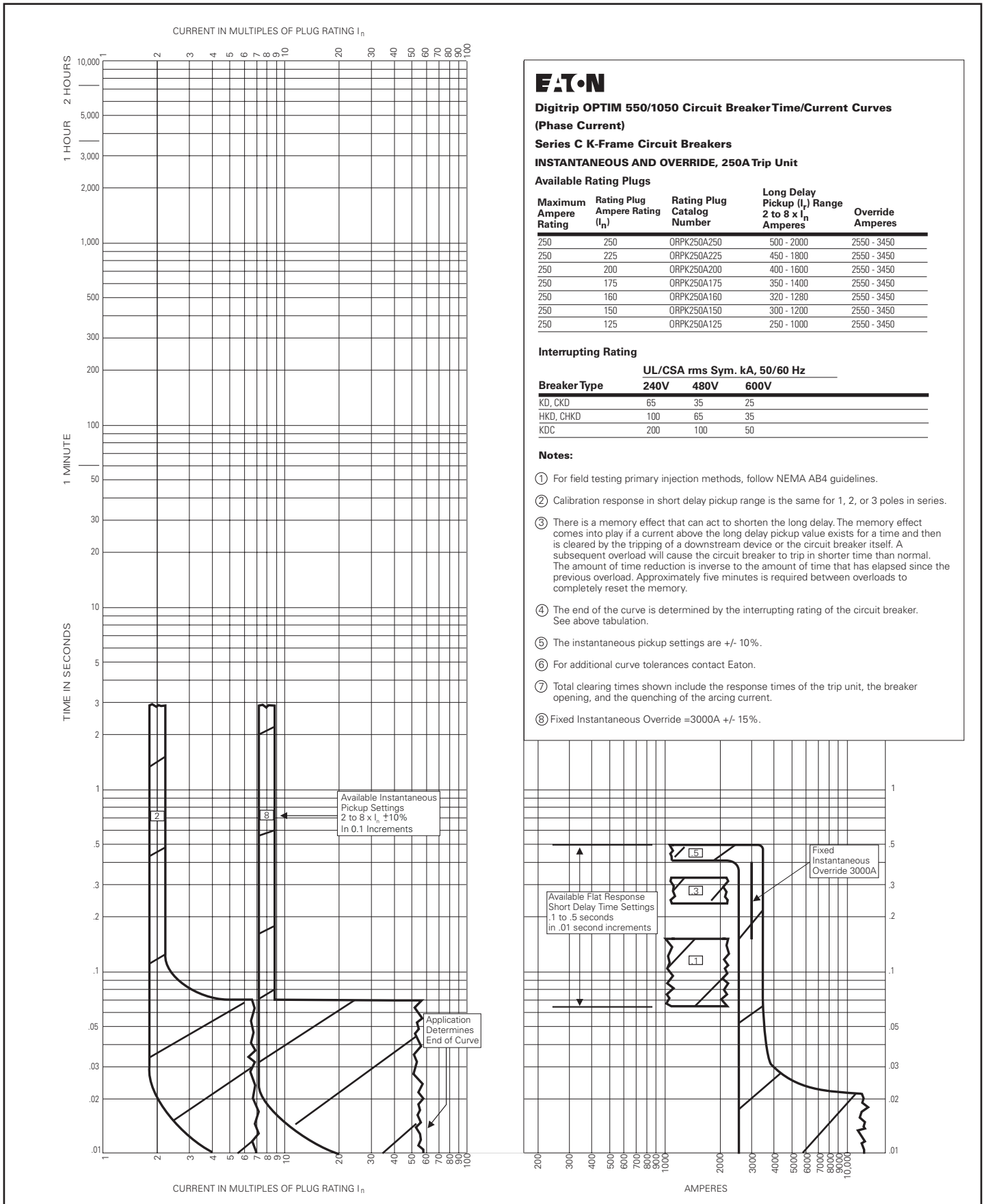


Figure 21. Digitrip Optim Instantaneous and Override, 250A Trip Unit - Curve Number SC-6928-98, May 1998

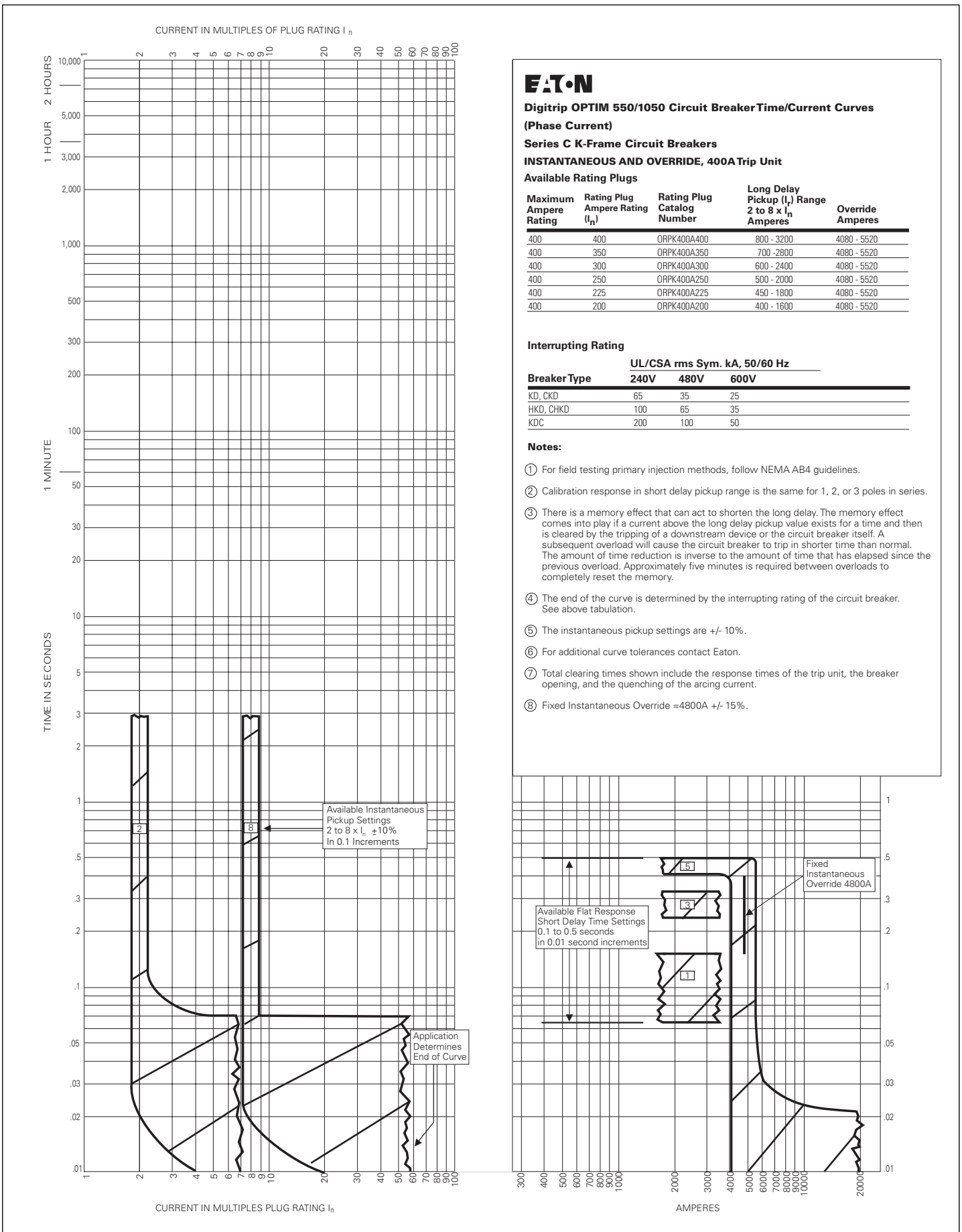


Figure 22. Digitrip Optim Instantaneous and Override, 400A Trip Unit - Curve Number SC-6929-98, May 1998

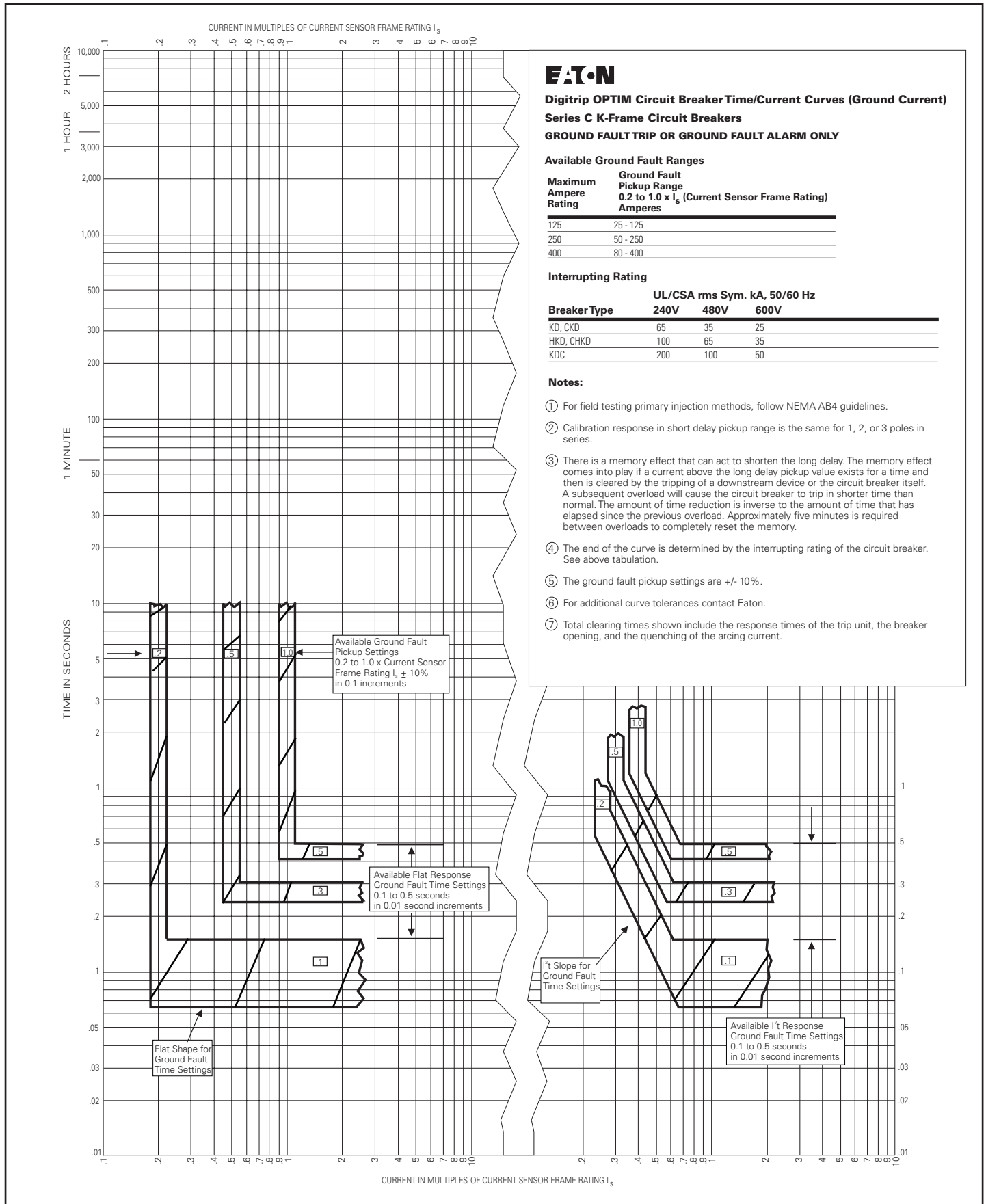


Figure 23. Digitrip Optim Ground Fault Trip or Ground Fault Alarm Only - Curve Number SC-6930-98, May 1998

- Legacy Product -

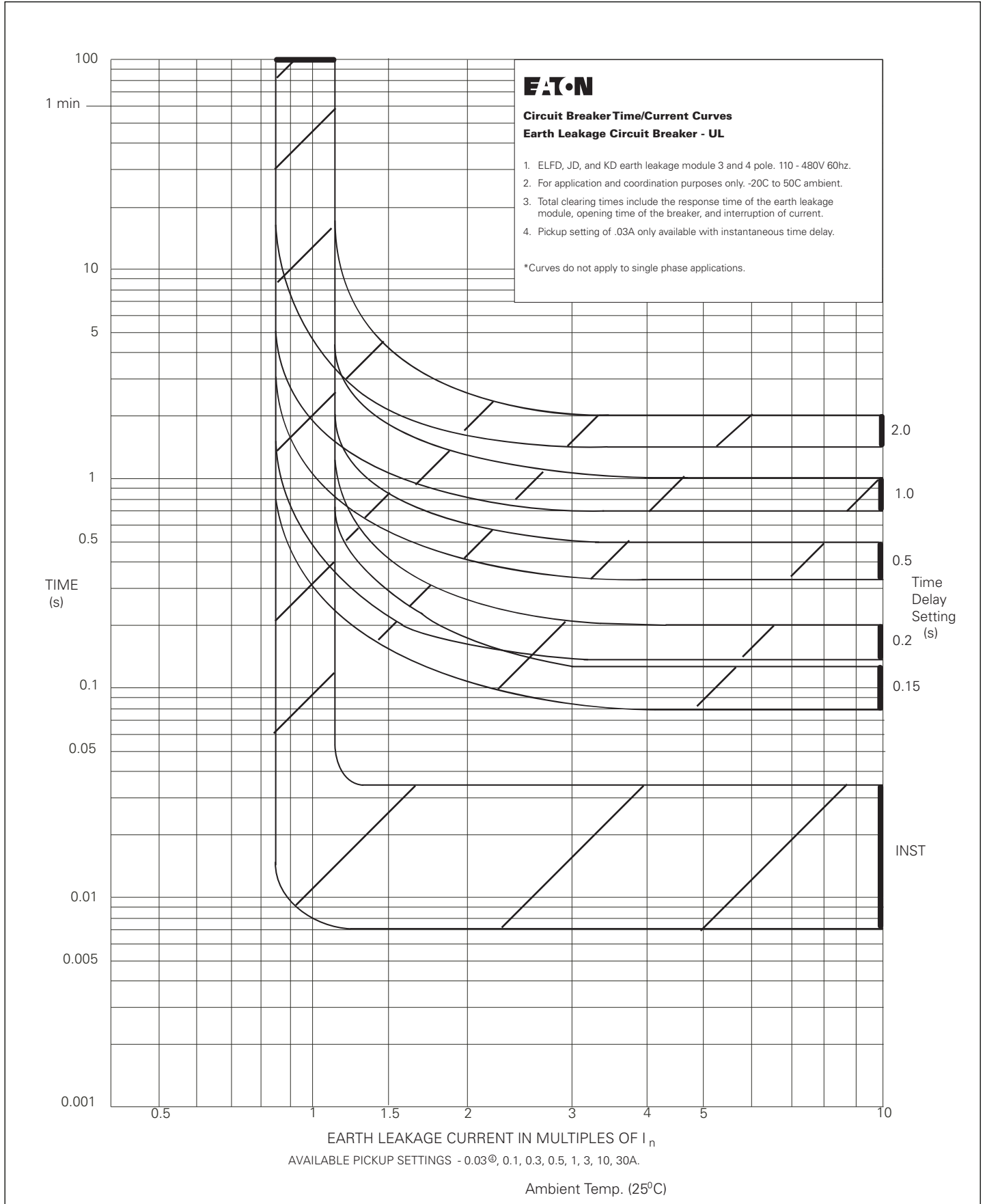


Figure 24. UL Series C K-Frame Circuit breaker Earth Leakage Module, 110-480V - Curve Number TC01212005E, March 2003

- Legacy Product -

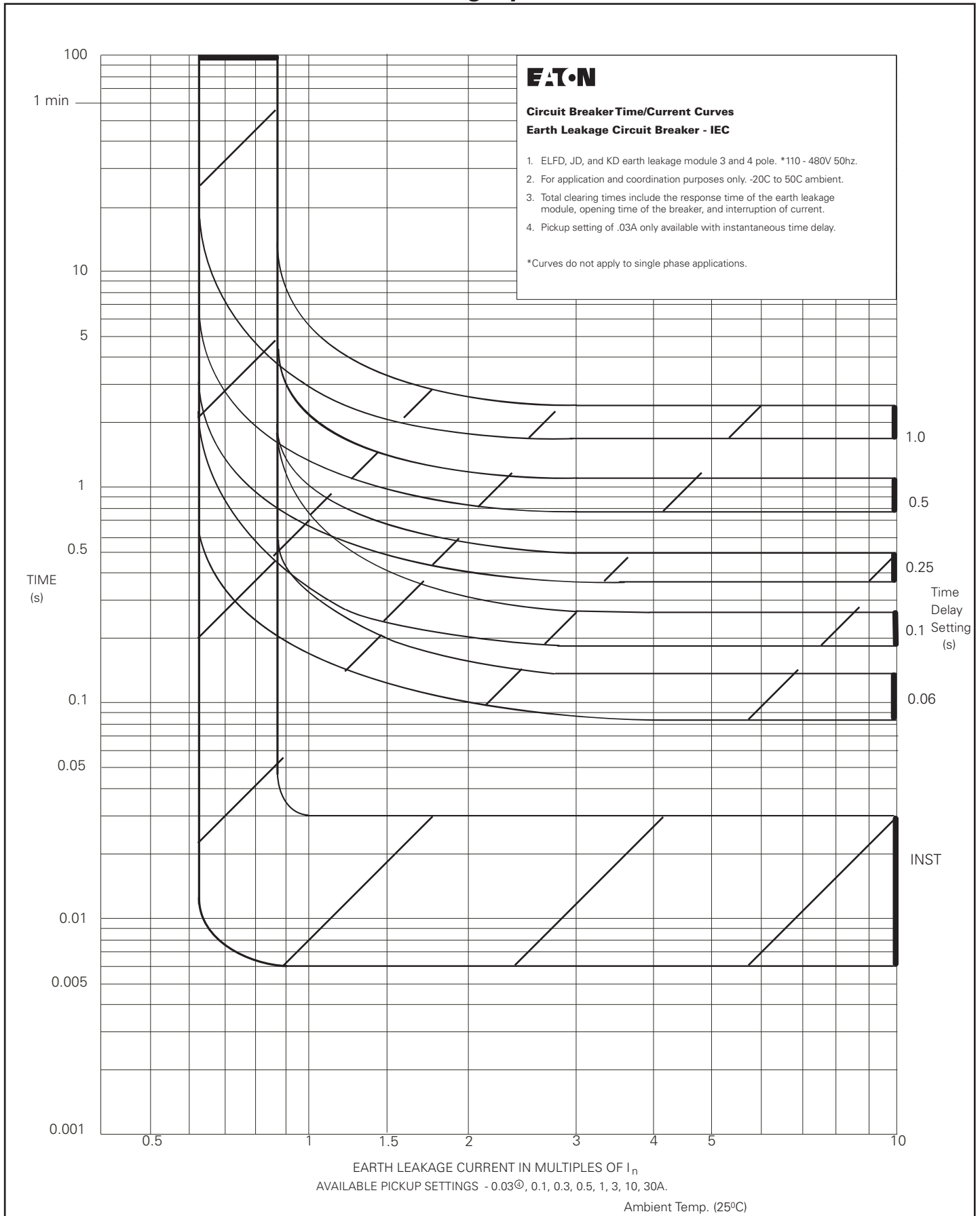
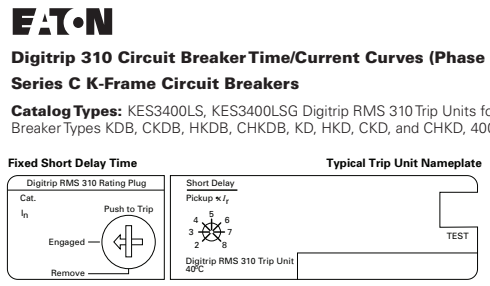
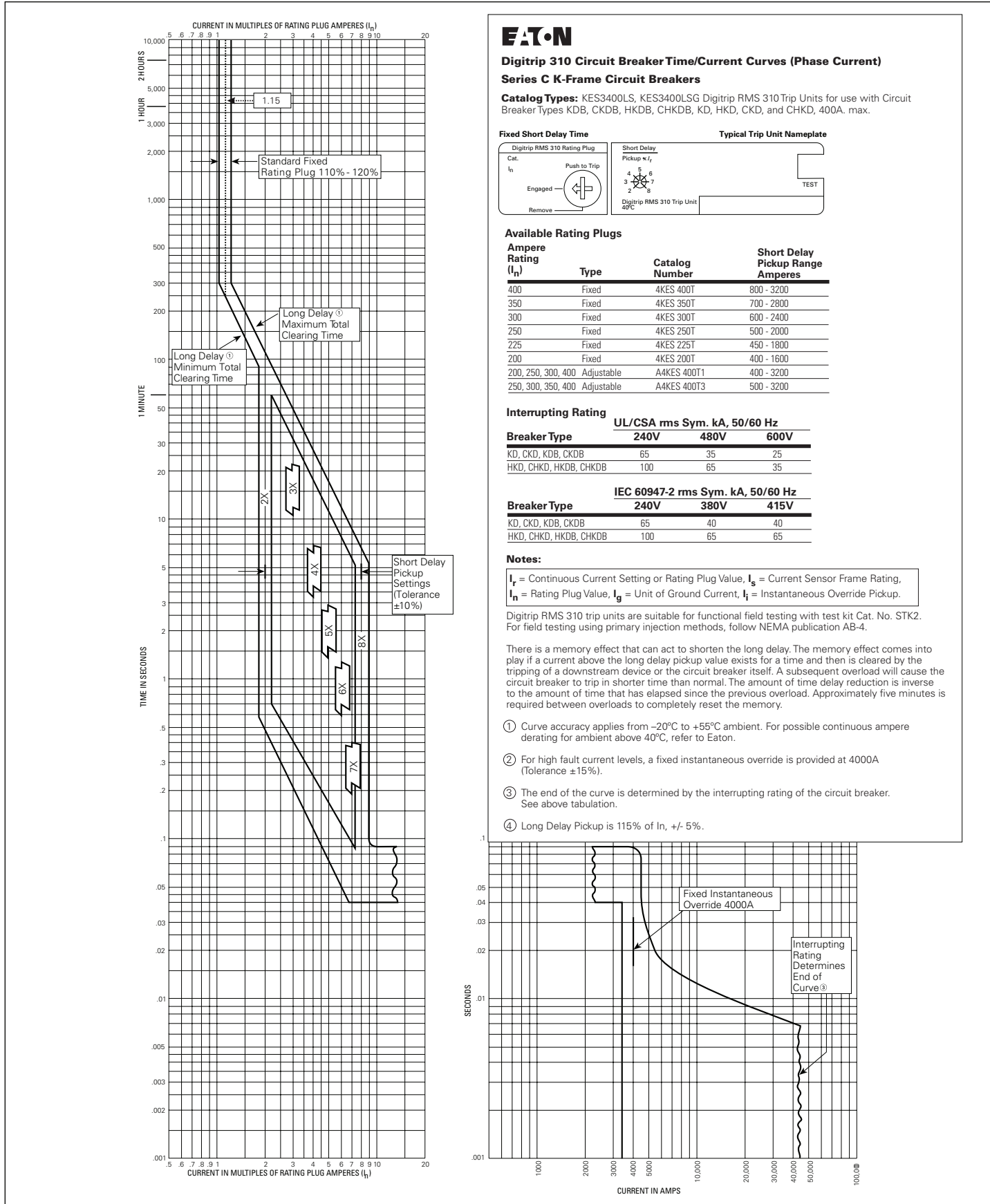


Figure 25. IEC Series C K-Frame Circuit breaker Earth Leakage Module, 110-480V - Curve Number TC01212006E, March 2003

- Legacy Product -



Available Rating Plugs

Ampere Rating (I _n)	Type	Catalog Number	Short Delay Pickup Range Amperes
400	Fixed	4KES 400T	800 - 3200
350	Fixed	4KES 350T	700 - 2800
300	Fixed	4KES 300T	600 - 2400
250	Fixed	4KES 250T	500 - 2000
225	Fixed	4KES 225T	450 - 1800
200	Fixed	4KES 200T	400 - 1600
200, 250, 300, 400	Adjustable	A4KES 400T1	400 - 3200
250, 300, 350, 400	Adjustable	A4KES 400T3	500 - 3200

Interrupting Rating *UL/CSA rms Sym. kA, 50/60 Hz*

Breaker Type	240V	480V	600V
KD, CKD, KDB, CKDB	65	35	25
HKD, CHKD, HKDB, CHKDB	100	65	35

IEC 60947-2 rms Sym. kA, 50/60 Hz

Breaker Type	240V	380V	415V
KD, CKD, KDB, CKDB	65	40	40
HKD, CHKD, HKDB, CHKDB	100	65	65

Notes:
 I_r = Continuous Current Setting or Rating Plug Value, I_g = Current Sensor Frame Rating,
 I_n = Rating Plug Value, I_g = Unit of Ground Current, I_i = Instantaneous Override Pickup.

Digitrip RMS 310 trip units are suitable for functional field testing with test kit Cat. No. STK2. For field testing using primary injection methods, follow NEMA publication AB-4.

There is a memory effect that can act to shorten the long delay. The memory effect comes into play if a current above the long delay pickup value exists for a time and then is cleared by the tripping of a downstream device or the circuit breaker itself. A subsequent overload will cause the circuit breaker to trip in shorter time than normal. The amount of time delay reduction is inverse to the amount of time that has elapsed since the previous overload. Approximately five minutes is required between overloads to completely reset the memory.

- ① Curve accuracy applies from -20°C to +55°C ambient. For possible continuous ampere derating for ambient above 40°C, refer to Eaton.
- ② For high fault current levels, a fixed instantaneous override is provided at 4000A (Tolerance ±15%).
- ③ The end of the curve is determined by the interrupting rating of the circuit breaker. See above tabulation.
- ④ Long Delay Pickup is 115% of I_n, +/- 5%.

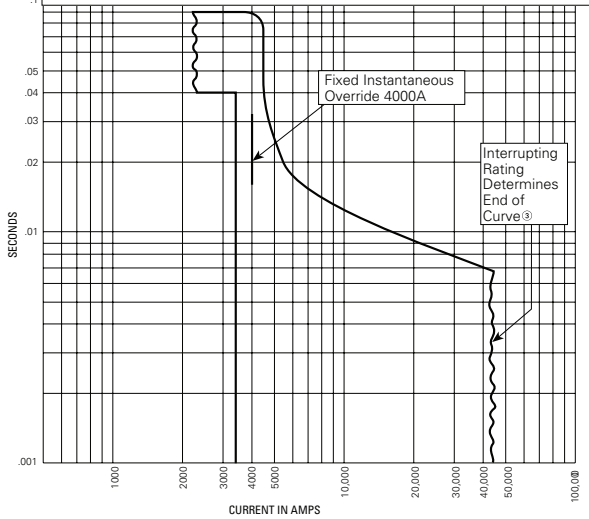


Figure 26. Series C Types KD, CKD, HKD, CHKD Circuit Breakers Equipped with Type KES Digitrip RMS 310 Trip Units, Types KES3400LS, KES3400LSG

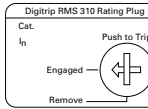


Digitrip 310 Circuit Breaker Time/Current Curves (Phase Current)

Series C K-Frame Circuit Breakers

Catalog Types: KES3400LSI, KES3400LSIG Digitrip RMS 310 Units for use with Circuit Breaker Types KD, HKD, CKD, and CHKD, 400A. max.

Fixed Short Delay Time



Short Delay

Pickup $\times I_r$

4 5 6

3 7

2 8

Digitrip RMS 310 Trip Unit

40°C

Typical Trip Unit Nameplate

TEST

Available Rating Plugs

Ampere Rating (I_n)	Type	Catalog Number	Short Delay Pickup Range Amperes
400	Fixed	4KES 400T	800 - 3200
350	Fixed	4KES 350T	700 - 2800
300	Fixed	4KES 300T	600 - 2400
250	Fixed	4KES 250T	500 - 2000
225	Fixed	4KES 225T	450 - 1800
200	Fixed	4KES 200T	400 - 1600
200, 250, 300, 400	Adjustable	A4KES 400T1	400 - 3200
250, 300, 350, 400	Adjustable	A4KES 400T3	500 - 3200

Interrupting Rating

Breaker Type	UL/CSA rms Sym. kA, 50/60 Hz		
	240V	480V	600V
KD, CKD	65	35	25
HKD, CHKD	100	65	35

Breaker Type	IEC 60947-2 rms Sym. kA, 50/60 Hz		
	240V	380V	415V
KD, CKD	65	40	40
HKD, CHKD	100	65	65

Notes:

I_r = Continuous Current Setting or Rating Plug Value, I_s = Current Sensor Frame Rating, I_n = Rating Plug Value, I_g = Unit of Ground Current, I_i = Instantaneous Override Pickup.

Digitrip RMS 310 trip units are suitable for functional field testing with test kit Cat. No. STK2. For field testing using primary injection methods, follow NEMA publication AB-4.

There is a memory effect that can act to shorten the long delay. The memory effect comes into play if a current above the long delay pickup value exists for a time and then is cleared by the tripping of a downstream device or the circuit breaker itself. A subsequent overload will cause the circuit breaker to trip in shorter time than normal. The amount of time delay reduction is inverse to the amount of time that has elapsed since the previous overload. Approximately five minutes is required between overloads to completely reset the memory.

- Curve accuracy applies from -20°C to $+55^{\circ}\text{C}$ ambient. For possible continuous ampere derating for ambient above 40°C , refer to Eaton.
- For high fault current levels, a fixed instantaneous override is provided at 4000A (Tolerance $\pm 15\%$).
- The end of the curve is determined by the interrupting rating of the circuit breaker. See above tabulation.
- Long Delay Pickup is 115% of I_n , $\pm 5\%$.

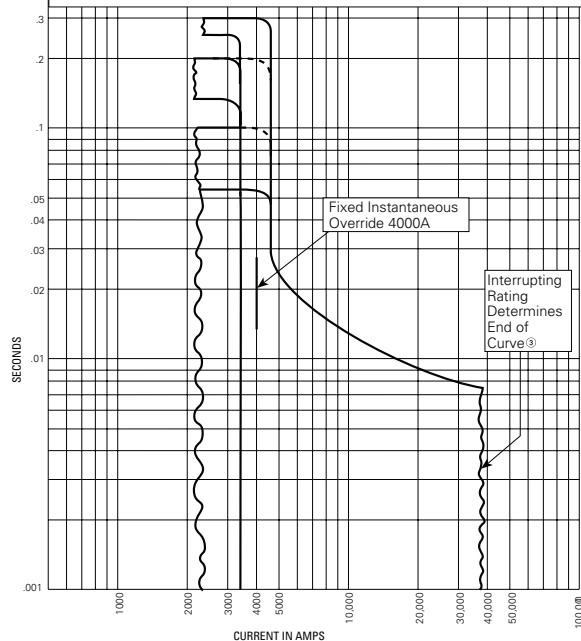
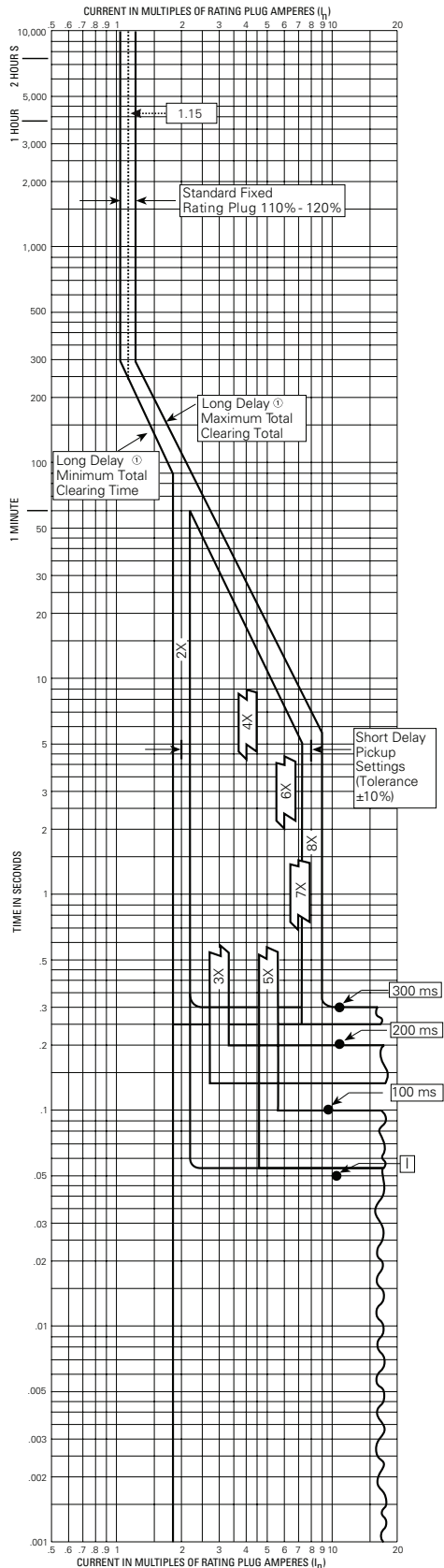
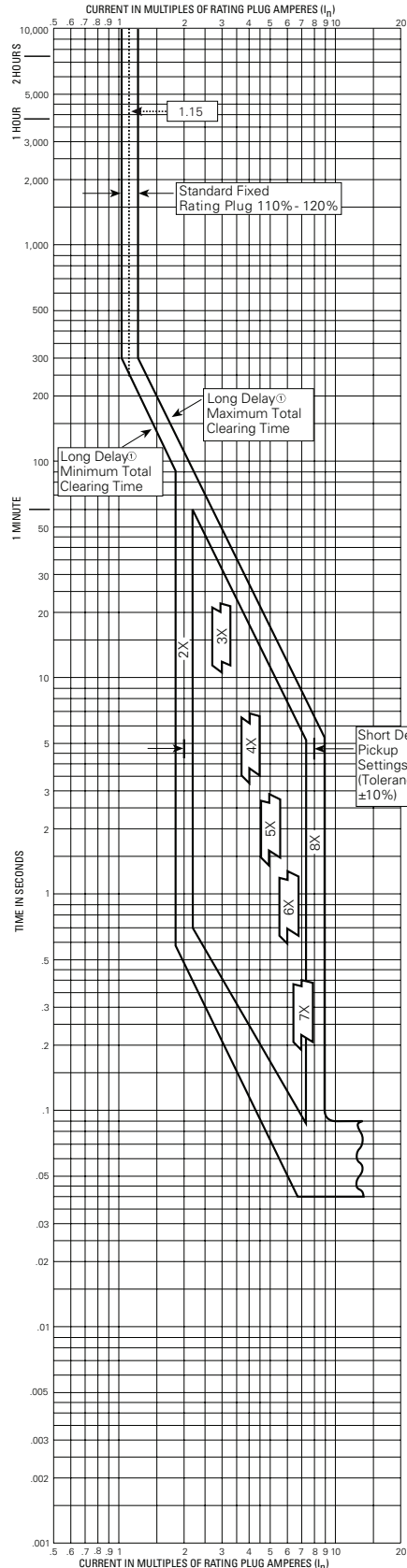


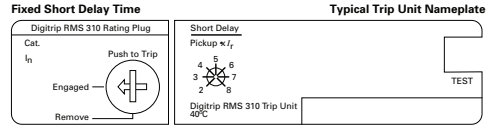
Figure 27. Series C Types KD, CKD, HKD, CHKD Circuit Breakers Equipped with Type KES Digitrip RMS 310 Trip Units, Types KES3400LSI, KES3400LSIG

- Legacy Product -



Digitrip 310 Circuit Breaker Time/Current Curves (Phase Current)
Series C K-Frame Circuit Breakers

Catalog Types: KES3250LS, KES3250LSG Digitrip RMS 310 Units for use with Circuit Breaker Types KDB, CKDB, HKDB, CHKDB, KD, HKD, CKD, and CHKD, 250A. max.



Available Rating Plugs

Ampere Rating (I _n)	Type	Catalog Number	Short Delay Pickup Range Amperes
250	Fixed	2KES 250T	250 - 1000
225	Fixed	2KES 225T	220 - 880
200	Fixed	2KES 200T	200 - 800
175	Fixed	2KES 175T	180 - 720
150	Fixed	2KES 150T	140 - 560
125	Fixed	2KES 125T	250 - 1000
125, 150, 200, 250	Adjustable	A2KES 250T1	250 - 2000

Interrupting Rating

Breaker Type	UL/CSA rms Sym. kA, 50/60 Hz		
	240V	480V	600V
KD, CKD, KDB, CKDB	65	35	25
HKD, CHKD, HKDB, CHKDB	100	65	35

Breaker Type	IEC 60947-2 rms Sym. kA, 50/60 Hz		
	240V	380V	415V
KD, CKD, KDB, CKDB	65	40	40
HKD, CHKD, HKDB, CHKDB	100	65	65

Notes:

I_r = Continuous Current Setting or Rating Plug Value, I_s = Current Sensor Frame Rating, I_n = Rating Plug Value, I_g = Unit of Ground Current, I_o = Instantaneous Override Pickup.

Digitrip RMS 310 trip units are suitable for functional field testing with test kit Cat. No. STK2. For field testing using primary injection methods, follow NEMA publication AB-4.

There is a memory effect that can act to shorten the long delay. The memory effect comes into play if a current above the long delay pickup value exists for a time and then is cleared by the tripping of a downstream device or the circuit breaker itself. A subsequent overload will cause the circuit breaker to trip in shorter time than normal. The amount of time delay reduction is inverse to the amount of time that has elapsed since the previous overload. Approximately five minutes is required between overloads to completely reset the memory.

- Curve accuracy applies from -20°C to +55°C ambient. For possible continuous ampere derating for ambient above 40°C, refer to Eaton.
- For high fault current levels, a fixed instantaneous override is provided at 4000A (Tolerance ±15%).
- The end of the curve is determined by the interrupting rating of the circuit breaker. See above tabulation.
- Long Delay Pickup is 115% of I_n, +/- 5%.

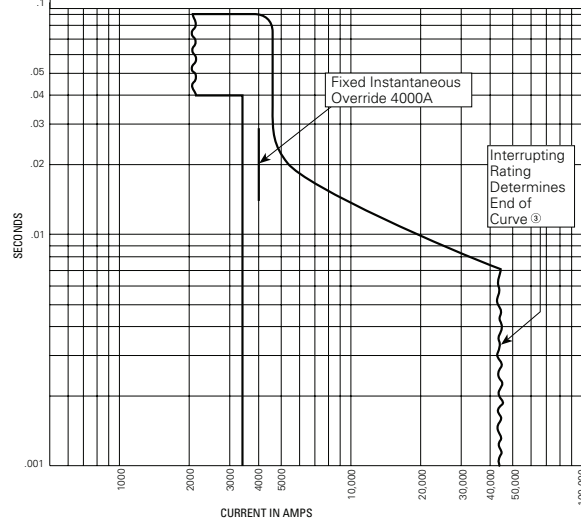


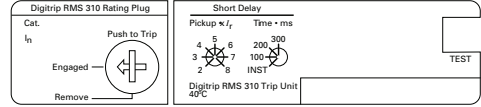
Figure 28. Series C Types KD, CKD, HKD, CHKD Circuit Breakers Equipped with Type KES Digitrip RMS 310 Trip Units, Types KES3250LS, KES3250LSG



Digitrip 310 Circuit Breaker Time/Current Curves (Phase Current)
Series C K-Frame Circuit Breakers

Catalog Types: KES3250LSI, KES3250LSIG Digitrip RMS 310 Units for use with Circuit Breaker Types KDB, CKDB, HKDB, CHKDB, KD, HKD, CKD, and CHKD, 250A. max.

Adjustable Short Delay Time



Available Rating Plugs

Ampere Rating (I _n)	Type	Catalog Number	Short Delay Pickup Range Amperes
250	Fixed	2KES 250T	500 - 2000
225	Fixed	2KES 225T	450 - 1800
200	Fixed	2KES 200T	400 - 1600
175	Fixed	2KES 175T	350 - 1400
150	Fixed	2KES 150T	300 - 1200
125	Fixed	2KES 125T	250 - 1000
125, 150, 200, 250	Adjustable	A2KES 250T1	250 - 2000

Interrupting Rating

Breaker Type	UL/CSA rms Sym. kA, 50/60 Hz		
	240V	480V	600V
KD, CKD, KDB, CKDB	65	35	25
HKD, CHKD, HKDB, CHKDB	100	65	35

Breaker Type	IEC 60947-2 rms Sym. kA, 50/60 Hz		
	240V	380V	415V
KD, CKD, KDB, CKDB	65	40	40
HKD, CHKD, HKDB, CHKDB	100	65	65

Notes:

I_c = Continuous Current Setting or Rating Plug Value, I_s = Current Sensor Frame Rating, I_n = Rating Plug Value, I_g = Unit of Ground Current, I_i = Instantaneous Override Pickup.

Digitrip RMS 310 trip units are suitable for functional field testing with test kit Cat. No. STK2. For field testing using primary injection methods, follow NEMA publication AB-4.

There is a memory effect that can act to shorten the long delay. The memory effect comes into play if a current above the long delay pickup value exists for a time and then is cleared by the tripping of a downstream device or the circuit breaker itself. A subsequent overload will cause the circuit breaker to trip in shorter time than normal. The amount of time delay reduction is inverse to the amount of time that has elapsed since the previous overload. Approximately five minutes is required between overloads to completely reset the memory.

- Curve accuracy applies from -20°C to +55°C ambient. For possible continuous ampere derating for ambient above 40°C, refer to Eaton.
- For high fault current levels, a fixed instantaneous override is provided at 4000A (Tolerance ±15%).
- The end of the curve is determined by the interrupting rating of the circuit breaker. See above tabulation.
- Long Delay Pickup is 115% of I_n, +/- 5%.

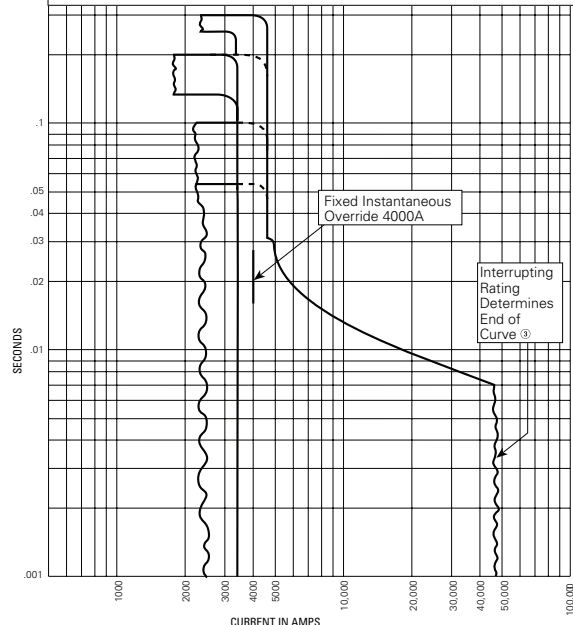
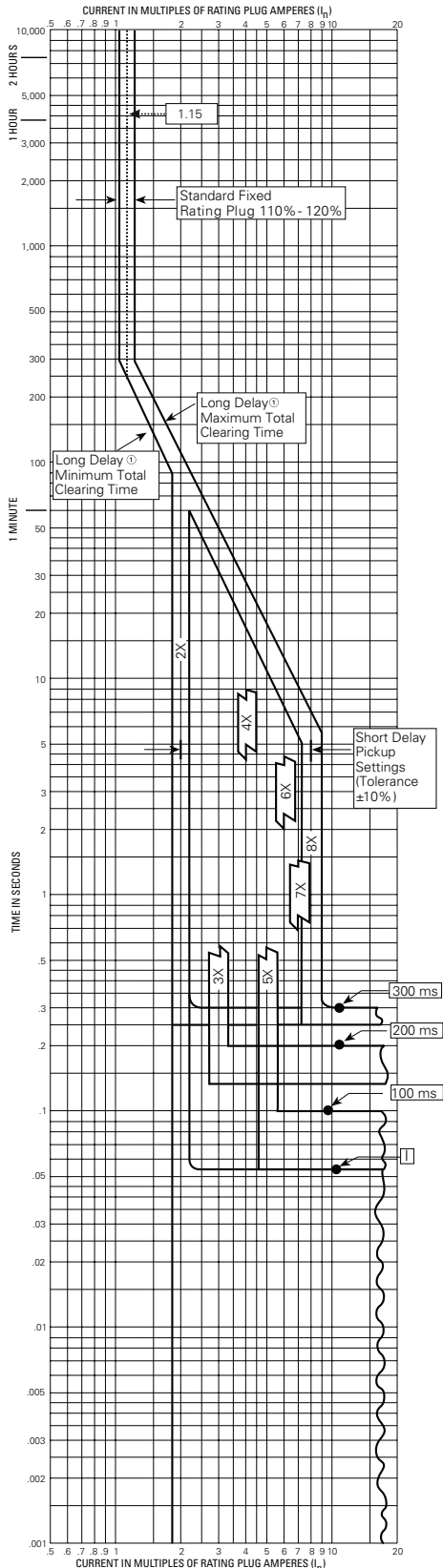
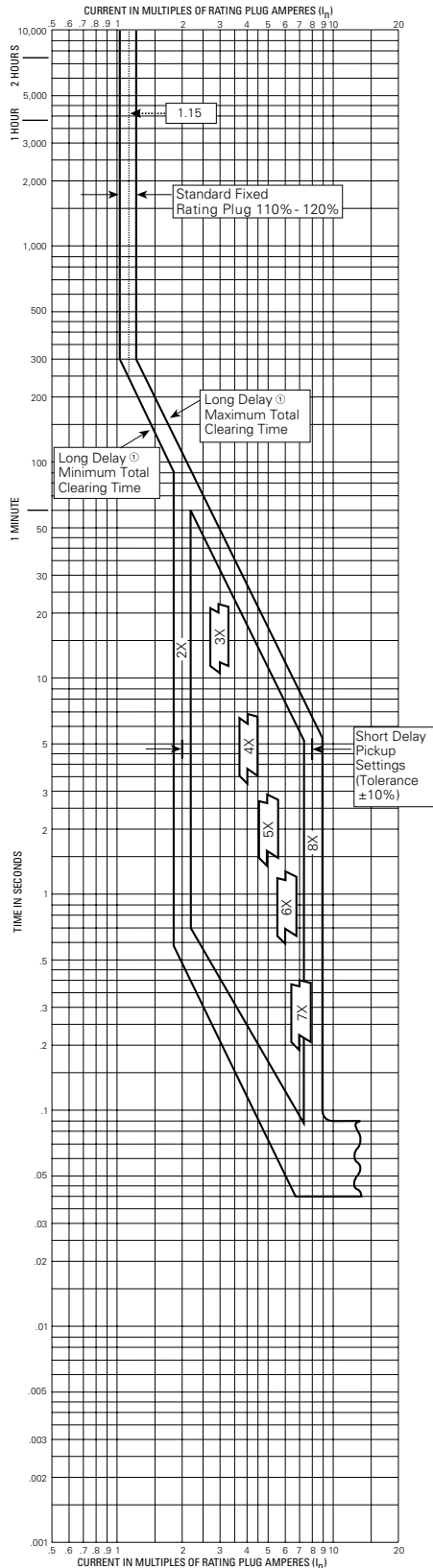


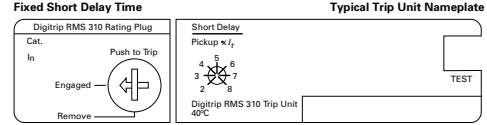
Figure 29. Series C Types KD, CKD, HKD, CHKD Circuit Breakers Equipped with Type KES Digitrip RMS 310 Trip Units, Types KES3250LSI, KES3250LSIG

- Legacy Product -



Digitrip 310 Circuit Breaker Time/Current Curves (Phase Current)
Series C K-Frame Circuit Breakers

Catalog Types: KES3125LS, KES3125LSG Digitrip RMS 310 Trip Units for use with Circuit Breaker Types: KDB, HKDB, CKDB, CHKDB, KD, HKD, CKD, and CHKD, 125A. max.



Available Rating Plugs

Ampere Rating (I_n)	Type	Catalog Number	Short Delay Pickup Range Amperes
125	Fixed	1KES 125T	250 - 1000
110	Fixed	1KES 110T	220 - 880
100	Fixed	1KES 100T	200 - 800
90	Fixed	1KES 90T	180 - 720
70	Fixed	1KES 70T	140 - 560
70, 90, 100, 125	Adjustable	A1KES 125T1	140- 1000

Interrupting Rating

Breaker Type	UL/CSA rms Sym. kA, 50/60 Hz		
	240V	480V	600V
KD, CKD, KDB, CKDB	65	35	25
HKD, CHKD, HKDB, CHKDB	100	65	35

Breaker Type	IEC 60947-2 rms Sym. kA, 50/60 Hz		
	240V	380V	415V
KD, CKD, KDB, CKDB	65	40	40
HKD, CHKD, HKDB, CHKDB	100	65	65
KDC	200	100	50

Notes:

I_r = Continuous Current Setting or Rating Plug Value, I_g = Current Sensor Frame Rating, I_n = Rating Plug Value, I_g = Unit of Ground Current, I_i = Instantaneous Override Pickup.

Digitrip RMS 310 trip units are suitable for functional field testing with test kit Cat. No. STK2. For field testing using primary injection methods, follow NEMA publication AB-4.

There is a memory effect that can act to shorten the long delay. The memory effect comes into play if a current above the long delay pickup value exists for a time and then is cleared by the tripping of a downstream device or the circuit breaker itself. A subsequent overload will cause the circuit breaker to trip in shorter time than normal. The amount of time delay reduction is inverse to the amount of time that has elapsed since the previous overload. Approximately five minutes is required between overloads to completely reset the memory.

- Curve accuracy applies from -20°C to $+55^{\circ}\text{C}$ ambient. For possible continuous ampere derating for ambient above 40°C , refer to Eaton.
- For high fault current levels, a fixed instantaneous override is provided at 3000A (Tolerance $\pm 15\%$).
- The end of the curve is determined by the interrupting rating of the circuit breaker. See above tabulation.
- Long Delay Pickup is 115% of I_n , $\pm 5\%$.

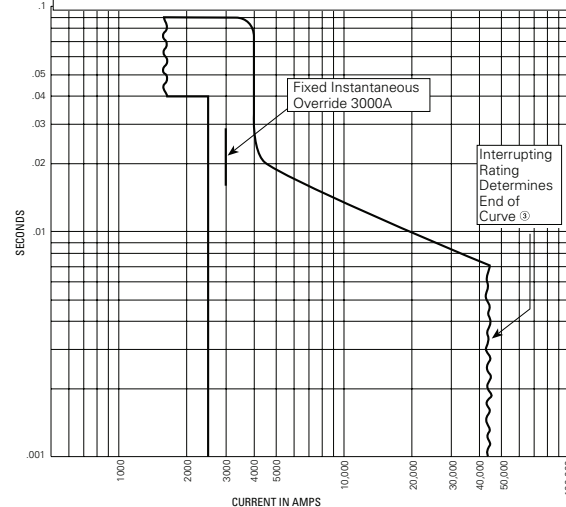
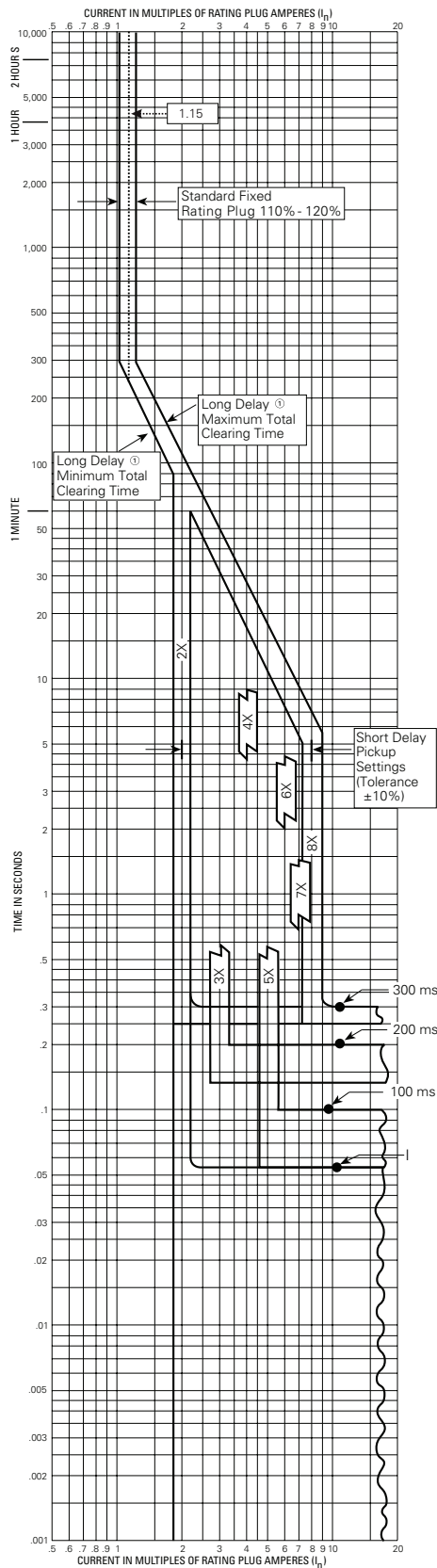


Figure 30. Series C Types KD, CKD, HKD, CHKD Circuit Breakers Equipped with Type KES Digitrip RMS 310 Trip Units, Types KES3125LS, KES3125LSG

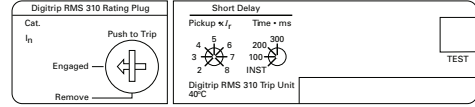
- Legacy Product -



Digitrip 310 Circuit Breaker Time/Current Curves (Phase Current)
Series C K-Frame Circuit Breakers

Catalog Types: KES3125LSI, KES3125LSIG Digitrip RMS 310 Trip Units for use with Circuit Breaker Types KDB, CKDB, HKDB, CHKDB, KD, HKD, CKD, and CHKD, 125A. max.

Adjustable Short Delay Time



Available Rating Plugs

Ampere Rating (I_n)	Type	Catalog Number	Short Delay Pickup Range Amperes
125	Fixed	1KES 125T	250 - 1000
110	Fixed	1KES 110T	220 - 880
100	Fixed	1KES 100T	200 - 800
90	Fixed	1KES 90T	180 - 720
70	Fixed	1KES 70T	140 - 560
70, 90, 100, 125	Adjustable	A1KES 125T1	140 - 1000

Interrupting Rating

Breaker Type	UL/CSA rms Sym. kA, 50/60 Hz		
	240V	480V	600V
KD, CKD, KDB, CKDB	65	35	25
HKD, CHKD, HKDB, CHKDB	100	65	35

Breaker Type	IEC 60947-2 rms Sym. kA, 50/60 Hz		
	240V	380V	415V
KD, CKD, KDB, CKDB	65	40	40
HKD, CHKD, HKDB, CHKDB	100	65	65

Notes:

I_r = Continuous Current Setting or Rating Plug Value, I_s = Current Sensor Frame Rating, I_n = Rating Plug Value, I_g = Unit of Ground Current, I_i = Instantaneous Override Pickup.

Digitrip RMS 310 trip units are suitable for functional field testing with test kit Cat. No. STK2. For field testing using primary injection methods, follow NEMA publication AB-4.

There is a memory effect that can act to shorten the long delay. The memory effect comes into play if a current above the long delay pickup value exists for a time and then is cleared by the tripping of a downstream device or the circuit breaker itself. A subsequent overload will cause the circuit breaker to trip in shorter time than normal. The amount of time delay reduction is inverse to the amount of time that has elapsed since the previous overload. Approximately five minutes is required between overloads to completely reset the memory.

- Curve accuracy applies from -20°C to $+55^{\circ}\text{C}$ ambient. For possible continuous ampere derating for ambient above 40°C , refer to Eaton.
- For high fault current levels, a fixed instantaneous override is provided at 3000A (Tolerance $\pm 15\%$).
- The end of the curve is determined by the interrupting rating of the circuit breaker. See above tabulation.
- Long Delay Pickup is 115% of I_n , $\pm 5\%$.

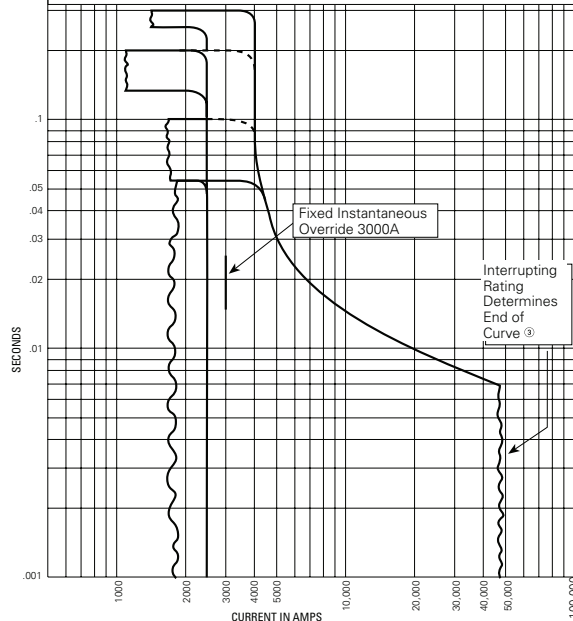


Figure 31. Series C Types KD, CKD, HKD, CHKD Circuit Breakers Equipped with Type KES Digitrip RMS 310 Trip Units, Types KES3125LSI, KES3125LSIG