

Table 3. MDL Breaker Assembly

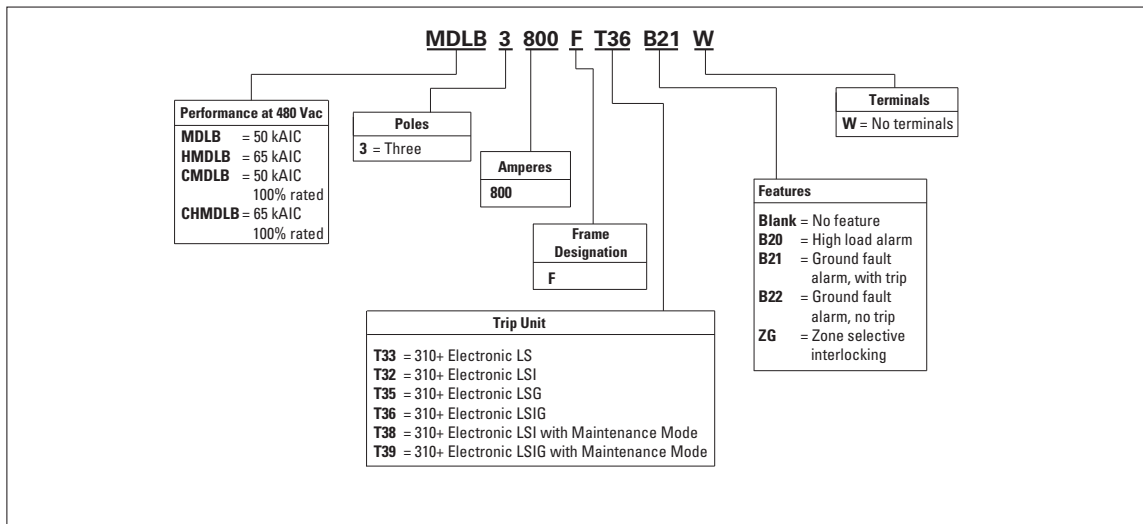


Table 4. MDL Electronic Trip Unit

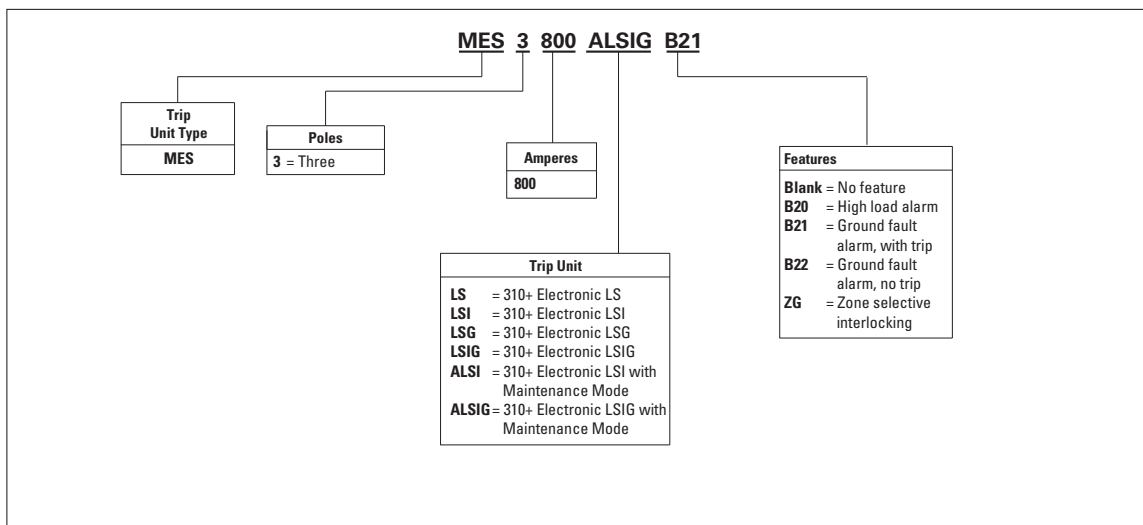
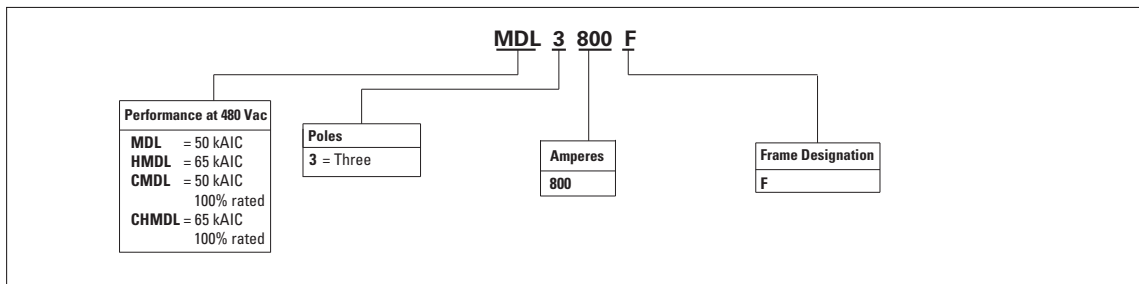


Table 5. MDL Frame Only



Note

- ① Maintenance Mode and ZSI are only available with LSI and LSIG trip units.
- ② B21 and B22 features available only with LSG, LSIG and ALSIG trip units.
- ③ B2x suffixes cannot be combined with other B2x suffixes.

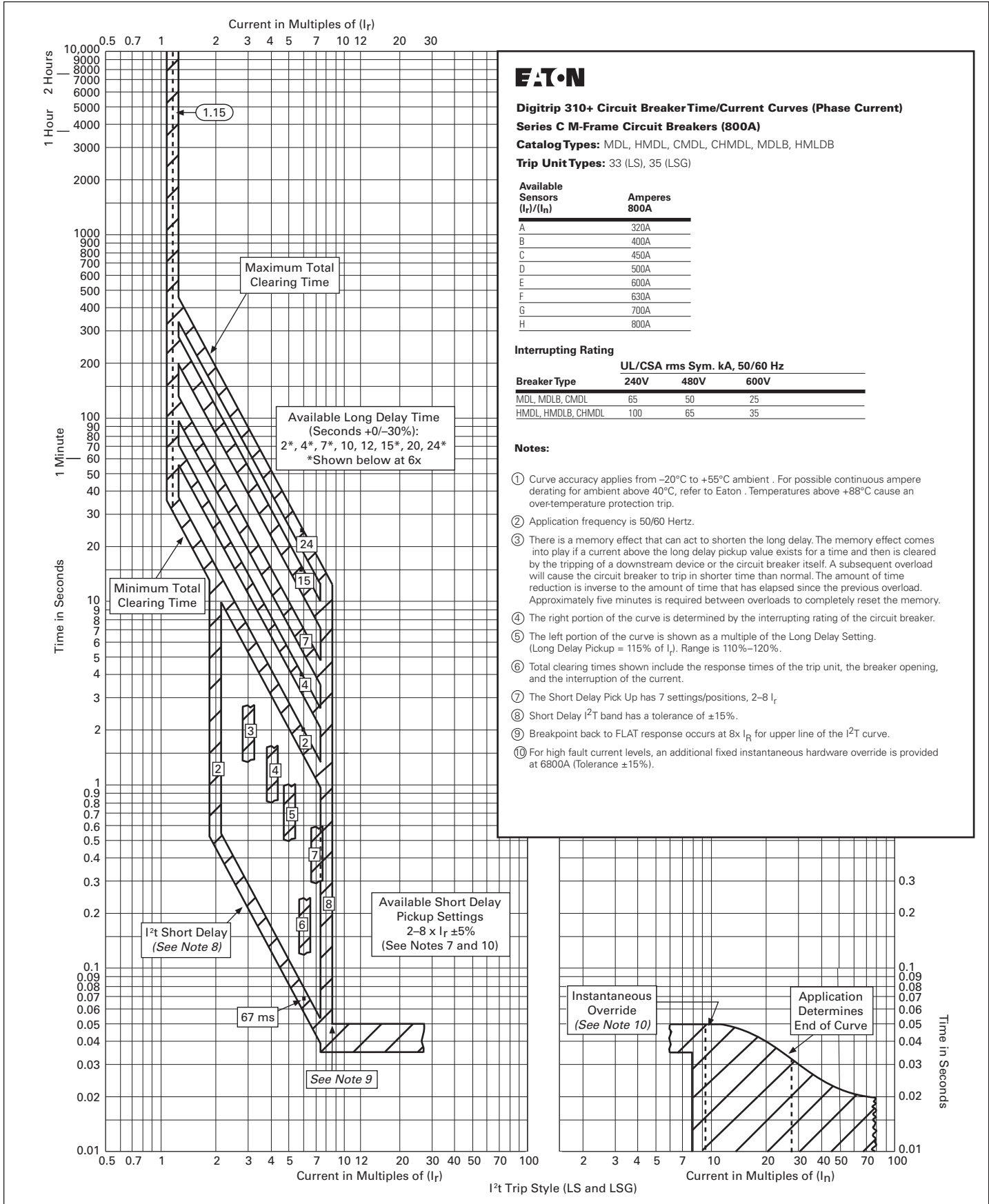
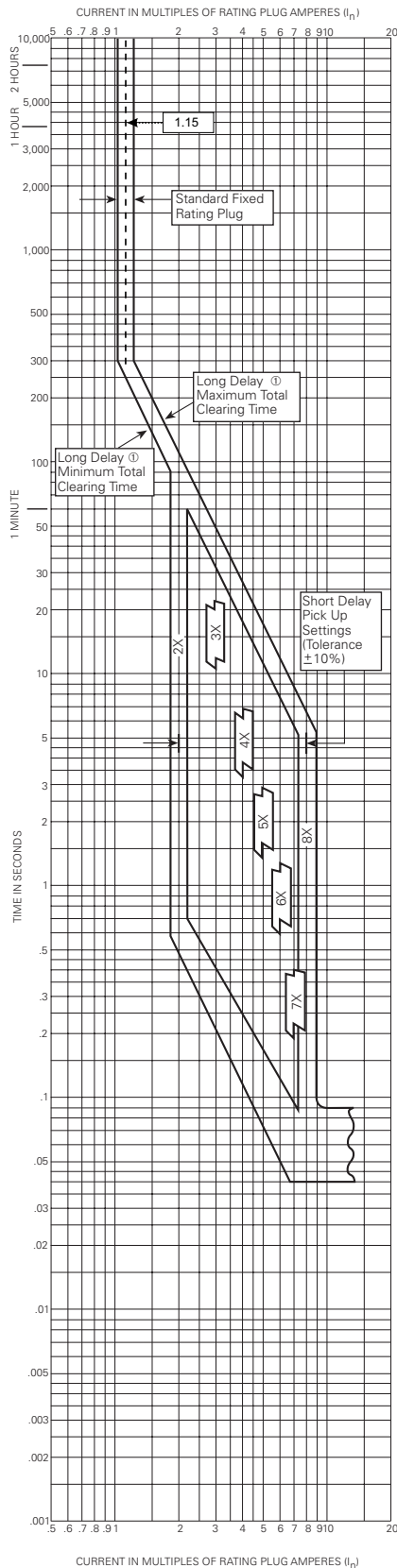


Figure 2. Digitrip 310+ Trip Units (800A), Long Delay Response and Short Delay with I²T Response Curve and Override (LS, LSG) - TD012051EN, October 2014

Types MDL, HMDL, CMDL, and CHMDL Equipped with Type MES Digitrip RMS 310 Trip Units, Types MES3800LS and MES3800LSG



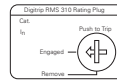
Circuit Breaker Time/Current Curves (Phase Current)

Series C M-Frame Circuit Breakers

Equipped With Type MES Digitrip RMS 310 Trip Units

Catalog Types: MES3800LS and MES3800LSG Digitrip RMS 310 Trip Units for use with Circuit Breaker Types MDL, HMDL, CMDL and CHMDL 3 Poles.

Fixed Short Delay Time



Typical Trip Unit Nameplate



Available Rating Plugs

Ampere Rating (I_n)	Type	Rating Plug Catalog Number	Short Delay Pickup Range Amperes
800	Fixed	8MES800T	1600-6400
700	Fixed	8MES700T	1400-5600
600	Fixed	8MES600T	1200-4800
500	Fixed	8MES500T	1000-4000
400	Fixed	8MES400T	800-3200
400, 500, 600, 800	Adjustable	A8MES800T1	800-6400

Interrupting Rating

Breaker Type	UL/CSA rms Sym. kA, 50/60 Hz		
	240V	480V	600V
MDL, MDLB, CMDL, CMDLB	65	50	25
HMDL, HMDLB, CHMDL, CHMDLB	100	65	35

Breaker Type	IEC 60947-2 rms Sym. kA, 50/60 Hz					
	240V		380/415V		690V	
	Icu	Ics	Icu	Ics	Icu	Ics
MDL, MDLB, CMDL, CMDLB	65	65	50	50	20	10
HMDL, HMDLB, CHMDL, CHMDLB	100	100	70	50	25	13

Utilization Category A

$$U_{IMP} = 8 \text{ kV}$$



Notes:

Digitrip RMS 310 trip units are suitable for functional field testing with test kit Cat. No. STK2. For field testing using primary injection methods, follow NEMA AB4 publications.

Calibration response in short delay pick-up range is same for 1, 2 or 3 poles in series.

There is a memory effect that can act to shorten the long delay. The memory effect comes into play if a current above the long delay pick-up value exists for a time and then is cleared by the tripping of a downstream device or the circuit breaker itself. A subsequent overload will cause the circuit breaker to trip in shorter time than normal. The amount of time delay reduction is inverse to the amount of time that has elapsed since the previous overload. Approximately five minutes is required between overloads to completely reset the memory.

- Curve accuracy applies from -20°C to $+55^{\circ}\text{C}$ ambient. For possible continuous ampere derating for ambient above 40°C , refer to Eaton.
- For high fault current levels a fixed instantaneous override is provided at 6800A. (Tolerance $\pm 15\%$).
- The end of the curve is determined by the interrupting rating of the circuit breaker. See above tabulation.
- Long delay pickup is 115% of I_n , $\pm 15\%$.

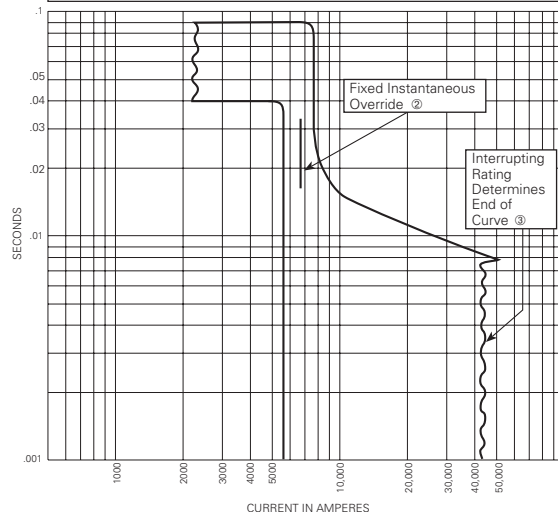


Figure 6. MDL, HMDL, CMDL, and CHMDL, types MES3800LS and MES3800LSG - Curve Number SC-7204-99, June 2007