

iQNNA1

- AC immune
- DC biased
- Twin relay
- Patent applied for
- SIL4

CONTACT LAYOUT :

Note : Layouts shown are when viewed from the rear of the relay.

	A	B	C	D	
1	+F	+F	+F	+F	
2	-F	-F	-F	-F	
3	+F	+F	+F	+F	
4	-F	-F	-F	-F	
5	-B	+F	+F	-B	
6	+B	-F	-F	+B	
7	-B	+F	+F	-B	
8	+B	-F	-F	+B	
R1	V ₁			V ₂	R2
R3	V ₁			V ₂	R4

2x(6F 2B)

	A	B	C	D	
1	+F	+F	+F	+F	
2	-F	-F	-F	-F	
3	+F	+F	+F	+F	
4	-F	-F	-F	-F	
5	-B	-B	-B	-B	
6	+B	+B	+B	+B	
7	-B	-B	-B	-B	
8	+B	+B	+B	+B	
R1	V ₁			V ₂	R2
R3	V ₁			V ₂	R4

2x(4F 4B)



Product code	Nominal working volt	Contacts	Max full operating volts	Minimum release volts	PIN code
iQNNA1-12F-4B-50	50	2x(6F 2B)	40V	7.5V	CEFHJ
iQNNA1-8F-8B-50	50	2x(4F 4B)	40V	7.5V	CEGHJ
iQNNA1-12F-4B-24	24	2x(6F 2B)	19.2V	3.6V	CDEHX
iQNNA1-8F-8B-24	24	2x(4F 4B)	19.2V	3.6V	ACDHJ