LSN50 & LSN50-V2 - LoRaWAN Sensor Node User Manual

last modified by Xiaoling

on 2022/06/29 14:24

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1. Introduction

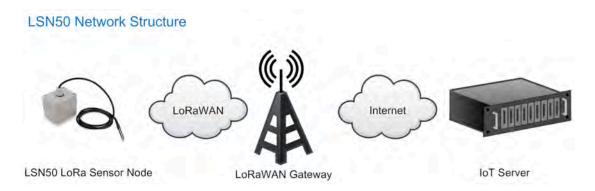
1.1 What is LSN50 LoRa Sensor Node

LSN50 is a Long Range LoRaWAN Sensor Node. It is designed for **outdoor data logging** and powered by Li/ SOCI2 battery for long term use and secure data transmission. It is designed to facilitate developers to quickly deploy industrial level LoRa and IoT solutions. It helps users to turn the idea into a practical application and make the Internet of Things a reality. It is easy to program, create and connect your things everywhere.

It is based on SX1276/SX1278 allows the user to send data and reach extremely long ranges at low data-rates. It provides ultra-long range spread spectrum communication and high interference immunity whilst minimizing current consumption. It targets professional wireless sensor network applications such as irrigation systems, smart metering, smart cities, smartphone detection, building automation, and so on.

LSN50 uses STM32l0x chip from ST, STML0x is the ultra-low-power STM32L072xx microcontrollers incorporate the connectivity power of the universal serial bus (USB 2.0 crystal-less) with the high-performance ARM® Cortex®-M0+ 32-bit RISC core operating at a 32 MHz frequency, a memory protection unit (MPU), high-speed embedded memories (192 Kbytes of Flash program memory, 6 Kbytes of data EEPROM and 20 Kbytes of RAM) plus an extensive range of enhanced I/Os and peripherals.

LSN50 is an **open source product**, it is based on the STM32Cube HAL drivers and lots of libraries can be found in ST site for rapid development.



1.2 Specifications

Micro Controller:

- STM32L072CZT6 MCU
- MCU: STM32L072CZT6
- Flash: 192KB
- RAM: 20KB
- EEPROM: 6KB
- Clock Speed: 32Mhz

Common DC Characteristics:

- Supply Voltage: 2.1v ~ 3.6v
- Operating Temperature: -40 ~ 85°C
- I/O pins: Refer to STM32L072 datasheet

LoRa Spec:

Frequency Range,
Band 1 (HF): 862 ~ 1020 Mhz

or

- Band 2 (LF): 410 ~ 528 Mhz
- 168 dB maximum link budget.
- +20 dBm 100 mW constant RF output vs.
- +14 dBm high efficiency PA.
- Programmable bit rate up to 300 kbps.
- High sensitivity: down to -148 dBm.
- Bullet-proof front end: IIP3 = -12.5 dBm.
- Excellent blocking immunity.
- · Low RX current of 10.3 mÅ, 200 nA register retention.
- · Fully integrated synthesizer with a resolution of 61 Hz.
- FSK, GFSK, MSK, GMSK, LoRaTM and OOK modulation.
- Built-in bit synchronizer for clock recovery.
- · Preamble detection.
- 127 dB Dynamic Range RSSI.
- · Automatic RF Sense and CAD with ultra-fast AFC.
- Packet engine up to 256 bytes with CRC.
- LoRaWAN 1.0.2 Specification

Battery:

- Li/SOCI2 un-chargeable battery
- · Capacity: 4000mAh
- Self Discharge: <1% / Year @ 25°C
- Max continuously current: 130mA
- Max boost current: 2A, 1 second

Power Consumption

- STOP Mode: 2.7uA @ 3.3v
- LoRa Transmit Mode: 125mA @ 20dBm 44mA @ 14dBm

1.3 Features

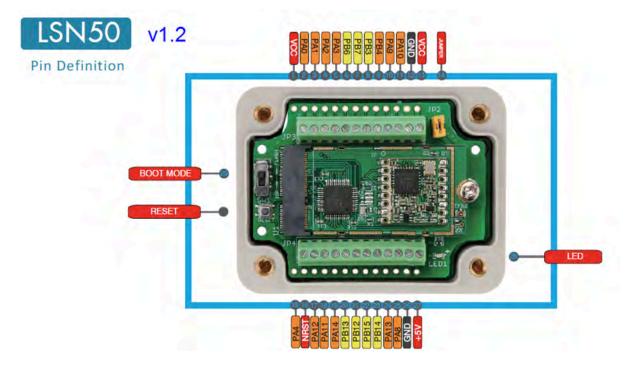
- LoRaWAN 1.0.3 Class A, Class C
- STM32L072CZT6 MCU
- SX1276/78 Wireless Chip
- Pre-load bootloader on USART1/USART2
- MDK-ARM Version 5.24a IDE
- I2C, LPUSART1, USB, SPI2
- 3x12bit ADC, 1x12bit DAC
- 20xDigital I/Os
- LoRa[™] Modem
- Preamble detection
- · Baud rate configurable
- CN470/EU433/KR920/US915/IN865
- EU868/AS923/AU915
- Open-source hardware / software
- Available Band:433/868/915/920 Mhz
- IP66 Waterproof Enclosure
- Ultra-Low Power consumption
- AT Commands to change parameters
- 4000mAh or 8500mAh Battery for long term use •

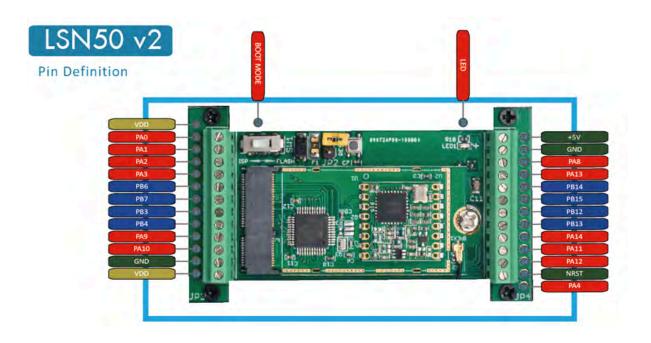
1.4 Applications

- Smart Buildings & Home Automation
- · Logistics and Supply Chain Management
- Smart Metering
- Smart Agriculture

- Smart CitiesSmart Factory

1.5 Pin Definitions and Switch





No.	Signal1	Direction	Function	Remark
1	VDD (3v)	OUTPUT	vcc	Directly connect to main power for board
2	PAO	In/Out	Directly from STM32 chip	Used as ADC in LSN50 image
3	PA1	In/Out	Directly from STM32 chip	
4	PA2	In/Out	Directly from STM32 chip, 10k pull up to VCC	Used as UART_TXD in LSN50 image
5	PA3	In/Out	Directly from STM32 chip, 10k pull up to VCC	Used as UART_RXD in LSN50 image
6	PB6	In/Out	Directly from STM32 chip, 10k pull up to VCC	
7	PB7	In/Out	Directly from STM32 chip, 10k pull up to VCC	
8	PB3	In/Out	Directly from STM32 chip, 10k pull up to VCC	
9	PB4	In/Out	Directly from STM32 chip	
10	PA9	In/Out	Directly from STM32 chip, 10k pull up to VCC	
11	PA10	In/Out	Directly from STM32 chip, 10k pull up to VCC	
12	GND		Ground	
13	VCC(2.9V)	OUTPUT	VCC	Directly connect to main power for board
14	Jumper		Power on/off jumper	
15	PA4	In/Out	Directly from STM32 chip	
16	NRST	In	Reset MCU	
17	PA12	In/Out	Directly from STM32 chip	
18	PA11	In/Out	Directly from STM32 chip	
19	PA14	In/Out	Directly from STM32 chip	
20	PB13	In/Out	Directly from STM32 chip	
21	PB12	In/Out	Directly from STM32 chip	
22	PB15	In/Out	Directly from STM32 chip	
23	PB14	In/Out	Directly from STM32 chip	
24	PA13	In/Out	Directly from STM32 chip	
25	PA8	In/Out	Directly from STM32 chip	Default use to turn on/off LED1 in LSN50 image
26	GND		Ground	
27	+5V	Out	5v output power	Controlled by PB5(Low to Enable, High to Disable)
				Continuous output : max 600mA
				Pulse output : max 1A
28	LED1		Controlled by PA8	Blink on transmit
29	BOOT MODE/ SW1		Configure device in working mode or ISP program mode	Flash: Normal Working mode and send AT Commands
				ISP: UART Program Mode
30	NRST	In	Reset MCU	

1.5.1 Jumper JP2

Power on Device when put this jumper.

1.5.2 BOOT MODE / SW1

1. ISP: upgrade mode, device won't have any signal in this mode. but ready for upgrade firmware. LED won't work. Firmware won't run.

2. Flash: work mode, device starts to work and send out console output for further debug

1.5.3 Reset Button

Press to reboot the device.

1.5.4 LED

It will flash:

- 1. When boot the device in flash mode
- 2. Send an uplink packet

1.6 Hardware Change log

Note: Hardware version is marked in the PCB.

LSN50 v2.1:

- 1. Change R14 to 1M ohm
- 2. Change R3, R4 to 4.7Kohm. So no need to modify them for 3 DS18B20 connection.
- 3. Add ESD to each I/O

LSN50 v2.0:

· Change to a new enclosure. Improve with external antenna, IP68, ear hook.

LSN50 v1.3:

· Add P-MOS to control 5V output

LSN50 v1.2:

- · Add LED. Turn on for every LoRa transmit
- Add pin PA4, PB13, NRST
- Add 5V Output, on/off control by PB5(Low to Enable, High to Disable)

1.7 Hole Option

The LSN50 provides different hole size options for different size sensor cable. The options provided are M12, M16 and M20. The definition is as below:



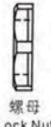


多项国际质量认证



抗拉力 韧性强 5





Lock Nut

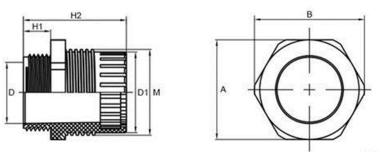
夹紧爪 主体 Main unit Claw



夹紧圈 Seal



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单位: mm

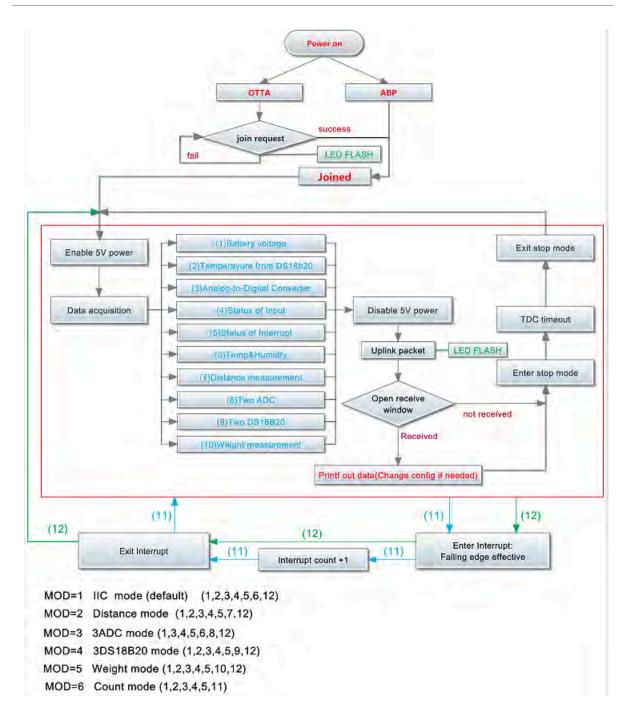
MODEL	HI	H2	М	M1	D	D1	А	В
M12+1.5	8	28. 3	12.0	12.0	10.4	8.5±0.2	16 ± 0. 2	18 ± 0. 2
M16+1.5	8	30.7	15.1	16.0	13.5	10.9±0.2	18.8±0.2	20.6±0.2
M20+1.5	9	34.0	20.2	20.0	18.7	16. 2 ± 0. 2	22.8 ± 0.2	25. 2 ± 0. 2

2. Use LSN50 with LoRaWAN firmware

2.1 How it works

The LSN50 is pre-loaded with a firmware and is configured as LoRaWAN OTAA Class A mode by default. It has OTAA keys to join LoRaWAN network. To connect a local LoRaWAN network, you just need to input the OTAA keys in the LoRaWAN IoT server and power on the LSN50. It will automatically join the network via OTAA.

The diagram below shows the working flow in default firmware (ver 1.7.0):



In case you can't set the OTAA keys in the LoRaWAN OTAA server, and you have to use the keys from the server, you can <u>use AT Commands</u> to set the keys in the LSN50.

2.2 Quick guide to connect to LoRaWAN server (OTAA)

Following is an example for how to join the <u>TTN V3 LoRaWAN Network</u>. Below is the network structure; we use the <u>LG308</u> as a LoRaWAN gateway in this example.



The LG308 is already set to connected to $\underline{\text{TTN V3 network}}$, so what we need to now is configure the TTN V3 server.

Step 1: Create a device in TTN with the OTAA keys from LSN50.

Each LSN50is shipped with a sticker with the default device EUI as below:



You can enter this key in the LoRaWAN Server portal. Below is TTN screen shot:

Register the device:

From The LoRaWAN Device Repository Manual	v and a second se		
Preparation			
Activation mode*			
Over the air activation (OTAA)			
Activation by personalization (ABP)			
Multicast			
Do not configure activation			
LoRaWAN version ⑦			
MAC V1.0.3	v 🔶	1	
Network Server address			
eu1.cloud.thethings.network			
and a set of the set			
Application Server address			
eu1.cloud.thethings.network			
External Join Server 🗇			
Enabled			
Join Server address			
eu1.cloud.thethings.network			

Add APP EUI and DEV EUI:

Register end device

Basic settings End device ID's, Name and Description	 Network layer settings Frequency plan, regional parameters, end device class and session keys. 	3 Join settings Root keys, NetID and kek labels.	
nd device ID ⑦ *			
snpk01			
ספבטו⊙* •••••••••••••••••••••••••••••••••••	99		
evEUI⊘*			
nd device name			
LSNPK01			
nd device description			
Description for my new end device			
ntional and dovice description: can a	⊿ Iso be used to save notes about the end device	2	

Add APP EUI in the application:

From The LoRaWAN Device Repository	Manually	
Basic settings End device ID's, Name and Description	2 Network layer settings Frequency plan, regional parameters, end device class and session keys.	3 Join settings Root keys, NetID and kek labels.
Frequency plan ⑦*		
Europe 863-870 MHz (SF12 for RX2)		
LoRaWAN version ② *		
MAC V1.0.3	\sim	
Regional Parameters version 🔊 *		
PHY V1.0.3 REV A	\sim	
LoRaWAN class capabilities 🔊		
Supports class B		
Supports class C		
Advanced settings 🗸		
< Basic settings		Join settings >

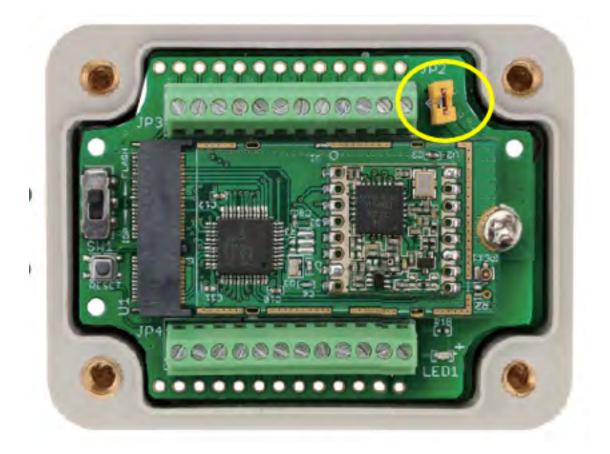
Add APP KEY

Register end device

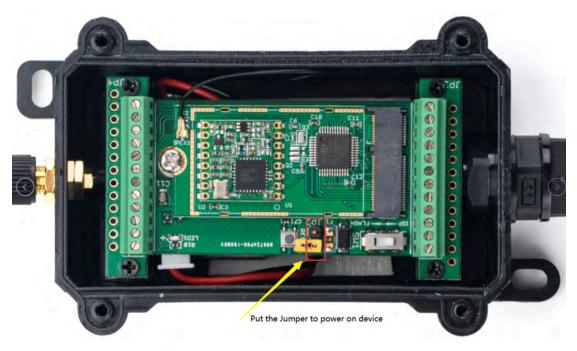
From The LoRaWAN Device Repository	Manually	
Basic settings End device ID's, Name and Description	Network layer settings Frequency plan, regional parameters, end device class and session keys.	- 3 Join settings Root keys, NetiD and kek labels.
Root keys		
АррКеу 🗇 *		
BD 72 1D AC F3 CC AB 67 72 8	D 7A F5 4D DF 30 8B 0	
Advanced settings 🗸		
< Network layer settings		Add end device

Step 2: Power on LSN50

Put a Jumper on JP2 to power on the device.



For LSn50v2:



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Step 3: The LSN50 will auto join to the TTN V3 network. After join success, it will start to upload messages to TTN V3 and you can see the messages in the panel.

	Data previ	ew					11 Pause	Clear
.1		26 08 D9 A5		d: 94 E9 CF 20 C6 C6 A4 94 5E 6 : -8.1, Work_mode: 110 }		RSSI: -110 Bandwidth		ddth: 125008
	• DevAddr:	26 08 D9 A5	,	Battery info	111	SHT2	0 or SHT31	
5	DevAddr:	26 08 D9 A5	FCot: 14	FPort: 2 MAC paymerat	EUCP20 ADCA4 94 5E 6F AF	Temperature ≋(SHT2020≉S⊧ out	(〒:3-2)5 RSSI: -110	8 Raw payloa
	DevAddr:	26 08 D9 A5			and			
	DevAdd1:	26 08 D9 A5	FCnt: 14	FPort: 2 MAC payload: 94	E9 CF 20 C6 C6 ADigital Ant	terrupth: 125000 s	NR: -3.5 RSSI: -11	9 Raw payloa
	*			m				

2.3 Working Mode & Uplink Payload

LSN50 has different working mode for the connections of different type of sensors. This section describes these modes. Use can use the AT Command AT+MOD to set LSN50 to different working modes.

For example:

AT+MOD=2 // will set the LSN50 to work in MOD=2 distance mode which target to measure distance via Ultrasonic Sensor.

NOTE:

1. Some working modes has payload more than 12 bytes, The US915/AU915/AS923 frequency bands' definition has maximum 11 bytes in **DR0**. Server sides will see NULL payload while LSn50 transmit in DR0 with 12 bytes payload.

2. All modes share the same Payload Explanation from HERE.

3. By default, the device will send an uplink message every 5 minutes.

2.3.1 MOD=1 (Default Mode)

In this mode, uplink payload includes in total 11 bytes. Uplink packets use FPORT=2.

Size(bytes)	2	2	2	1	2	2
Value	BAT	Temperature(DS18B20)	ADC	Digital in & Digital Interrupt	Temperature(SHT20 or SHT31 or Ultrasonic Sensor)	Humidity(SHT20)

	Data preview		II Pause	📋 Clear
D		MAC payload: 94 E9 CF 20 C6 C6 A4 94 5E 6F AF FPort: 2 SNR: -3.5 RSSI: -110 Bandwidth: 125000 TempC SHT: -0.1, Work mode: "IIC" } 08 44 0C CC 00 00 00 FF		
	6553.5, TempC1: 327.6,	SHT20 or S		idth: 12500
	DevAddr: 26 0B D9 A5 DevAddr: 26 0B D9 A5	Battery info FCnt: 14 FPort: 2 MAC payload: 74 EV CP 20 ADQ 4 94 5E 6F AF Ba(SHT202000SHT31)5	RSSI: -11	9 Raw pavl
э	•	(DS18B20) Digital input		
	DevAddr: 26 0B D9 A5	and FCnt: 14 FPort: 2 MAC payload: 94 E9 CF 20 C6 C6 ADigital Interrupth: 125000 SNR: -3.5		
	DevAddr: 26 0B D9 A5	FCht: 14 FPort: 2 MAC payload: 94 E9 CF 20 C6 C6 A#99202 6F A# 'S Canon poth: 1250000 SNR: -3.5	R221: -110	<pre>/ Raw payle</pre>

2.3.2 MOD=2 (Distance Mode)

This mode is target to measure the distance. The payload of this mode is totally 11 bytes. The 8^{th} and 9^{th} bytes is for the distance.

Size(bytes)	2	2	2	1	2	2
Value	BAT	Temperature(DS18B20)	ADC	Digital in & Digital Interrupt	Distance measure by:	Reserved
					1) LIDAR-Lite V3HP	
					Or	
					2) Ultrasonic Sensor	

	Overview Live data Messa	ging Location Payl	oad formatters Claiming	General settings	Digital Input and Digital Interrupt	
Time	Туре	Data preview			•	II Pause 📓 Cl
18:48:85	Forward data message to Applic	DeyAddr: 26 0B 9B AB	MAC payload: 6E FB 49	3C 03 54 7D 3D 72 AD A7	FPort: 2 INR: -1 RSSI: -189	Bandwidth: 125000
↑ 18:48:06	Forward uplink data message	EXTI_Trigger: *FALSE	, TempC1: 8, Work_mode:	" Distance" } 0A E0 00	9 08 98 C2 94 9D 78 FF FF FFOR	Reserved 189 Bandwidth: 125
↑ 18:48:06	Receive uplink data message	DevAddr: 26 08 98 AB	Battery Info	/ /		
↑ 18:48:06	Successfully processed data me	DevAddr: 26 08 98 AB	FPort: 2 MAC payload:	6E FB 49 3C 03 54 75 3D		SNR: -1 RSSI: -109 Raw payload: 40 AB
↑ 18:48:85	Receive data message	DevAddr: 26 0B 9B AB	FPort: 2 MAC payload:	6Е FB (Ю\$18820) 0 т	72 AD A7 ADC width: 125800	SNR: -1 RSSI: -109 Raw payload: 40 AB
↑ 18:47:59	Forward join-accept to Applica	DevAddr: 26 08 98 AB				
↑ 18:47:59	Forward join-accept message	DevAddr: 26 0B 9B AB				
↑ 18:47:59	Receive join-accept message	DevAddr: 26 08 98 AB				
↓ 18:47:59	Successfully scheduled ioin-ac_	DevAddr: 26 08 9C A5				

Connection of LIDAR-Lite V3HP:



Connection to Ultrasonic Sensor:

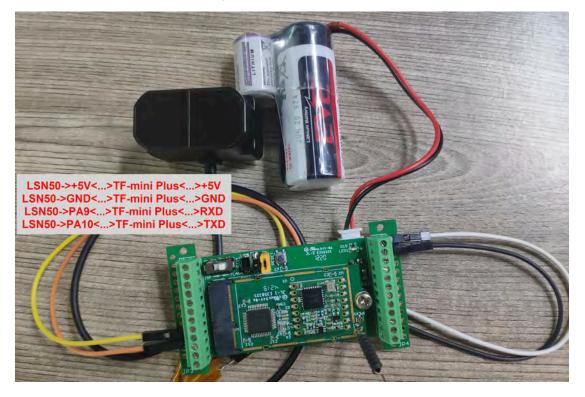


For the connection to TF-Mini or TF-Luna , MOD2 payload is as below:

Size(byte	s) 2	2	1	2	2	2
Value	BAT	Temperature(DS18B20)	Digital in & Digital Interrupt	ADC	Distance measure by: 1)TF-Mini plus LiDAR Or 2) TF-Luna LiDAR	Distance signal strength
DevAddr:	26 08 98 40			_	5.5 RSSI: -134 Bandwidth	Distance
_status:	"OPEN", EXT	I_Trigger: "FALSE", TempC1	: 0, Work_mode: " Distance" }	0C BF 90 0	00 01 00 04 DE 28 FF FFFF	
_status:	"OPEN", EXT		ery Info	OC BF DO G		signal
	"OPEN", EXT. 26 0B 98 4C	Batt	ery Info	/	10 91 90 94 9E 2E FF FF _ EE	
4		Batt		ADC	gital Input	signal strength
∢ DevAddr;	26 0B 98 4C	Batt	ery Info Temperature	ADC		signal strength
<pre> Addr: DevAddr: DevAddr: </pre>	26 0B 98 4C 26 0B 98 4C	Batt	ery Info Temperature	ADC Dia	gital Input Dista	signal strength
<pre>devAddr: DevAddr: DevAddr:</pre>	26 08 98 4C 26 08 98 4C 26 08 98 4C	Batt Rxi Delay: 5	ery Info Temperature	ADC Dia Dig	igital Input Dista and	signal strength ance

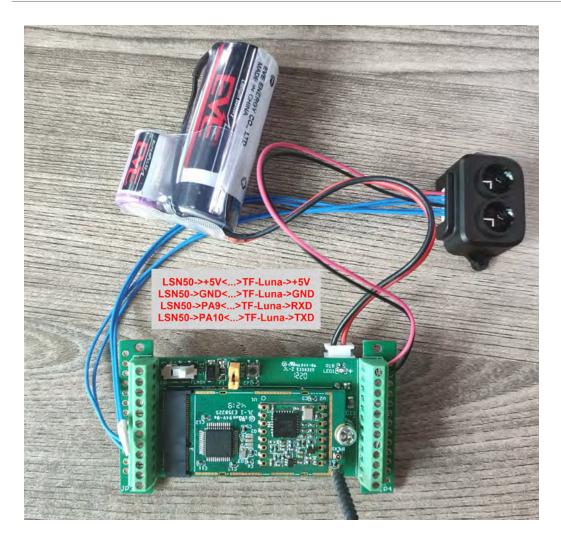
Connection to **TF-Mini plus** LiDAR(UART version):

Need to remove R3 and R4 resistors to get low power. Since firmware v1.7.0



Connection to TF-Luna LiDAR (UART version):

Need to remove R3 and R4 resistors to get low power. Since firmware v1.7.0



Please use firmware version > 1.6.5 when use MOD=2, in this firmware version, user can use LSn50 v1 to power the ultrasonic sensor directly and with low power consumption.

2.3.3 MOD=3 (3 ADC + I2C)

This mode has total 12 bytes. Include 3 x ADC + 1x I2C

Size(bytes)	2	2	2	1	2	2	1
Value	ADC1(Pin PA0)	ADC2(PA1)	ADC3(PA4)	Digital in & Digital Interrupt	Temperature(SHT20 or SHT31)	Humidity(SHT20 or SHT31)	BAT

↔ 18:56:18	Link ADR request enqueued	DevAddr:	26 68 89 66			igital Input		
↓ 18:56:18	Successfully scheduled data do.	DevAddr:	26 0B 89 00			and	Temperature	
18:56:18	Schedule data downlink for tra_	DevAddr:	26 0B 89 00	Rx1 Delay: 5	D	igital Interrupt	(SHT20 or SHT31)	
18:56:18	Forward data message to Applic_	DevAddr:	26 0B 89 00	MAC payload: 98 89 12	65 74 27 28 60 64 E7 D8 AB	FPort: 2 SNR: 0.2	RSSI: -107 Bandwidth: 125000	
↑ 18:56:18	Forward uplink data message	"FALSE",	Hum_SHT: 59.	2, TempC_SHT: 28.7, Work	_mode: " 3ADC" } 00 DE	8 91 97 10 C1 88 91 1F 12 1	Battery Infor: -107 Bandw	idth D
↑ 18:56:18	Receive uplink data message	DevAddr:	26 0B 89 00		ADC1 ADC2		Humidity	
18:56:18	Successfully processed data me_	DevAddr:	26 0B 89 00	FPort: 2 MAC payload:		64 E7 DADC3 andwidth	(SHT20 or SHT31)	48
18:56:18	Receive data message	DevAddr:	26 08 89 00	FPort: 2 MAC payload:	98 89 12 65 74 27 28 60	64 E7 D8 AB Bandwidth	: 125000 SNR: -0.2 RSSI: -107 Raw payload:	40
18:56:12	Forward join-accept to Applica	DevAddr:	26 0B 89 00					
18:56:12	Forward join-accept message	DevAddr:	26 8B 89 88					

2.3.4 MOD=4 (3 x DS18B20)

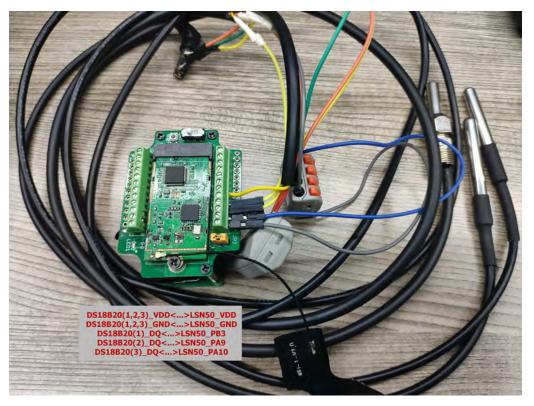
This mode is supported in firmware version since v1.6.1. Software set to AT+MOD=4

Hardware connection is as below,

(Note:

- In hardware version v1.x and v2.0, R3 & R4 should change from 10k to 4.7k ohm to support the other 2 x DS18B20 probes.
- In hardware version v2.1 no need to change R3 , R4, by default, they are 4.7k ohm already.

See here for hardware changelog.)



This mode has total 11 bytes. As shown below:

2	2	2	1	2	2
BAT	Temperature1	ADC	Digital in & Digital Interrupt	Temperature2	Temperature3
	(DS18B20)			(DS18B20)	(DS18B20)
	(PB3)			(PA9)	(PA10)
	2 ВАТ	(DS18B20)	(DS18B20)	(DS18B20)	(DS18B20) (DS18B20)

DevAddr:	26 08 DE BC				Digital Inp	ut				
DevAddr:	26 ØB DE BC					errupt				
DevAddr:	26 08 DE BC	Rx1 Delay: 5		(0010010)						
DevAddr:	26 OB DE BC	MAC payload:	13 4F 40 6	ED C8 AB 77 E8 53 68 28	FPort: 2 SNR: 0.	5 RSSI: -1	08 Bandwi)) dth: 125000		
"FALSE",	TempC1: 28.2	, Weight: 283,	Work_mode	e: "Weight" } 0D 93 01 1	A 01 00 90 01 18 01	17 FPor	t: 2 SNR:	0.5 RSSI:	-108 Bandwidth	h: 125000
+				1	1		III	-		
DevAddr:	26 OB DE BC		Ba	ttery Info	ADC		Temp	erature	3	
DevAddr:	26 0B DE BC	FPort: 2 MAC	payload:	13 4F 40 ED C8 AB 77 E8 5		th: 125000	SNR (D.S.	18B20)08	Raw payload:	40 BC DE
				m						
DevAddr:	26 OB DE BC	FPort: 2 MAC	payload:	13 4F 40 ED C8 AB 77 E8 5	3 68 28 Bandwid	th: 125000	SNR: 0.5	RSSI: -108	Raw payload:	40 BC DE
+				m		-				
DevAdd1:	26 0B DE BC									
DevAddr:	26 0B DE BC									
	DevAddr: DevAddr: OevAddr: *FALSE*, DevAddr: DevAddr: EevAddr: *	DevAddr: 26 08 DE BC OevAddr: 26 08 DE BC "FALSE", TempC1: 28.2 DevAddr: 26 08 DE BC DevAddr: 26 08 DE BC devAddr: 26 08 DE BC devAddr: 26 08 DE BC devAddr: 26 08 DE BC	DevAddr: 26 0B DE BC DevAddr: 26 0B DE BC Rx1 Delay: 5 DevAddr: 26 0B DE BC MAC payload: "FALSE", TempC1: 28.2, Weight: 283, 4 DevAddr: 26 0B DE BC DevAddr: 26 0B DE BC FPort: 2 MAC 4 DevAddr: 26 0B DE BC FPort: 2 MAC 4	DevAddr: 26 0B DE BC DevAddr: 26 0B DE BC Rx1 Delay: 5 DevAddr: 26 0B DE BC MAC payload: 13 4F 40 "FALSE", TempC1: 28.2, Weight: 283, Work_mode 4 DevAddr: 26 0B DE BC BC DevAddr: 26 0B DE BC FPort: 2 MAC payload: 4 DevAddr: 26 0B DE BC FPort: 2 MAC payload: 4	DevAddr: 26 0B DE BC Rx1 Delay: 5 DevAddr: 26 0B DE BC Rx1 Delay: 5 DevAddr: 26 0B DE BC MAC payload: 13 4F 40 ED C8 AB 77 E8 53 68 28 "FALSE", TempC1: 28.2, Weight: 283, Work_mode: "Weight" } @D 93 01 1 4 DevAddr: 26 0B DE BC FPort: 2 MAC payload: 13 4F 40 ED C8 AB 77 E8 5 4 DevAddr: 26 0B DE BC FPort: 2 MAC payload: 13 4F 40 ED C8 AB 77 E8 5 4 DevAddr: 26 0B DE BC FPort: 2 MAC payload: 13 4F 40 ED C8 AB 77 E8 5 4 DevAddr: 26 0B DE BC FPort: 2 MAC payload: 13 4F 40 ED C8 AB 77 E8 5 4 DevAddr: 26 0B DE BC FPort: 2 MAC payload: 13 4F 40 ED C8 AB 77 E8 5 4 DevAddr: 26 0B DE BC FPort: 2 MAC payload: 13 4F 40 ED C8 AB 77 E8 5 4 DevAddr: 26 0B DE BC FPort: 2 MAC payload: 13 4F 40 ED C8 AB 77 E8 5 4 DevAddr: 26 0B DE BC FPort: 2 MAC payload: 13 4F 40 ED C8 AB 77 E8 5 4 DevAddr: 26 0B DE BC FPort: 2 MAC payload: 13 4F 40 ED C8 AB 77 E8 5 4 DevAddr: 26 0B DE BC FPort: 2 MAC payload: 13 4F 40 ED C8 AB 77 E8 5 4 DevAddr: 26 0B DE BC FPort: 2 MAC payload: 13 4F 40 ED C8 AB 77 E8 5 4 DevAddr: 26 0B DE BC FPort: 2 MAC payload: 13 4F 40 ED C8 AB 77 E8 5 4 DevAddr: 26 0B DE BC FPort: 2 MAC payload: 13 4F 40 ED C8 AB 77 E8 5 4 DevAddr: 26 0B DE BC FPort: 2 MAC payload: 13 4F 40 ED C8 AB 77 E8 5 4 DevAddr: 26 0B DE BC FPort: 2 MAC payload: 13 4F 40 ED C8 AB 77 E8 5 4 DevAddr: 26 0B DE BC FPort: 2 MAC payload: 13 4F 40 ED C8 AB 77 E8 5 4 DevAddr: 26 0B DE BC FPort: 2 MAC payload: 13 4F 40 ED C8 AB 77 E8 5 4 DevAddr: 26 0B DE BC FPort: 2 MAC payload: 13 4F 40 ED C8 AB 77 E8 5 4 DevAddr: 26 0B DE BC FPORT: 2 MAC payload: 13 4F 40 ED C8 AB 77 E8 5 4 DevAddr: 26 0B DE BC FPORT: 2 MAC payload: 13 4F 40 ED C8 AB 77 E8 5 4 DevAddr: 26 0B DE BC FPORT: 2 MAC payload: 13 4F 40 ED C8 AB 77 E8 5 4 DevAddr: 26 0B DE BC FPORT: 2 MAC payload: 13 4F 40 ED C8 AB 77 E8 5 4 DevAddr: 26 0B DE BC FPORT: 2 MAC payload: 13 4F 40 ED C8 AB 77 E8 5 4 DevAddr: 26 0B DE BC FPORT 20 5 4 DevAddr: 26	DevAddr: 26 0B DE BC Rx1 Delay: 5 DevAddr: 26 0B DE BC Rx1 Delay: 5 DevAddr: 26 0B DE BC MAC payload: 13 4F 40 ED C8 AB 77 E8 53 68 28 FPort: 2 SNR "FALSE", TempC1: 28.2, Weight: 283, Work_mode: "Weight" } 00 93 01 1A 1 00 90 01 1E 01 "PALSE", TempC1: 28.2, Weight: 283, Work_mode: "Weight" } 00 93 01 1A 1 00 90 01 1E 01 "PALSE", TempC1: 28.2, Weight: 283, Work_mode: "Weight" } 00 93 01 1A 1 00 90 01 1E 01 "PALSE", TempC1: 28.2, Weight: 283, Work_mode: "Weight" } 00 93 01 1A 1 00 90 01 1E 01 "PALSE", TempC1: 28.2, Weight: 283, Work_mode: "Weight" } 00 93 01 1A 1 00 90 01 1E 01 "PALSE", TempC1: 28.2, Weight: 283, Work_mode: "Weight" } 00 93 01 1A 1 00 90 01 1E 01 "PALSE", TempC1: 28.2, Weight: 283, Work_mode: "Weight" } 00 93 01 1A 1 00 90 01 1E 01 "PALSE", TempC1: 28.2, Weight: 283, Work_mode: "Weight" } 00 93 01 1A 1 00 90 01 1E 01 "PALSE", TempC1: 28.2, Weight: 283, Work_mode: "Weight" } 00 93 01 1A 1 00 90 01 1E 01 "PALSE", TempC1: 28.2, Weight: 283, Work_mode: "Weight" } 00 93 01 1A 1 00 90 01 1E 01 "PALSE", TempC1: 28.2, Weight: 283, Work_mode: "Weight" } 00 93 01 1A 10 00 90 01 1E 01 "PALSE", TempC1: 26 08 DE BC FPort: 2 MAC payload: 13 4F 40 ED C8 AB 77 E8 53 68 28 Bandwid "PALSE", TempC1: 26 08 DE BC FPort: 2 MAC payload: 13 4F 40 ED C8 AB 77 E8 53 68 28 Bandwid "PALSE", TempC1: 26 08 DE BC FPort: 2 MAC payload: 13 4F 40 ED C8 AB 77 E8 53 68 28 Bandwid "PALSE", TempC1: 26 08 DE BC FPORT: 2 MAC payload: 13 4F 40 ED C8 AB 77 E8 53 68 28 Bandwid "PALSE", TempC1: 26 08 DE BC FPORT: 2 MAC payload: 13 4F 40 ED C8 AB 77 E8 53 68 28 Bandwid "PALSE", TempC1: 26 08 DE BC FPORT: 2 MAC payload: 13 4F 40 ED C8 AB 77 E8 53 68 28 Bandwid "PALSE", TempC1: 26 08 DE BC FPORT: 2 MAC payload: 13 4F 40 ED C8 AB 77 E8 53 68 28 Bandwid	DevAddr: 26 0B DE BC Rx1 Delay: 5 DevAddr: 26 0B DE BC Rx1 Delay: 5 DevAddr: 26 0B DE BC MAC payload: 13 4F 40 ED C8 AB 77 E8 53 68 28 "FALSE", TempC1: 28.2, Weight: 283, Work_mode: "Weight" } 0D 93 01 1A 1 00 99 01 1B 117 PevAddr: 26 0B DE BC BC FPort: 2 MAC payload: 13 4F 40 ED C8 AB 77 E8 53 68 28 DevAddr: 26 0B DE BC FPort: 2 MAC payload: 13 4F 40 ED C8 AB 77 E8 53 68 28 DevAddr: 26 0B DE BC FPort: 2 MAC payload: 13 4F 40 ED C8 AB 77 E8 53 68 28 DevAddr: 26 0B DE BC FPort: 2 MAC payload: 13 4F 40 ED C8 AB 77 E8 53 68 28 Manual Mathing Statement Sta	DevAddr: 26 0B DE BC Rx1 Delay: 5 DevAddr: 26 0B DE BC Rx1 Delay: 5 DevAddr: 26 0B DE BC MAC payload: 13 4F 40 ED C8 AB 77 E8 53 68 28 "FALSE", TempC1: 28.2, Weight: 283, Work_mode: "Weight" } 00 93 01 1A 1 00 90 01 12 01 7 PovAddr: 26 0B DE BC BC Battery Info ADC Temp DevAddr: 26 0B DE BC FPort: 2 MAC payload: 13 4F 40 ED C8 AB 77 E8 53 68 28 PovAddr: 26 0B DE BC FPort: 2 MAC payload: 13 4F 40 ED C8 AB 77 E8 53 68 28 PovAddr: 26 0B DE BC FPort: 2 MAC payload: 13 4F 40 ED C8 AB 77 E8 53 68 28 PovAddr: 26 0B DE BC FPort: 2 MAC payload: 13 4F 40 ED C8 AB 77 E8 53 68 28 PovAddr: 26 0B DE BC FPort: 2 MAC payload: 13 4F 40 ED C8 AB 77 E8 53 68 28 PovAddr: 26 0B DE BC FPort: 2 MAC payload: 13 4F 40 ED C8 AB 77 E8 53 68 28 PovAddr: 26 0B DE BC FPort: 2 MAC payload: 13 4F 40 ED C8 AB 77 E8 53 68 28 PovAddr: 26 0B DE BC FPort: 2 MAC payload: 13 4F 40 ED C8 AB 77 E8 53 68 28 PovAddr: 26 0B DE BC FPort: 2 MAC payload: 13 4F 40 ED C8 AB 77 E8 53 68 28 PovAddr: 26 0B DE BC FPort: 2 MAC payload: 13 4F 40 ED C8 AB 77 E8 53 68 28 PovAddr: 26 0B DE BC FPort: 2 MAC payload: 13 4F 40 ED C8 AB 77 E8 53 68 28 PovAddr: 26 0B DE BC FPort: 2 MAC payload: 13 4F 40 ED C8 AB 77 E8 53 68 28 PovAddr: 26 0B DE BC FPort: 2 MAC payload: 13 4F 40 ED C8 AB 77 E8 53 68 28 PovAddr: 26 0B DE BC FPort: 2 MAC payload: 13 4F 40 ED C8 AB 77 E8 53 68 28 PovAddr: 26 0B DE BC FPort: 2 MAC payload: 13 4F 40 ED C8 AB 77 E8 53 68 28 PovAddr: 26 0B DE BC FPort: 2 MAC payload: 13 4F 40 ED C8 AB 77 E8 53 68 28 PovAddr: 26 0B DE BC FPort: 2 MAC payload: 13 4F 40 ED C8 AB 77 E8 53 68 28 PovAddr: 26 0B DE BC FPort: 2 MAC payload: 13 4F 40 ED C8 AB 77 E8 53 68 28 PovAddr: 26 0B DE BC FPort: 2 MAC payload: 13 4F 40 ED C8 AB 77 E8 53 68 28 PovAddr: 26 0B DE BC FPort: 2 MAC payload: 13 4F 40 ED C8 AB 77 E8 53 68 28 PovAddr: 26 0B DE BC FPort: 2 MAC payload: 13 4F 40 ED C8 AB 77 E8 53 68 28 PovAddr: 26 08 DE BC FPort: 2 MAC payload: 13 4F 40 ED C8 AB 77 E8 53 68 28 PovAddr: 26 08 DE BC FPort: 2 MAC payload: 13 4F 40 ED C8 AB 77 E8 53 68 28	DevAddr: 26 0B DE BC Rx1 Delay: 5 Temperature1 and Digital Interrupt DevAddr: 26 0B DE BC Rx1 Delay: 5 Temperature2 (DS18B20) DevAddr: 26 0B DE BC MAC payload: 13 4F 40 ED C8 AB 77 E8 53 69 28 FPort: 2 SNR: 0.5 RSSI: -108 Bandwidth: 125000 FALSE*, TempC1: 28.2, Weight: 283, Work_mode: "Weight" } 0D 93 01 1A 1 00 90 01 10 11 7 FPort: 2 SNR: 0.5 RSSI: DevAddr: 26 0B DE BC FPort: 2 MAC payload: 13 4F 40 ED C8 AB 77 E8 53 68 28 DevAddr: 26 0B DE BC FPort: 2 MAC payload: 13 4F 40 ED C8 AB 77 E8 53 68 28 DevAddr: 26 0B DE BC FPort: 2 MAC payload: 13 4F 40 ED C8 AB 77 E8 53 68 28 Temperature3 DevAddr: 26 0B DE BC FPort: 2 MAC payload: 13 4F 40 ED C8 AB 77 E8 53 68 28 Temperature3	DevAddr: 26 0B DE BC Rx1 Delay: 5 DevAddr: 26 0B DE BC Rx1 Delay: 5 DevAddr: 26 0B DE BC MAC payload: 13 4F 40 ED C8 AB 77 E8 53 68 28 "FALSE", TempC1: 28.2, Weight: 283, Work_mode: "Weight" } 0D 93 p1 1A 1 00 00 p1 1E p1 17 PevAddr: 26 0B DE BC BC FPort: 2 MAC payload: 13 4F 40 ED C8 AB 77 E8 53 68 28 DevAddr: 26 0B DE BC FPort: 2 MAC payload: 13 4F 40 ED C8 AB 77 E8 53 68 28 DevAddr: 26 0B DE BC FPort: 2 MAC payload: 13 4F 40 ED C8 AB 77 E8 53 68 28 Port: 2 SNR: 0.5 RSSI: -108 Bandwidth: 125000 SNR (DS1/8B20)08 Raw payload: DevAddr: 26 0B DE BC FPort: 2 MAC payload: 13 4F 40 ED C8 AB 77 E8 53 68 28 Port: 2 SNR: 0.5 RSSI: -108 Raw payload: DevAddr: 26 0B DE BC FPort: 2 MAC payload: 13 4F 40 ED C8 AB 77 E8 53 68 28 Port: 2 SNR: 0.5 RSSI: -108 Raw payload: DevAddr: 26 0B DE BC FPort: 2 MAC payload: 13 4F 40 ED C8 AB 77 E8 53 68 28 Port: 2 SNR: 0.5 RSSI: -108 Raw payload: DevAddr: 26 0B DE BC FPort: 2 MAC payload: 13 4F 40 ED C8 AB 77 E8 53 68 28 Port: 2 SNR: 0.5 RSSI: -108 Raw payload: DevAddr: 26 0B DE BC FPort: 2 MAC payload: 13 4F 40 ED C8 AB 77 E8 53 68 28 Port: 2 SNR: 0.5 RSSI: -108 Raw payload: DevAddr: 26 0B DE BC FPort: 2 MAC payload: 13 4F 40 ED C8 AB 77 E8 53 68 28 Port: 2 SNR: 0.5 RSSI: -108 Raw payload: DevAddr: 26 0B DE BC FPort: 2 MAC payload: 13 4F 40 ED C8 AB 77 E8 53 68 28 Port: 2 SNR: 0.5 RSSI: -108 Raw payload: DevAddr: 26 0B DE BC FPort: 2 MAC payload: 13 4F 40 ED C8 AB 77 E8 53 68 28 Port: 2 SNR: 0.5 RSSI: -108 Raw payload: DevAddr: 26 0B DE BC FPort: 2 MAC payload: 13 4F 40 ED C8 AB 77 E8 53 68 28 Port: 2 SNR: 0.5 RSSI: -108 Raw payload: DevAddr: 26 0B DE BC FPort: 2 MAC payload: 13 4F 40 ED C8 AB 77 E8 53 68 28 Port: 2 SNR: 0.5 RSSI: -108 Raw payload: Port: 2 SNR: 0.5 RSSI:

2.3.5 MOD=5(Weight Measurement by HX711)

This mode is supported in firmware version since v1.6.2. Please use v1.6.5 firmware version so user no need to use extra LDO for connection.



Each HX711 need to be calibrated before used. User need to do below two steps:

- 1. Zero calibration. Don't put anything on load cell and run AT+WEIGRE to calibrate to Zero gram.
- 2. Adjust calibration factor (default value 400): Put a known weight thing on load cell and run **AT+WEIGAP** to adjust the Calibration Factor.

For example:

AT+WEIGAP =403.0

Response: Weight is 401 g

Check the response of this command and adjust the value to match the real value for thing.

Size(bytes)	2	2	2	1	2	2
Value	<u>BAT</u>	Temperature(DS18B20)	<u>ADC</u>	Digital in & Digital Interrupt	Weight	Reserved

	DevAddr:	26 0B 29 3B				Digital In	nput					
do.	DevAddr:	26 0B 29 3B			Temperate	ure and	1					
					(DS18B20)) Digital In	terri	int				
tra	OevAddr:	26 0B 29 3B	Rx1 Delay: 5		(DSTOP20		iterre	.pr				
					1	1						
lic	DevAddz:	26 0B 29 3B	MAC payload:	3F 60 37 9	2 56 F2 BF 88 34 8A 1A FPG	ort: 2 SNR: 0.2 RS	SSI: -10	98 Bandwi	dth: 1	25000		
	EXTI_Tri	gger: "TRUE",	TempC1: 0, Work	k_mode:	Distance" } 0C E9 00 00	0 FB 05 00 FC FF FF	FPort	: 2 SNR:	0.2 R	SSI: -1	108 Bandwidt	1: 12500
	*				1		-	"	ť	-		
	DevAddr:	26 0B 29 3B		Batter	v Info			Deser				
					ADC	A 1A Bandweigh	+	Reser				
	DevAddr:	26 0B 29 3B	FPort: 2 MAC p	payload:	3F 60 37 92 56 F2 BF 8B 34 B	A 1A Bandwilly	125000	SNR: 0.2	RSSI:	-108	Raw payload:	40 3B 2
	*				m	_	-					1
	DevAddr:	26 0B 29 3B	FPort: 2 MAC p	payload:	3F 60 37 92 56 F2 BF 8B 34 B	A 1A Bandwidth: :	125000	SNR: 0.2	RSSI:	-108	Raw payload:	40 3B 2
	1	_		_	m	1						
ica	DevAddr:	26 0B 29 3B										
	DevAddr:	26 0B 29 3B										
	DevAddr:	26 0B 29 3B										

2.3.6 MOD=6(Counting Mode, Since firmware v1.6.5)

In this mode, the device will work in counting mode. It counts the interrupt on the interrupt pins and sends the count on TDC time.

Connection is as below. The PIR sensor is a count sensor, it will generate interrupt when people come close or go away. User can replace the PIR sensor with other counting sensors.



Note: LoRaWAN wireless transmission will infect the PIR sensor. Which cause the counting value increase +1 for every uplink. User can change PIR sensor or put sensor away of the LSN50 to avoid this happen.

Siz	e(bytes)	2	2	2	1	4	
/al	ue	<u>BAT</u>	Temperature(DS18B20) ADC	<u>Digital in</u>	Count	
c		26 0B 26 CD				-110 Bandwidth: 125000	
	OPEN", E	XTI_Trigger:	"FALSE", TempC1: 0, Wo	rk_mode: "Count" } OC FE 00 00	91 14 14 00 00 00 0B	FPort: 2 SNR: -0.8 RSSI: -110	Bandwidth:
	DevAddr:	26 0B 26 CD	Battery	Info		Count	1 20
e	DevAddr:	26 0B 26 CD	FPort: 2 MAC payload	37 Temperature C7 07 C	Band Idth: 1250	00 SNR: -0.8 RSSI: -110 Raw pay	load: 40 CD
	<pre> Uplink is</pre>	a duplicate			Digital Input		,
	DevAddr:	26 0B 26 CD	FPort: 2 MAC payload	: 37 E7 B8 34 BF DF B7 3D C7 D7 C	Bandwidth: 1250	00 SNR: -12.8 RSSI: -130 Raw pa	yload: 40 C
	*		and the second second second	m			,
	DevAddr:	26 0B 26 CD	FPort: 2 MAC payload	: 37 E7 B8 34 BF DF B7 3D C7 D7 C	Bandwidth: 1250	00 SNR: -0.8 RSSI: -110 Raw pay	load: 40 CD
	•			m			
a	DevAddr:	26 0B 26 CD					

2.3.7 Decode payload in The Things Network

While using TTN V3 network, you can add the payload format to decode the payload.

Si Overview	Overview Live data Messaging Location Payload formatters Claiming General settings
A. End devices	Uplink Downlink
Live data	
<> Payload formatters ~	These payload formatters are executed on uplink messages from this end device and take precedence over application level payload formatters.
λ Integrations ↔	Formutter type
2 Collaborators	Use application payload formatter None Javascript GRPC service CayenneLPP Repository
Ov API keys	Formatter parameter* function decodeuplink(input) [
C General settings	<pre>return [dta: i bytes: input.bytes i, warings: [], errors: [] i; }</pre>
< Hidesidebar	Save changes

The payload decoder function for TTN V3 are here:

LSN50 TTN V3 Payload Decoder: <u>http://www.dragino.com/downloads/downloads/LSN50-LoRaST/</u> Payload_decoder/

2.4 Payload Explanation and Sensor Interface

2.4.1 Battery Info

Check the battery voltage for LSN50.

Ex1: 0x0B45 = 2885mV

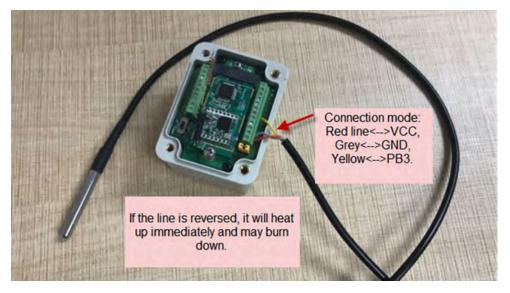
Ex2: 0x0B49 = 2889mV

2.4.2 Temperature (DS18B20)

If there is a DS18B20 connected to PB3 pin. The temperature will be uploaded in the payload.

More DS18B20 can check the <u>3 DS18B20 mode</u>

Connection:



Example:

If payload is: 0105H: (0105 & FC00 == 0), temp = 0105H /10 = 26.1 degree

If payload is: FF3FH: (FF3F & FC00 == 1), temp = (FF3FH - 65536)/10 = -19.3 degrees.

2.4.3 Digital Input

The digital input for pin PA12,

- When PA12 is high, the bit 1 of payload byte 6 is 1.
- When PA12 is low, the bit 1 of payload byte 6 is 0.

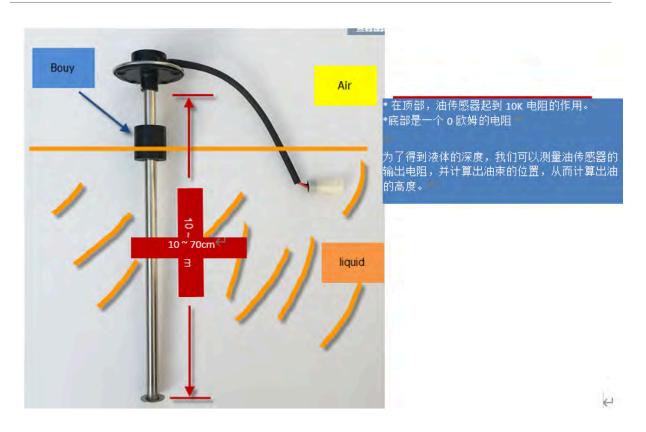
2.4.4 Analogue Digital Converter (ADC)

The ADC pins in LSN50 can measure range from 0~3.3v, it use reference voltage from STM32. If user need to measure a voltage >3.3v, please use resistors to divide this voltage to lower than 3.3v, otherwise, it may destroy the ADC pin.

The ADC monitors the voltage on the PA0 line, in mV.

Ex: 0x021F = 543mv,

Example1: Reading an Oil Sensor (Read a resistance value):

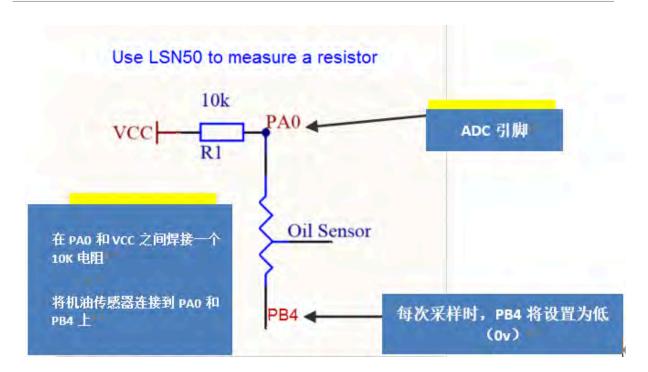


In the LSN50, we can use PB4 and PA0 pin to calculate the resistance for the oil sensor.

Steps:

- 1. Solder a 10K resistor between PA0 and VCC.
- 2. Screw oil sensor's two pins to PA0 and PB4.

The equipment circuit is as below:



According to above diagram:

$$VCC - V_{PA0} \frac{1}{10} k = \frac{V_{PA0}}{R_{oilsensor}}$$

So

$$R_{oil_{sensor}} = V_{PA0} \times 10 \frac{K}{(VCC - V_{PA0})}$$

 V_{PA0} is the reading of ADC. So if ADC=0x05DC=0.9 v and VCC (BAT) is 2.9v

The $R_{oil_{sensor}} = 0.9 \times \frac{10K}{2.9 - 0.9} = 4.5 \text{K ohm}$

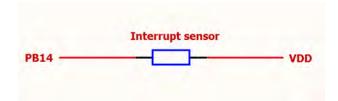
Since the Bouy is linear resistance from 10 ~ 70cm.

The position of Bouy is $\frac{4.5K}{10K} \times (70cm - 10cm) + 10cm = 37cm$, from the bottom of Bouy.

2.4.5 Digital Interrupt

Digital Interrupt refers to pin PB14, and there are different trigger methods. When there is a trigger, the LSN50 will send a packet to the server.

Interrupt connection method:



Example to use with door sensor : (Requires firmware > 1.5.1)

The door sensor is shown at right. It is a two wire magnetic contact switch used for detecting the open/close status of doors or windows.



When the two pieces are close to each other, the 2 wire output will be short or open (depending on the type), while if the two pieces are away from each other, the 2 wire output will be the opposite status. So we can use LSN50 interrupt interface to detect the status for the door or window.

Below is the installation example:

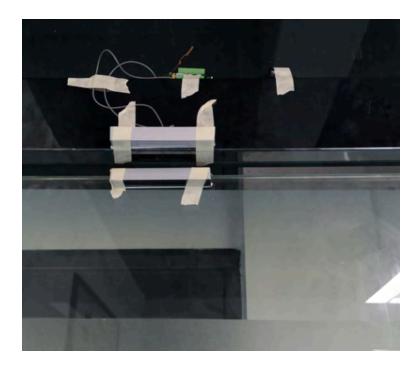
Fix one piece of the magnetic sensor to the door and connect the two pins to LSN50 as follows:

- One pin to LSN50's PB14 pin
- The other pin to LSN50's VCC pin

Install the other piece to the door. Find a place where the two pieces will be close to each other when the door is closed. For this particular magnetic sensor, when the door is closed, the output will be short, and PB14 will be at the VCC voltage.

Door sensors have two types: NC (Normal close) and NO (normal open). The connection for both type sensors are the same. But the decoding for payload are reverse, user need to modify this in the IoT Server decoder.

When door sensor is shorted, there will extra power consumption in the circuit, the extra current is 3v3/R14 = 3v2/1Mohm = 0.3uA which can be ignored.



The above photos shows the two parts of the magnetic switch fitted to a door.

The software by default uses the falling edge on the signal line as an interrupt. We need to modify it to accept both the rising edge ($0v \rightarrow VCC$, door close) and the falling edge ($VCC \rightarrow 0v$, door open) as the interrupt.

The command is:

AT+INTMOD=1 //(more info about INMOD please refer AT Command Manual.)

Below shows some screen captures in TTN V3:

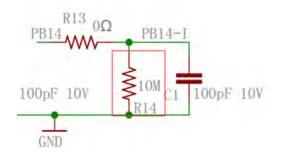
	STORE .	energies, he													
8.984	BatV: 2.895	Digital_IStatus	"L"	Door_status:	"OPEN"	EXTI_Trigger:	"FALSE"	Hum_SHT:	"6553.5"	MOD1	"IIC"	TempC:	"0.00"	TempC_SHT:	"-0.10"
									_	_	m	_	_		
0.941	BatV: 2.895	Digital_IStatus	: "L"	Door_status:	"OPEN"	EXTI_Trigger:	"FALSE"	Hum_SHT:	"6553.5"	MOD1:	"IIC"	TempC:	"0.00"	TempC_SHT:	"-0.10"
•							-				III		-		F
V: 0.954	BatV: 2.89	7 Digital_IStatu	is: "L"	Door_status	: "OPEN"	EXTI_Trigger	TRUE"	Hum_SHT:	"6553.5"	MOD1:	"IIC"	TempC:	"0.00"	TempC_SHT:	"-0.10"
4											111	_			F
0V: 0.95	BatV: 2.85	7 Digital_IStatu	IS: "L"	Door_status	TOPEN"	EXTI_Trigger	TRUE"	Hum_SHT:	"6553.5"	MOD1:	"IIC"	TempC:	"0.00"	TempC_SHT:	*-0.10*
•							-		_				-	_	+
V: 0.952	BatV: 2.89	9 Digital_IStatu	is: "L"	Door_status	E "OPEN"	EXTI_Trigger	"TRUE"	Hum_SHT:	"6553.5"	MODI	"IIC"	TempC:	"0.00"	TempC_SHT:	"-0.10"
4												_			+
0.946	BatV: 2.899	Digital_IStatus	"L"	Door_status:	"CLOSE"	EXTI_Trigger	"TRUE"	Hum_SHT:	"6553.5"	MOD1:	"IIC"	TempC:	"0.00"	TempC_SHT:	"-0.10"

In MOD=1, user can use byte 6 to see the status for door open or close. TTN V3 decoder is as below:

door= (bytes[6] & 0x80)? "CLOSE":"OPEN";

Notice for hardware version LSN50 v1 < v1.3 (produced before 2018-Nov).

In this hardware version, there is no R14 resistance solder. When use the latest firmware, it should set AT +INTMOD=0 to close the interrupt. If user need to use Interrupt in this hardware version, user need to solder R14 with 10M resistor and C1 (0.1uF) on board.

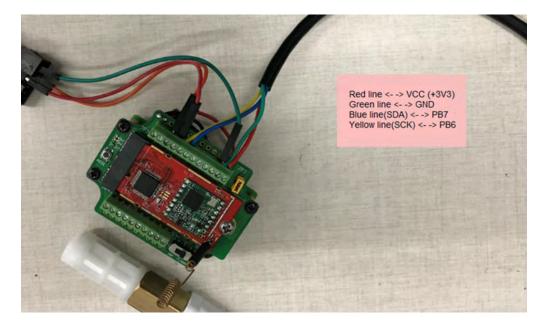


2.4.6 I2C Interface (SHT20)

The PB6(SDA) and PB7(SCK) are I2C interface lines. You can use these to connect to an I2C device and get the sensor data.

We have made an example to show how to use the I2C interface to connect to the SHT20 Temperature and Humidity Sensor. This is supported in the stock firmware since v1.5 with AT+MOD=1 (default value).

Below is the connection to SHT20.



The device will be able to get the I2C sensor data now and upload to IoT Server.

<⇒ 20:01:26	Link ADR request enqueued	DevAddr:	26 08 1C 1D								
↓ 20:01:26	Successfully scheduled data do	DevAddr:	26 0B 1C 1D								
↓ 20:01:26	Schedule data downlink for tra	DevAddr:	26 0B 1C 1D	Rx1 Delay: 5							
↑ 20:01:25	Forward data message to Applic	DevAddr:	26 0B 1C 1D	MAC payload:	14 F8 FA 6	1 78 58 7F 0E 49 A8 B0 F	Port: 2 SNR:	-5.2 RSSI: -	110 Bandwidth:	125000	
个 28:81:25	Forward uplink data message	r: 1817.8	, TempC1: 0,	TempC_SHT: 27.8,	Work_mod	de: "IIC" } 08 21 00 00	00 00 00 01 16 1	7 02 FPort	: 2 SNR: -5.2 R	SSI: -110 Bandwidt	h: 1250
↑ 20:01:25	Receive uplink data message	DevAddr:	26 08 1C 1D			Temper	rature	Н	umidity		
↑ 20:01:25	Successfully processed data me_	DevAddr:	26 08 1C 1D	FPort: 2 MAC p	-	44 F8 FA 61 7B 58 7F 0E 49	9 A8 B0 Bandw	idth: 125000	SNR: -5.2 RSSI	: -110 Raw payload	: 40 10
↑ 20:01:25	Receive data message	DevAddr:	26 0B 1C 1D	FPort: 2 MAC p		44 F8 FA 61 7B 58 7F 0E 49	9 AS BO Bandw	idth: 125000	SNR: -5.2 RSSI	: -110 Raw payload	: 40 10
个 28:01:19	Forward join-accept to Applica	• DevAddr:	26 0B 1C 1D	1.		11					
↑ 28:81:19	Forward join-accept message	DevAddr:	26 0B 1C 1D								

Convert the read byte to decimal and divide it by ten.

Example:

Temperature: Read:0116(H) = 278(D) Value: $278 / 10=27.8^{\circ}$ C; Humidity: Read:0248(H)=584(D) Value: 584 / 10=58.4, So 58.4°

If you want to use other I2C device, please refer the SHT20 part source code as reference.

2.4.7 Distance Reading

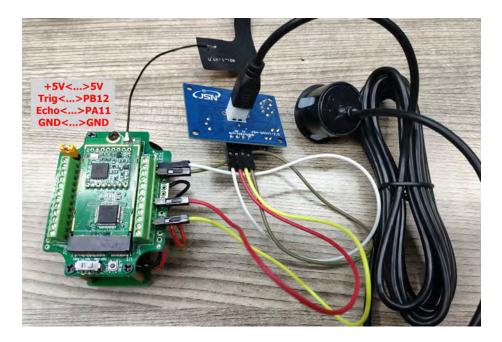
Refer Ultrasonic Sensor section.

2.4.8 Ultrasonic Sensor

The LSN50 v1.5 firmware supports ultrasonic sensor (with AT+MOD=2) such as SEN0208 from DF-Robot. This Fundamental Principles of this sensor can be found at this link: <u>https://wiki.dfrobot.com/Weather_-</u> _proof_Ultrasonic_Sensor_with_Separate_Probe_SKU___SEN0208

The LSN50 detects the pulse width of the sensor and converts it to mm output. The accuracy will be within 1 centimeter. The usable range (the distance between the ultrasonic probe and the measured object) is between 24cm and 600cm.

The picture below shows the connection:



Connect to the LSN50 and run AT+MOD=2 to switch to ultrasonic mode (ULT).

The ultrasonic sensor uses the 8^{th} and 9^{th} byte for the measurement value.

Example:

Distance: Read:0C2D(Hex) = 3117(D) Value: 3117 mm=311.7 cm

÷									
DevAddr:	26 0B 23 59								
DevAddr:	26 0B 23 59								
DevAddr:	26 0B 23 59	Rx1 Delay: 5							
DevAddr:	26 0B 23 59	MAC payload: 89 86 73	30 27 18 98 82 A6 CE 37	FPort: 2	SNR: -15.2	RSSI: -13	3 Bandwidth:	125000	
, EXTI_T	rigger: "FALS	E", TempC1: 0, Work_mode	: " Distance" } OA BE	00 00 00 2	7 04 0C 2D FF	FF FPort	: 2 SNR: -15	2 RSSI: -133 Bar	dwidth:
٠.							m		. ,
DevAddr:	26 0B 23 59								
DevAddr:	26 0B 23 59	FPort: 2 MAC payload:	89 B6 73 30 27 1B 9B B2				R: -15.2 RSSI	: -133 Raw paylos	ad: 40 5
•			m	in t	ne range				,
Uplink is	s a duplicate								
DevAddr:	26 0B 23 59	FPort: 2 MAC payload:	89 B6 73 30 27 1B 9B B2	A6 CE 37	Bandwidth:	125000 SN	R: 0.5 RSSI:	-111 Raw payload	40 59 2
			111						,
DevAddr:	26 0B 23 59	FPort: 2 MAC payload:	89 B6 73 30 27 1B 9B B2	A6 CE 37	Bandwidth:	125000 SN	R: -15.2 RSSI	: -133 Raw paylos	ad: 40 5
+ 1						1			

4				1		_	111	-			
DevAddr:	26 08 59 43		out	of range							
DevAddr:	26 0B 59 43	FPort: 2 MAC payload:	BB CA FF 95 75 DA 63 DB 90 D7 A3	Bandwidth:	125000	SNR:	-2.2	RSSI:	-105	Raw payload:	40 43
1			111		1						,
DevAddr:	26 0B 59 43	FPort: 2 MAC payload:	BB CA FF 95 75 DA 63 DB 90 D7 A3	Bandwidth:	125000	SNR:	-2.2	RSSI:	-105	Raw payload:	40 43
•				_							+
DevAddr:	26 0B 59 43										
DevAddr:	26 0B 59 43										

You can see the serial output in ULT mode as below:

```
LSN50 Device
Image Version: v1.4
Frequency Band: EU868
DevEui= 00 97 16 CF B8 37 CF 4E
***** UpLinkCounter= 0 *****
TX on Freq 868300000 Hz at DR 5
txDone
rxDone
JOINED
Distance is out of range
***** UpLinkCounter= 0 *****
TX on Freq 867700000 Hz at DR 0
txDone
rxTimeOut
rxTimeOut
Distance=3117 mm
***** UpLinkCounter= 1 *****
TX on Freq 868100000 Hz at DR 0
```

TX on freq 868100000 Hz at DR 0 txDone rxTimeOut rxDone

In TTN V3 server:

DevAddr:	26 0B 23 59										
DevAddr:	26 0B 23 59										
DevAddr:	26 0B 23 59	Rx1 Delay: 5									
DevAddr:	26 0B 23 59	MAC payload:	89 B6 73 3	30 27 1B 9B B2 A6 CE 37	FPort: 2	SNR: -15.2	RSSI: -	133 Band	ridth: 1250	90	
, EXTI_T	rigger: "FALS	E", TempC1: 0,	Work_mode	: " Distance" } OA B	E 00 00 00 :	27 04 0C 2D FF	FF FP	ort: 2 SN	R: -15.2 RS	SSI: -133 Band	width:
								m			
DevAddr:	26 0B 23 59										
DevAddr:	26 0B 23 59	FPort: 2 MAC	payload:	89 B6 73 30 27 1B 98 B2	A6 CE 37	Bandwidth:	125000	SNR: -15.	2 RSSI: -1	33 Raw payloa	d: 40
+			_	III	in t	he range					
Uplink is	a duplicate										
DevAddr:	26 0B 23 59	FPort: 2 MAC	payload:	89 B6 73 30 27 18 98 B2	A6 CE 37	Bandwidth:	125000	SNR: 0.5	RSSI: -111	Raw payload:	40 59
4			_	m			-				
DevAddr:	26 0B 23 59	FPort: 2 MAC	payload:	89 B6 73 30 27 1B 9B B2	A6 CE 37	Bandwidth:	125000	SNR: -15.	2 RSSI: -1	33 Raw payloa	d: 40 !
+1			-				-				

EXTI_Tri	gger: "FALSE"	, TempC1: 0, Work_mode:	" Distance" } 0A BE 00 00 00 00	04 FF FF FF FF	FPo	rt: 2	SNR:	-2.2	RSSI:	-105 Bandwi	dth: 128
				7			m				
DevAddr:	26 0B 59 43		out	of range							
DevAddr:	26 OB 59 43	FPort: 2 MAC payload:	BB CA FF 95 75 DA 63 DB 90 D7 A3			SNR:	-2.2	RSSI:	-105	Raw payload	: 40 43
*			m								
DevAddr:	26 0B 59 43	FPort: 2 MAC payload:	BB CA FF 95 75 DA 63 DB 90 D7 A3	Bandwidth:	125000	SNR:	-2.2	RSSI:	-105	Raw payload	40 43
¥ [,
DevAddr:	26 0B 59 43										
DevAddr:	26 0B 59 43										
DevAddr:	26 0B 59 43										

2.4.9 +5V Output

Since v1.2 hardware version, a +5v output is added in the hardware. The +5V output will be valid for every sampling. LSN50 will enable +5V output before all sampling and disable the +5v after all sampling.

Since firmware v1.6.3, The 5V output time can be controlled by AT Command.

AT+5VT=1000

Means set 5V valid time to have 1000ms. So the real 5V output will actually have 1000ms + sampling time for other sensors.

By default the AT+5VT=500. If the external sensor which require 5v and require more time to get stable state, user can use this command to increase the power ON duration for this sensor.

2.4.10 Weigh Sensor HX711

Since v1.6.2 firmware, LSN50 support Weigh Sensor HX711.

2.4.11 BH1750 Illumination Sensor

Since v1.7.0 firmware, MOD=1 support this sensor. The sensor value is in the 8th and 9th bytes.



	DevAddr:	26 0B F3 D8									[
o	DevAddr:	26 0B F3 D8									
a	DevAddr:	26 0B F3 D8	Rx1 Delay: 5								
ə	DevAddr:	26 0B F3 D8	MAC payload: A6	36 50 7F C3 13 F9 7C 59	BF AF FPort: 2	SNR: -4.8 R	RSSI: -111	Bandwid	lth: 125000		
	Digital_1	Status: "L",	Door_status: "OPE	", EXTI_Trigger: "F	ALSE", Illum: 53,	TempC1: 0, W	Work_mode:	"IIC" }	OC E7 00 0	0 00 A2 00 00 35	00 00
	•										P.
	DevAddr:	26 0B F3 D8									
e	DevAddr:	26 0B F3 D8	FPort: 2 MAC pay	Load: A6 B6 50 7F C3	13 F9 7C 59 BF AF	Bandwidth: :	125000 SN	R: -4.8	RSSI: -111	Raw payload:	40 D8 F
	•										
	DevAddr:	26 0B F3 D8	FPort: 2 MAC pay	Load: A6 B6 50 7F C3	13 F9 7C 59 BF AF	Bandwidth: :	125000 SN	R: -4.8	RSSI: -111	Raw payload:	40 D8 F
	•			III							F.
a	DevAddr:	26 0B F3 D8									
	DevAddr:	26 0B F3 D8									

2.4.12 Working MOD

The working MOD info is contained in the Digital in & Digital Interrupt byte (7^{th} Byte).

User can use the $3^{rd} \sim 7^{th}$ bit of this byte to see the working mod:

Case 7th Byte >> 2 & 0x1f:

- 0: MOD1
- 1: MOD2
- 2: MOD3

- 3: MOD4
- 4:MOD5
- 5:MOD6

2.5 Configure LSN50 via AT or Downlink

User can configure LSN50 via AT Commands or LoRaWAN Downlink Commands

There are two kinds of Commands:

- Common Commands: They should be available for each sensor, such as: change uplink interval, reset device. For firmware v1.7.0, user can find what common commands it supports: End Device AT Commands
- Sensor Related Commands: These commands are special designed for LSN50. User can see these commands below:

2.5.1 Common Commands:

They should be available for each of Dragino Sensors, such as: change uplink interval, reset device. For firmware v1.7.0, user can find what common commands it supports: <u>End Device AT Commands</u>

2.5.2 Sensor related commands:

Set work mode:

• AT Command:

AT+MOD=2 // Set work MOD =2. (1:IIC mode,2:Distance mode,3:3ADC mode, 4:3DS18B20 mode,5:weight mode)

AT+MOD=? // Get current work MOD

• Downlink Payload:

0x0A aa // Same as AT+MOD=aa

Set the trigger interrupt mode:

• AT Command:

AT+INTMOD=2 // Set INTMOD =2. (0:Disable,1:falling or rising,2:falling,3:rising)

AT+INTMOD=? // Get current INTMOD

• Downlink Payload:

0x06 000003 // Set AT+INTMOD=3

Set the 5V power open time during sampling:

• AT Command:

AT+5VT=1000 // Set 5v open time to 1000ms

AT+5VT=? // Check current 5v open duration

• Downlink Payload:

0x07 aa bb // Equal AT+5VT=0x(aa bb)

Set the weight to 0g (Zero Calibration)

• AT Command:

AT+WEIGRE // Set the weight to 0g

• Downlink Payload:

0x08 01 // Set the weight to 0g

Get or Set the GAP Value (calibrate factor) of measurement

• AT Command:

AT+WEIGAP=403.0 // Set GAP Value =403.0 (response: Weight: xx g)

AT+WEIGAP=? // Get current GAP Value

• Downlink Payload:

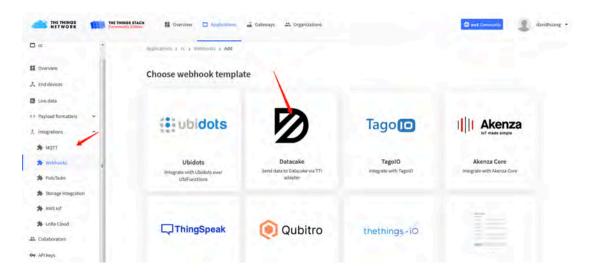
0x08 02 aa bb // Equal to AT+WEIGAP=0x(aa bb)/10

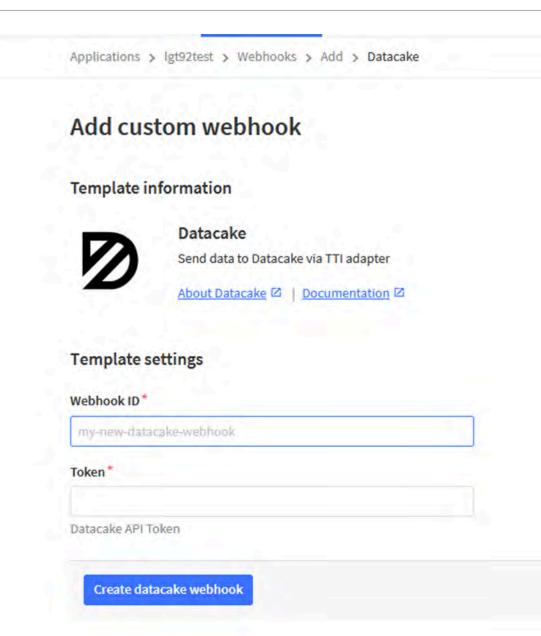
2.6 Show Data in Datacake IoT Server

Datacake provides a human friendly interface to show the sensor data, once we have data in TTN V3, we can use Datacake to connect to TTN V3 and see the data in Datacake. Below are the steps:

Step 1: Be sure that your device is programmed and properly connected to the network at this time.

Step 2: To configure the Application to forward data to Datacake you will need to add integration. To add the Datacake integration, perform the following steps:





Dragino LSE01 Dragino	
Dragino LT-22222-L Dragino	
Dragino LWL01 Dragino	CHIRARIA 🕲
ESP32-Paxcounter cyberman54	SPRESSIF
Elsys ELT-2 Elsys.se	ELSYS.se
Showing 26 to 30 of 79 results	Previous Next

Step 3: Create an account or log in Datacake.

Step 4: Search the LSN50 and add DevEUI.

IOD	Door Status	Battery
0	Closed	0.00v
<u> </u>		Last Update: 9 months ago
0		
Last Update: 9 months a	igo	
istance		
	O mm	
	Last Update: 8 months ago	
end		

2.7 Firmware Change Log

Firmware download: http://www.dragino.com/downloads/index.php?dir=LSN50-LoRaST/Firmware/LSN50.hex/ Firmware Change Log: http://www.dragino.com/downloads/index.php?dir=LSN50-LoRaST/Firmware/LSN50.hex/

2.8 Use VDD or +5V to Power External Sensor

User can use VDD or +5V to power external sensor.

Note:

- 1. VDD is 2.5~3.3v from the battery + diode, the VDD is always on, so when use VDD to power external sensor, make sure the sensor has a low power consumption in sleep mode (less 50 uA) to get a long battery life.
- 2. +5V output is only ON when sampling. And MCU will turn off it after sampling. So if sensor can support 5v, +5V out is the best choice. See here for more info.

Note: Always test the actually current pass by the JP2 jumper when connect to a new type of sensor.

2.9 Battery Analysis

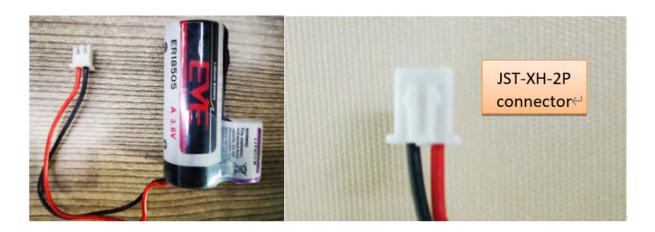
2.9.1 Battery Type

The LSN50 battery is a combination of a 4000mAh or 8500mAh Li/SOCI2 Battery and a Super Capacitor. The battery is non-rechargeable battery type with a low discharge rate (<2% per year). This type of battery is commonly used in IoT devices such as water meter.

The battery is designed to last for more than 5 years for the LSN50.

The battery related documents as below:

- Battery Dimension,
- Lithium-Thionyl Chloride Battery datasheet, Tech Spec
- Lithium-ion Battery-Capacitor datasheet, Tech Spec



2.9.2 Power consumption Analyze

When connect to different sensors, it is good to test the power consumption with the sensor working. User can remove the ON/OFF Jumper of LSN50, and connect a multimeter between the two pins of this header and measure the current to know the whole system power consumption. Because the sleep mode will have as low as 10uA, at least 4.5 digit multimeter is required to measure this level of current.

A victor VC86E is recommended.

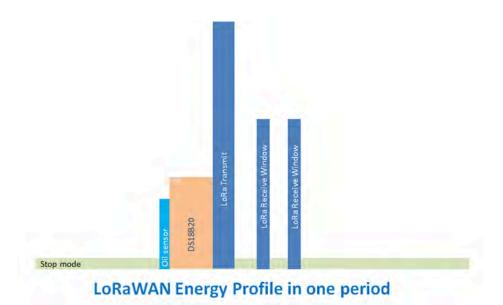


In a minimum system with DS18B20 and Oil Sensor and default firmware, the power consumption includes:

- 1. Deep Sleep (Stop mode) for STM32. ~ 5uA
- 2. Sampling current while reading DS18B20 and Oil Sensor
 - Oil Sensor sampling time: 200us, current: 0.3mA
 - DS18B20 sampling time: 750ms, current: 0.64mA
 - Above power should add 8mA CPU power in working mode.

3. LoRaWAN transmit and receive time consumption. The LoRa TX / RX time and power can be found in the LoRa calculator tool.

In a typical LoRaWAN data transmit. The energy profile is as below:



In the LoRaWAN protocol, the device will transfer in different LoRa Radio, and have different energy profile in LoRa part. We can calculate the battery life in two cases:

- 1. Lower power LoRa radio. Device has a good signal to gateway
- 2. Higher power LoRa radio. Device has a poor signal to gateway

Low Power Case:

- Radio Parameter: SF7, 125kHz, 20dbm
- Transmit interval: 15 minutes.
- · Payload: 8 Bytes.

High Power Case:

- Radio Parameter: SF10, 125kHz, 20dbm
- Transmit interval: 15 minutes.
- Payload: 8 Bytes.

To simplify the calculation, we can:

- Combine oil sensor and DS18B20 sampling energy together to 751ms@8.64ma
- · Combine the two RX windows together.

There is a power consumption tool for easy analysis. Below is the analysis result.

cenarios		A	B	C	D	E	F
ime	Units	Scenario_A	Scenario_B	Scenario_C	Scenario_D	Scenario_E	Scenario_F
Sleep	min	15	15	15		100 m 10 1	
Sampling	ms	751	751	5000			
Transmit	ms	100	274.4	34.3			
Receive	ms	72	491.4	82			-
Radio type		SF7_125K_20dB	SF10_125K_20dB	SF7_125K_14dB			
# of bytes transmitte	d	8	8	8		1	
Sleep	mA	0.005	0.005	0.005	1	1	
Sampling	mA	0.64	0.64	0.64			
Transmit	mA	133	133	52			
Receive	mA	18.8	18.8	18.8	[
				4	o-Controller Act	tive power (mA)	8
ower usage comparison							
Sleep	%	22.92%	8.87%	40.82%	0.00%	0.00%	0.00%
Sampling	%	2.45%	0.95%	29.02%	0.00%	0.00%	0.00%
Transmit	%	67.74%	71.96%	16.18%	0.00%	0.00%	0.00%
Receive	%	6.89%	18.22%	13.98%	0.00%	0.00%	0.00%
and the second second					Legend	t Red > 100%,	Green <= 100
verage current	mA	0.021793472	0.056254259	0.012180976	0	0	0
Design Goals							
System efficiency		90%	90%	90%	90%	90%	90%
Target battery life	yr	2	2	2	2	2	2
Required battery capac		424.54	1095.83	237.29	0.00	0.00	0.00
OF	(inclusion)						
	mAh	4000	4000	4000	4000	4000	4000
Given battery capacity							

Note: Ignore the 18 year result, because the battery has a max 2% discharge per year.

2.9.3 Battery Note

The Li-SICO battery is designed for small current / long period application. It is not good to use a high current, short period transmit method. The recommended minimum period for use of this battery is 5 minutes. If you use a shorter period time to transmit LoRa, then the battery life may be decreased.

2.9.4 Replace the battery

You can change the battery in the LSN50. The type of battery is not limited as long as the output is between 3v to 3.6v. On the main board, there is a diode (D1) between the battery and the main circuit. If you need to use a battery with less than 3.3v, please remove the D1 and shortcut the two pads of it so there won't be voltage drop between battery and main board.

The default battery pack of LSN50 includes a ER18505 plus super capacitor. If user can't find this pack locally, they can find ER18505 or equivalence, which will also work in most case. The SPC can enlarge the battery life for high frequency use (update period below 5 minutes).

3. Using the AT Commands

3.1 Access AT Commands

LSN50 supports AT Command set in the stock firmware. You can use a USB to TTL adapter to connect to LSN50 for using AT command, as below.

LSN50 v1 UART connection photo



LSN50 v2 UART connection photo



In the PC, you need to set the serial baud rate to **9600** to access the serial console for LSN50. LSN50 will output system info once power on as below:

友善串口调试助手		
文件(F)编辑(E)视图(V)工具(T)控制	J(C) 報助(H)	
串口设置 端 □ COM9 · 波特率 9600 · 数据位 8 ·	[238]***** UpLinkCounter= 0 ***** [239]TX on freq 868500000 Hz at DR 5 [304]txDone [5293]RX on freq 868500000 Hz at DR 5 [5381]rxDone Rssi= -79 JOINED	-
校验位 None 停止位 1 流 控 None ✓	Join Accept: DevAddr:26 01 2a a6 RxlDrOffset:0	
接收设置 ・ ASCII	[5493]***** UpLinkCounter= 0 ***** [5494]TX on freq 868500000 Hz at DR 0 [6980]txDone [8010]RX on freq 868500000 Hz at DR 0 [8210]rxTimeOut [8975]RX on freq 869525000 Hz at DR 3 ADR Message: TX Datarate 0 change to 3	
发送设置 ⓒ ASCII ○ Hex └ 自动重发 1000 ÷ ms	TxPower 0 change to 1 NDRep 1 change to 1 [9151]rxDone Rssi= -70 5 Incorrect Password Correct Password	
	123456 There must be a new line after each command	
	123456	*
OM9 OPENED, 9600, 8, NONE, 1, OFF	Rx: 778 Bytes Tx: 26 Bytes	

3.2 Common AT Command Sequence

3.2.1 Multi-channel ABP mode (Use with SX1301/LG308)

If device has not joined network via OTAA:

AT+FDR

AT+NJM=0

ATZ

If device already joined network:

AT+NJM=0

ATZ

3.2.2 Single-channel ABP mode (Use with LG01/LG02)

See Sect 6.7

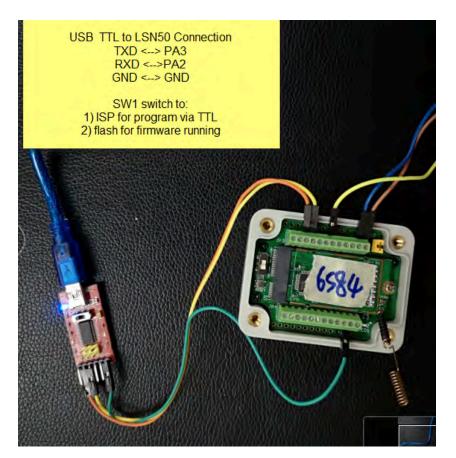
4. Upload Firmware

Notes:

- Since image v1.3, the firmware will show version info during boot. If your device doesn't show version info, you may have a very old image version.
- Always run AT+FDR to reset parameters to factory default after an update image. If the update is from image >= v1.3 to another image version >=v1.3, then the keys will be kept after running AT+FDR. Otherwise (e.g. from v1.2 to v1.3), AT+FDR may erase the keys.

4.1 Upload Firmware via Serial Port

The LSN50's AT Command port can be used for firmware upgrade. The hardware connection for upgrade firmware is as below:



Step1: Download <u>flash loader</u>. Step2: Download the <u>LSN50 Image files</u>.

Flash Load	ler Demonst	_	saugment	ed	2) X	Flash Loader Demonstrator	ugrienled
Select the co connection. Common for	mmunication po		1000	C. C. Martin	pen	Target is readable. Please click "Ne	set" to proceed.
 UART Port Name Baud Rate Data Bits 	СОМ19 115200 8	र र	Parity Echo Timeout(s)	Even Disabled 10		Board detected	Theory a condition
2): Pus	1): Put S h RESET I	SW1 to outton t	ISP poso o enter pr	tion ogram mo	de	Select Next	
						1	24

Step3: Open flashloader; choose the correct COM port to update

	5	I the ough	norded		Also.augmented
Please, seld	ct your device in th	ne target list			C Erase
Target	STM32L0_x3_x2	2_x1_192K			C.N. C. Seetlan
PID (h)	0447				
BID (h)	15.15		need to w		Download to device You can see the location of Download from the your programming
BID (h)	Toria.	1	moment.		
Version	3.1	Selec	ct the first	one	E\资料\软件下载\ST官方\STM32CubeExpansion_LRWAN_V
Flash mappin	0	OCICC	a une mot	one.	(* Erase necessary pages C No Erase C Global Erase
Name	Start address	End address	Size		
Paget	0+ 2000000	Qii 800007F	DVED (DK)		(h) 8000000 - Long to the user program
Page1	Dx 9000080	0x 80000FF	0x80 (0K.)		☐ Optimize (Remove some FFs) Verily after download
Page2	Dx 9000100	0x 800017F	0x90 (0K)		Apply option bytes
Page3	0x 8000180	0x 80001FF	0x80 (0K)		
	Dx 9000200	0x 900027F	0x80 (0K.)		C Upload from device
Page4	Dx 8000280	0x 80002FF	0x80 (0K)		Upload to file
Page5	Dx 9000300	0x 800037F	0x80 (0K)		
	DX 00000300				
Page5	0x 8000380	0x 80003FF	Oveo (ok)		
Page5 Page5		0x 80003FF 0x 800047F	0x80 (0K) 0x80 (0K)		c
Page5 Page5 Page7	0x 8000380				C Destant Dest
Page5 Page5 Page7 Page8	0x 8000380 0x 8000400	0x 900047F	0+90 (0K)		
Page5 Page5 Page7 Page9 Page9	0x 9000390 0x 9000400 0x 9000480	0x 900047F 0x 90004FF	0x80 (0K) 0x80 (0K)		

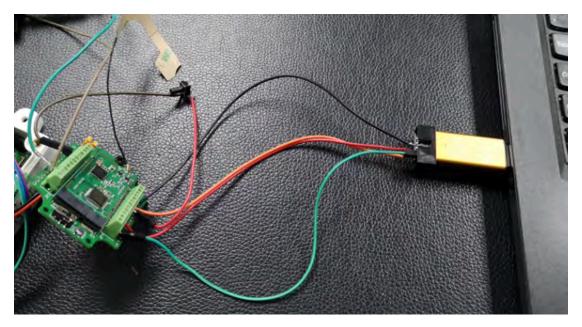
lash Load	ter Demonstrator	Flash Loa	der Demonstrator
Target Map file	STM32L0_x3_x2_x1_192K STM32L0_x3_x2_x1_192K_STmap	Target Map file	STM32L0_x3_x2_x1_192K STM32L0_x3_x2_x1_192K.STmap
	D0WNL0AD E·波科·软件下载\ST官方 \STM32CubeExpansion_LRWAN_V1.1.2\Projects\Multi\Appl		DOWNLOAD E:V资料·收件下载\ST官方 \STM32CubeExpansion_LRWAN_V1.1.2\Projects\Multi\Ap
File size Status Time	50.24 K8 (51448 bytes) 6.53 K8 (6682 bytes) of 50.24 K8 (51448 bytes) 00:04 Waiting for download	File size Status Time	50.24 KB (51448 bytes) 50.24 KB (51448 bytes) of 50.24 KB (51448 bytes) 00.53 The programming has been completed
	Downloading data 13%	-	ownload operation finished successfully
	Bed Bet Cancel Dom		Back Med Dece Doce

Step4: Switch SW1 back to flash state and push the RESET button.

The LSN50 will then run the new firmware.

4.2 Upload Firmware via ST-Link V2

You can use ST-LINK to upgrade firmware into LSN50. The hardware connection for upgrade firmware is as below:



Connection:

- ST-LINK v2 GND <--> LSN50 GND
- ST-LINK v2 SWCLK <--> LSN50 PA14
- ST-LINK v2 SWDIO <--> LSN50 PA13
- ST-LINK v2 RST <--> LSN50 NRST

Step1: Install ST-LINK driver first and then install ST-LINK Utility

Step2: Download the LSN50 Image files.

Step3: Open ST-LINK utility, file --> open file to select the image to be upgraded.

Step4: Click the "Program Verify" button on ST-LINK.

File Edit View Target STENK External Loader Help Memory display Address: 0x08000000 V Size: 0x129D0 Data Width: 32 bits V Device Memory File : IN865.hex	Device Device ID Revision ID Flash size	LiveUpdate
Device Memory		

Step5: The led on the ST-LINK adapter will now blinking, and the ST-Link utility will pop up a download window. Click the start button to download the image to LSN50.

NOTE: If this step fails, ST-LINK can't establish connection to LSN50, please try to swap SWDIO & SWCLK pin. Some ST-LINK v2 devices are incorrectly marked.

Memory display Address: 0x0800000 - S	Size: 0x129D0	Data Width: 32 bits 👻	Device STM32L07x/STM32L08x Device ID 0x447 Revision ID Unknown	(
Device Memory File : IN865.he	Download [IN8	865.hex]	23	LiveUpdate
Device Memory	Start address File path Extra options Verification	Dx08000000 D:\Projects\LoRa Product Line\Lo	Skip Flash Protection verification	
	Click "Start" to p	 Verify while programming program target. 	Verify after programming	
	After programm	Reset after programming	Full Flash memory Checksum	

5. Developer Guide

- Software Source Code Download : <u>https://github.com/dragino/LoRa_STM32/tree/master/</u> <u>STM32CubeExpansion_LRWAN</u>
- Hardware Source Code Download: https://github.com/dragino/Lora/tree/master/LSN50

LSN50 is an open source project, developer can use compile their firmware for customized applications. User can get the source code from:

- Software Source Code: <u>https://github.com/dragino/LoRa_STM32/tree/master/</u> STM32CubeExpansion_LRWAN
- Hardware Design files: <u>https://github.com/dragino/Lora/tree/master/LSN50</u>
- Compile instruction: http://wiki.dragino.com/xwiki/bin/view/Main/Firmware%20Compile%20Instruction
 %20--%20STM32/

Use Keil to open project file:

STM32CubeExpansion_LRWAN/Projects/Multi/Applications/LoRa/DRAGINO-LRWAN(AT)/MDK-ARM/ STM32L072CZ-Nucleo/Lora.uvprojx

In Keil, you can see what frequency band the code support.

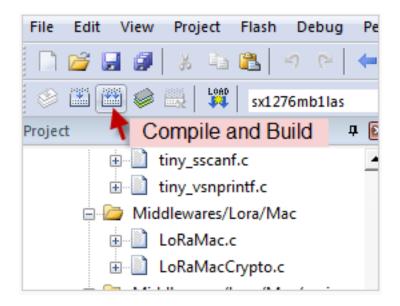
🖨 🦾 Mie	ddlewares/Lora/Mac/region
	Region.c
	RegionAS923.c
	RegionAU915.c
	RegionCN470.c
	RegionCN779.c
	RegionCommon.c
	RegionEU433.c
	RegionEU868.c
	RegionIN865.c
	RegionKR920.c
±	RegionUS915.c
	RegionUS915-Hybrid.c

1. If you want to change frequency, modify the Preprocessor Symbols.

For example, change EU868 to US915

Presenter	sor Symbols				
Define:	STM32L072x, USE_STM3	2L0XX_NUCLEO,USE_HAL_DRIVER, REGIO	N EUSSS		
Undefine:	1	Fr	equency		
Language	/ Code Generation				
Execut	te-only Code	Strict ANSI C	Warnings: All Warnings		
Optimizatio	n: Level 3 (-03) -	Enum Container always int.			
Optimi	te for Time	Plain Char is Signed	Therein Milde No Auto Includes		
Splt La	and and Store Multiple	F Read-Only Position Independent			
One E	LF Section per Function	☐ Read-Write Postion Independent	IT C99 Mode		
Include Paths	[.1.Vine:.1.1.1.1.1.VDth	vers\BSP\STM32LQx_Nucleo:\.\.\.	Drivers\STM32LDox_HAL		
Misc Controls	-C99				
Compiler control string	c -cpu Cotex-M0+ 0_MICROLIB g 03 -apcs-intervolk -splt_sections 4 .7 ./nc 4 .7 .7 .7 .7 .7 .7 .7 .0nvers/BSP/STM32L0x_Nucleo 4				

2. Compile and build



6. FAQ

6.1 Why there is 433/868/915 version?

Different countries have different rules for the ISM band for LoRa. Although the LoRa chip can support a wide range of Frequencies, we provide different versions of the hardware for best tune of the LoRa hardware part.

6.2 What is the frequency range of LT LoRa part? ?

Different LT version supports different frequency range, below is the table for the working frequency and recommend bands for each model:

Version	LoRa IC	Working Frequency	Best Tune Frequency	Recommend Bands
433	SX1278	Band2(LF): 410 ~525 Mhz	433Mhz	CN470/EU433
868	SX1276	Band1(HF):862~1020 Mhz	868Mhz	EU868
915	SX1276	Band1(HF):862 ~1020 Mhz	915Mhz	AS923/AU915/KR920/US915

6.3 How to change the LoRa Frequency Bands/Region?

You can follow the instructions for <u>how to upgrade image</u>. When downloading the images, choose the required image file for download.

6.4 Can I use Private LoRa protocol?

The stock firmware is based on LoRaWAN protocol. You can use a private LoRa protocol in LSN50. This section describes an example for base LoRa transfer. It is a reference/demo and we do not provide further software development support on this topic.

In this demo, we will show the communication between LoRa Shield and LSN50, both of them using the basic LoRa library. LSN50 will send a message to a LoRa Shield and the LoRa Shield will print it to the console.

LoRa Shield + UNO:

Use the LoRa Library and upload the LoRa Receive Sketch to Arduino.

Refs:

http://www.dragino.com/downloads/index.php?dir=LSN50-LoRaST/LoRa_Raw_Example/Arduino/&file=LoRa.zip http://www.dragino.com/downloads/downloads/LSN50-LoRaST/LoRa_Raw_Example/Arduino/LoRaReceiver.ino

Open the serial monitor to Arduino. The device acts as a LoRa Receiver and listen on the frequency 868.3Mhz by default.

LSN50:

Use the <LoRa RAW code> . The project file is in: MDK-ARM\STM32L072CZ-Nucleo\ Lora.uvprojx

Compile it and Upload it to LSN50, the LSN50 will transfer on the frequency 868.3Mhz.

In the Arduino Console, it will see the received packets as below.

LoRa Receiver			
Received packet 'Hello, LoRa shield' with RSSI -32			
Received packet 'Hello, LoRa shield' with RSSI -33			
Received packet 'Hello,LoRa shield' with RSSI -33			
Received packet 'Hello, LoRa shield' with RSSI -32			
Received packet 'Hello, LoRa shield' with RSSI -33			
Received packet 'Hello, LoRa shield' with RSSI -33			
Received packet 'Hello,LoRa shield' with RSSI -33			
Received packet 'Hello, LoRa shield' with RSSI -34			
Received packet 'Hello, LoRa shield' with RSSI -33			
Received packet 'Hello,LoRa shield' with RSSI -33			
Autoscroll	No line ending ~	9600 baud	~

6.5 How to set up LSN50 to work in 8 channel mode

By default, the frequency bands US915, AU915, CN470 work in 72 frequencies. Many gateways are 8 channel gateways, and in this case, the OTAA join time and uplink schedule is long and unpredictable while the end node is hopping in 72 frequencies.

You can configure the end node to work in 8 channel mode by using the AT+CHE command. The 500kHz channels are always included for OTAA.

For example, in **US915** band, the frequency table is as below. By default, the end node will use all channels (0~71) for OTAA Join process. After the OTAA Join, the end node will use these all channels (0~71) to send uplink packets.

CHE	US915 Uplink Channels(125KHz,4/5,Unit:MHz,CHS=0)									
0	ENABLE	ENABLE Channel 0-63								
1	902.3	902.5	902.7	902.9	903.1	903.3	903.5	903.7	Channel 0-7	
2	903.9	904.1	904.3	904.5	904.7	904.9	905.1	905.3	Channel 8-15	
3	905.5	905.7	905.9	906.1	906.3	906.5	906.7	906.9	Channel 16-23	
4	907.1	907.3	907.5	907.7	907.9	908.1	908.3	908.5	Channel 24-31	
5	908.7	908.9	909.1	909.3	909.5	909.7	909.9	910.1	Channel 32-39	
6	910.3	910.5	910.7	910.9	911.1	911.3	911.5	911.7	Channel 40-47	
7	911.9	912.1	912.3	912.5	912.7	912.9	913.1	913.3	Channel 48-55	
8	913.5	913.7	913.9	914.1	914.3	914.5	914.7	914.9	Channel 56-63	
Chann	els(500KH	lz,4/5,Un	it:MHz,C	HS=0)						
	903	904.6	906.2	907.8	909.4	911	912.6	914.2	Channel 64-71	

When you use the TTN V3 network, the US915 frequency bands use are:

- 903.9 SF7BW125 to SF10BW125
- 904.1 SF7BW125 to SF10BW125
- 904.3 SF7BW125 to SF10BW125
- 904.5 SF7BW125 to SF10BW125
- 904.7 SF7BW125 to SF10BW125
- 904.9 SF7BW125 to SF10BW125
- 905.1 SF7BW125 to SF10BW125
- 905.3 SF7BW125 to SF10BW125
- 904.6 SF8BW500

Because the end node is now hopping in 72 frequency, it makes it difficult for the devices to Join the TTN V3 network and uplink data. To solve this issue, you can access the device via the AT commands and run:

AT+CHE=2

ATZ

to set the end node to work in 8 channel mode. The device will work in Channel 8-15 & 64-71 for OTAA, and channel 8-15 for Uplink.

The AU915 band is similar. Below are the AU915 Uplink Channels.

CHE	AU915 Uplink Channels(125KHz,4/5,Unit:MHz,CHS=0)									
0	ENABL	ENABLE Channel 0-63								
1	915.2	915.4	915.6	915.8	916	916.2	916.4	916.6	Channel 0-7	
2	916.8	917	917.2	917.4	917.6	917.8	918	918.2	Channel 8-15	
3	918.4	918.6	918.8	919	919.2	919.4	919.6	919.8	Channel 16-23	
4	920	920.2	920.4	920.6	920.8	921	921.2	921.4	Channel 24-31	
5	921.6	921.8	922	922.2	922.4	922.6	922.8	923	Channel 32-39	
6	923.2	923.4	923.6	923.8	924	924.2	924.4	924.6	Channel 40-47	
7	924.8	925	925.2	925.4	925.6	925.8	926	926.2	Channel 48-55	
8	926.4	926.6	926.8	927	927.2	927.4	927.6	927.8	Channel 56-63	
Chanr	nels(500K	(Hz,4/5,U	nit:MHz,	CHS=0)						
	915.9	917.5	919.1	920.7	922.3	923.9	925.5	927.1	Channel 64-71	

6.6 How to set up LSN50 to work with Single Channel Gateway such as LG01/LG02?

In this case, users need to set LSN50 to work in ABP mode and transmit in only one frequency.

Assume we have a LG02 working in the frequency 868400000 now, below is the steps.

Step1: Log in TTN V3, Create an ABP device in the application and input the network session key (NETSKEY), app session key (APPSKEY) from the device.

CONSOLE	Applications Gateways Support
Applications \rightarrow \geqslant dragino_test_application1 \rightarrow Devices \rightarrow $=$ 23232	
Application ID dragino_test_application1 Device ID 23232 Description LT-33222-L-5645	
Activation Method ABP Device EUI <> □ 00 B9 14 BE 07 0A 90 34 E Application EUI <> □ 70 B3 D5 7E F0 00 46 18 E	
Device Address <> == 26 01 1A F1 E Network Session Key <> == # DD 86 97 F6 BD 8E 7F 43 CE 69 44 4F 26 64 16 41 E App Session Key <> == # # 78 48 B2 5C D6 BE 8B 2F 8B C8 47 B8 13 21 FE 14 E	In ABP mode, The device Address, Network Session Key, App Session Key must match between the End Node and LoRaWAN server
Status • 4 minutes ago	

Note: You need to make sure the above three keys match in the device and in TTN V3. You can change them either in TTN V3 or in the Device to make them match. In TTN V3, NETSKEY and APPSKEY can be configured in the setting page, but the Device Addr is generated by TTN V3.

You can also change the Device ADDR in TTN V3 by using the The Things Network CLI.

Step2: Run AT commands to make the LSN50 work in Single frequency and ABP mode. Below are the AT commands:

AT+FDR Reset Parameters to Factory Default, Keys Reserve

AT+NJM=0 Set to ABP mode

AT+ADR=0 Set the Adaptive Data Rate Off

AT+DR=5 Set Data Rate (Set AT+DR=3 for 915 band)

AT+TDC=300000 Set transmit interval to 5 minutes

AT+CHS=868400000 Set transmit frequency to 868.4Mhz

AT+DADDR=26 01 1A F1 Set Device Address to 26 01 1A F1

ATZ Reset MCU

As shown below:

COM19 - PuTTY

```
LSN50 Device
Image Version: v1.3
Frequency Band: AU915
DevEui= A8 40 41 00 01 81 89 98
Please set the parameters or reset Device to apply change
OK
OK
OK
OK
OK
OK
LSN50 Device
Image Version: v1.3
Frequency Band: AU915
DevEui= A8 40 41 00 01 81 89 98
JOINED
***** UpLinkCounter= 0 *****
TX on freq 868400000 Hz at DR 5
txDone
rxTimeOut
rxTimeOut
```

6.7 How to configure the EUI keys in LSN50?

The early version of LSN50 firmware doesn't have pre-configured keys. It is recommended that you update the image to the latest version before configure the keys. Refer <u>upgrade_image</u> to update the firmware to the latest version.

Run AT commands to set the keys to desired keys; refer AT Command manual.

7. Trouble Shooting

7.1 Connection problem when uploading firmware.

Issue: While using USB to TTL to upload firmware via UART interface. It works for several times but most of times it fails.

Checklist:

1. Double check if follow up exactly the steps as manual.

2. Check if hardware works fine: a) check if AT command works, b) check if ISP / flash switch works: PA12 will have different output level while set the ISP/Flash Switch in different position. c) check if reset button works.

3. If you use Windows10 system. Please change the flash loader to run in Windows7 compatibility mode.

常规(快捷方式	兼容性 安全 详细信息	
如果此程序不能在 容性疑难解答。	这个版本的 Windows 上正常]	〔作,请尝试运行兼
运行兼容	性疑难解答	
如何手动选择兼容	性设置?	
兼容模式		-
☑ 以兼容模式运	行这个程序:	
Windows 7		~)
设置		
简化的颜色模	迂	
8 位(256)色		
□用 640 x 480	屏幕分辨率运行	
□禁用全屏优化		
□ 以管理员身份	运行此程序	
更改	高 DPI 设置	
更改所有	用户的设置	

4. We have seen cases where the FT232 USB TTL adapter has a reliability issue with the PC USB chipset (Intel). In this case, even though points 1 and 2 above work, it still has a reliability issue for uploading. If this happens, change to a different PC or change the USB to TTL adapter to solve the issue.

7.2 Why I can't join TTN V3 in US915 / AU915 bands?

It is due to channel mapping. Please see the Eight Channel Mode section above for details.

7.3 AT Command input doesn't work

In the case if user can see the console output but can't type input to the device. Please check if you already include the **ENTER** while sending out the command. Some serial tool doesn't send **ENTER** while press the send key, user need to add ENTER in their string.

8. Order Info

Part Number: LSN50-XX-YY or LSN50-v2-XX-YY-ZZ

XX: The default frequency band

- AS923 : LoRaWAN AS923 band
- AU915 : LoRaWAN AU915 band
- EU433 : LoRaWAN EU433 band
- EU868 : LoRaWAN EU868 band
- KR920 : LoRaWAN KR920 band
- US915 : LoRaWAN US915 band
- IN865 : LoRaWAN IN865 band
- CN470 : LoRaWAN CN470 band

YY: Hole Option

- 12: With M12 waterproof cable hole
- 16: With M16 waterproof cable hole
- 20: With M20 waterproof cable hole (LSN50 v2 doesn't have this version)
- NH: No Hole

ZZ: Battery Option (Only valid for v2 model)

- 4: with 4000mAh battery
- 8: with 8500mAg battery

9. Packing Info

For LSN50:

Package Includes:

LSN50 LoRa Sensor Node x 1

Dimension and weight:

- Device Size: 8 x 6.5 x 5 cm
- Device Weight: 137g
- Package Size / pcs : 9 x 7 x 6cm
- Weight / pcs : 160g

For LSN50 v2:

Package Includes:

- LSN50 v2 LoRa Sensor Node x 1
- External antenna x 1
- Spring Antenna (evaluate purpose)

Dimension and weight:

- Device Size: 9.7 x 6.5 x 4.7 cm
- Device Weight: 150g
- Package Size / pcs : 14.0 x 8x 5 cm
- Weight / pcs : 180g

10. Support

- Support is provided Monday to Friday, from 09:00 to 18:00 GMT+8. Due to different timezones we cannot offer live support. However, your questions will be answered as soon as possible in the before-mentioned schedule.
- Provide as much information as possible regarding your enquiry (product models, accurately describe your
 problem and steps to replicate it etc) and send a mail to <u>support@dragino.com</u>.

11. References

- Product Page
- <u>Data Sheet</u>
- Image Download
- Mechanical Drawing